

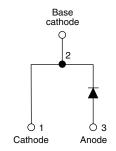
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Vishay Semiconductors

HEXFRED® Ultrafast Soft Recovery Diode, 30 A



TO-247AC 2L



PRIMARY CHARACTERISTICS				
I _{F(AV)}	30 A			
V_{R}	1200 V			
V _F at I _F	2.3 V			
t _{rr} typ.	47 ns			
T _J max.	150 °C			
Package	TO-247AC 2L			
Circuit configuration	Single			

FEATURES

- Ultrafast and ultrasoft recovery
- Very low I_{RRM} and Q_{rr}
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



COMPLIANT HALOGEN

BENEFITS

- Reduced RFI and EMI
- Reduced power loss in diode and switching transistor
- Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

DESCRIPTION

VS-HFA30PB120... is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 1200 V and 30 A continuous current, the VS-HFA30PB120... is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED VS-HFA30PB120... is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V_R		1200	V	
Maximum continuous forward current	I _F	T _C = 100 °C	30		
Single pulse forward current	I _{FSM}	t _p = 10 ms	120	Α	
Maximum repetitive forward current	I _{FRM}		90		
Maximum power dissipation	P _D	T _C = 25 °C	350	W	
Maximum power dissipation		T _C = 100 °C	140	VV	
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C	

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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA		1200	-	-	
		I _F = 30 A		=	2.4	4.1	V
Maximum forward voltage	V_{FM}	I _F = 60 A	See fig. 1	=	3.1	5.7	
		I _F = 30 A, T _J = 125 °C		=	2.3	4.0	
Maximum reverse		V _R = V _R rated	Coo fig. 0	=	1.3	40	
leakage current		$T_J = 125$ °C, $V_R = 0.8 \times V_R$ rated	See fig. 2		1100	4000	μΑ
Junction capacitance	C _T	V _R = 200 V See fig. 3		=	50	75	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		-	nH		

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t _{rr}	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	47	-	
Reverse recovery time See fig. 5, 10	t _{rr1}	T _J = 25 °C		-	110	170	ns
occ lig. 5, 10	t _{rr2}	T _J = 125 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	170	260	
Peak recovery current	I _{RRM1}	T _J = 25 °C		-	10	15	- A - nC
See fig. 6	I _{RRM2}	T _J = 125 °C		-	16	24	
Reverse recovery charge	Q _{rr1}	T _J = 25 °C		-	650	980	
See fig. 7	Q _{rr2}	T _J = 125 °C		-	1540	2310	
Peak rate of fall of recovery current during t _b See fig. 8	dI _{(rec)M} /dt1	T _J = 25 °C		-	270	-	- A/μs
	dI _{(rec)M} /dt2	T _J = 125 °C		-	240	_	Ανμδ

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Thermal resistance, junction to case	R _{thJC}		-	-	0.36	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.50	-	
Weight			-	5.61	-	g
vveigni			-	0.198	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC 2L	HFA30PB120			

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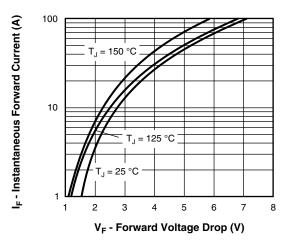


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

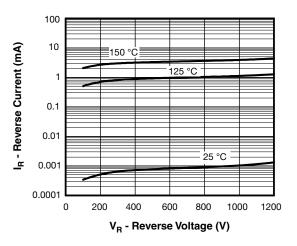


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

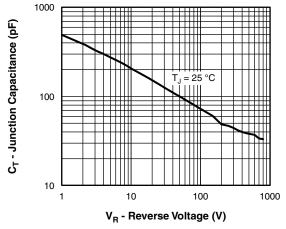


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

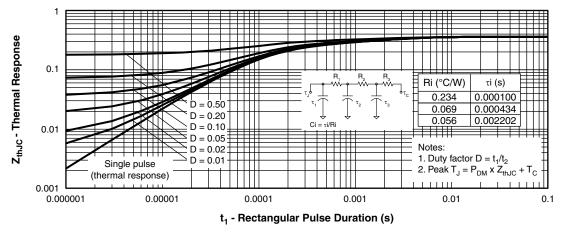


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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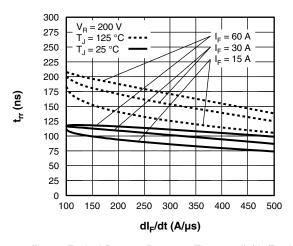


Fig. 5 - Typical Reverse Recovery Time vs. dl_F/dt (Per Leg)

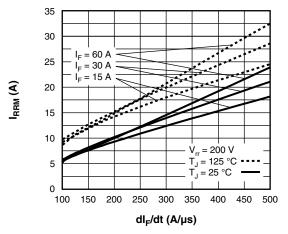


Fig. 6 - Typical Recovery Current vs. dl_F/dt (Per Leg)

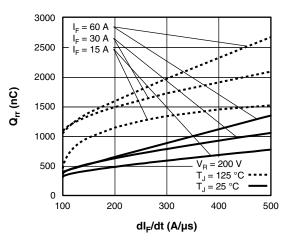


Fig. 7 - Typical Stored Charge vs. dl_F/dt (Per Leg)

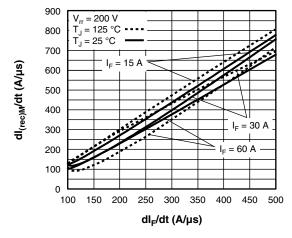
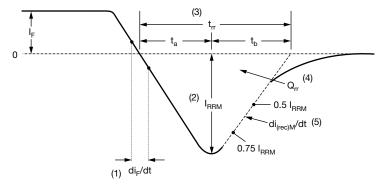


Fig. 8 - Typical $dI_{(rec)M}/dt$ vs. $dI_F/dt(Per\ Leg)$



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

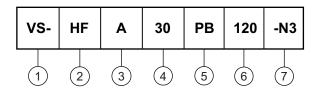
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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - HEXFRED® family

- Electron irradiated

- Current rating (30 = 30 A)

5 - PB = TO-247AC, 2 pins

6 - Voltage rating: (120 = 1200 V)

7 - Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-HFA30PB120-N3	25	500	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?96144</u>			
Part marking information	www.vishay.com/doc?95648		

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