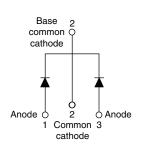


### VS-30CTQ...PbF Series, VS-30CTQ...-N3 Series

Vishay Semiconductors

## Schottky Rectifier, 2 x 15 A

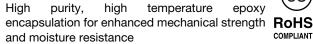




PRODUCT SUMMARY					
Package	TO-220AB				
I <sub>F(AV)</sub>	2 x 15 A				
$V_{R}$	80 V, 100 V				
V <sub>F</sub> at I <sub>F</sub>	0.67 V				
I <sub>RM</sub> max.	7.0 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	7.50 mJ				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High frequency operation





- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION**

The center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS VALUES UNIT						
I <sub>F(AV)</sub>	Rectangular waveform	30	Α				
V <sub>RRM</sub>		80/100	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	Α				
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.67	V				
TJ	Range	- 55 to 175	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS- 30CTQ080PbF	VS- 30CTQ080-N3	VS- 30CTQ100PbF	VS- 30CTQ100-N3	UNITS
Maximum DC reverse voltage	$V_R$	80	80	100	100	V
Maximum working peak reverse voltage	$V_{RWM}$	60	60	100	100	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST COND	ITIONS	VALUES	UNITS	
Maximum average forward current	per device	le.o.o	50 % duty cycle at T <sub>C</sub> = 129 °C, rectangular waveform		30	Α	
See fig. 5	per leg	I <sub>F(AV)</sub>	30 70 duty cycle at 16 = 129 G, 1	15	^		
Maximum peak one cycle surge current per leg	non-repetitive	l	5 μs sine or 3 μs rect. pulse	μs sine or 3 μs rect. pulse Following any rated load condition and with rated		Α	
See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	275	Α	
Non-repetitive avalanche energy per leg $E_{AS}$ $T_J = 25$ °C, $I_{AS} = 0.50$ A, L = 60 mH		Н	7.50	mJ			
Repetitive avalanche current per leg I <sub>AF</sub>		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		0.50	Α	



# VS-30CTQ...PbF Series, VS-30CTQ...-N3 Series

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		15 A	T 05 %C	0.86	V	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> (1)	30 A	T <sub>J</sub> = 25 °C	1.05		
		15 A	T 105 00	0.67		
		30 A	T <sub>J</sub> = 125 °C	0.82		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	0.55	mA	
See fig. 2	IRM (")	T <sub>J</sub> = 125 °C	VR = nateu VR	7.0	IIIA	
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	BOL TEST CONDITIONS		UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C			
Maximum thermal resistance, junction to case per leg	D	DC operation	3.25				
Maximum thermal resistance, junction to case per package	- R <sub>thJC</sub>	DC operation	1.63	°C/W			
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50				
Approximate weight			2	g			
Approximate weight			0.07	OZ.			
Mounting torque minimum			6 (5)	kgf · cm			
Mounting torque — maximum			12 (10)	$(lbf \cdot in)$			
Marking daying		Coop obdo TO 200AB	30CTQ080				
Marking device		Case style TO-220AB	30CTQ100				

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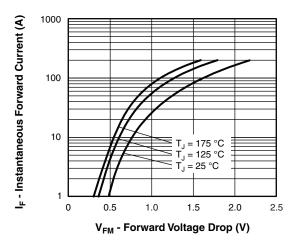


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

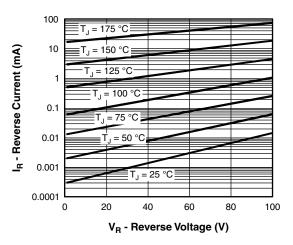


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

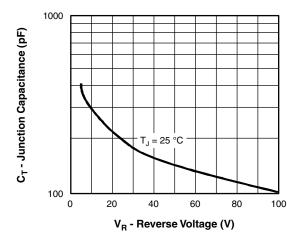


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

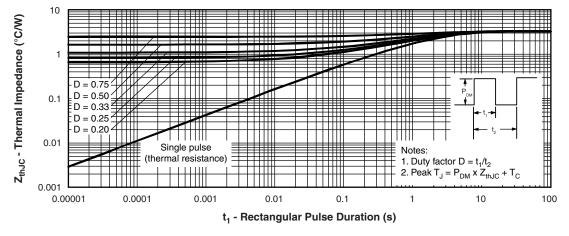


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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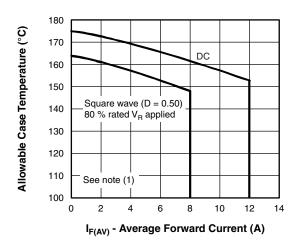


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

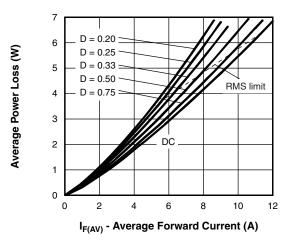


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

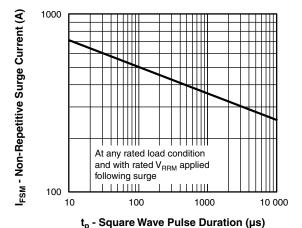


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

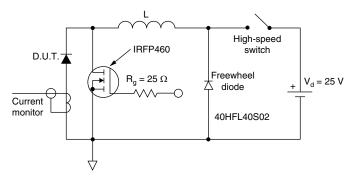


Fig. 8 - Unclamped Inductive Test Circuit

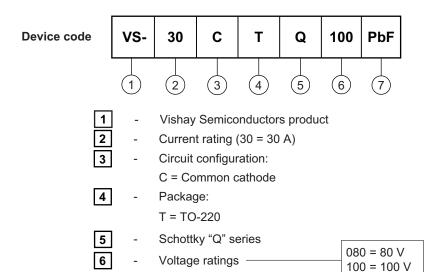
#### Note

 $^{(1)}$  Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$  at (I\_{F(AV)}/D) (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 10 \text{ V}$ 

# VS-30CTQ...PbF Series, VS-30CTQ...-N3 Series

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**



Environmental digit

- PbF = Lead (Pb)-free and RoHS compliant
- -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-30CTQ080PbF	50	1000	Antistatic plastic tube				
VS-30CTQ080-N3	50	1000	Antistatic plastic tube				
VS-30CTQ100-N3	50	1000	Antistatic plastic tube				
VS-30CTQ100-N3	50	1000	Antistatic plastic tube				

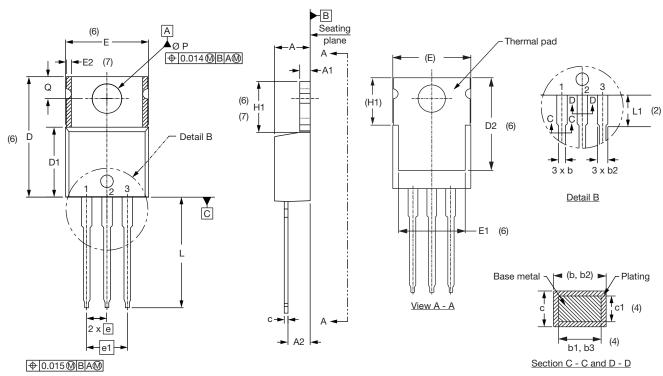
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95222</u>					
Deut erandin erinfamentian	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			



### Vishay Semiconductors

### **TO-220AB**

#### **DIMENSIONS** in millimeters and inches



#### Lead assignments

#### <u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

#### Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip

## **Legal Disclaimer Notice**



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