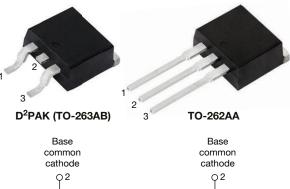
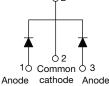
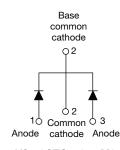


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High Performance Schottky Rectifier, 2 x 20 A







VS-40CTQ150S-M3

VS-40CTQ150-1-M3

PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V _R	150 V				
V _F at I _F	0.71 V				
I _{RM}	15 mA at 125 °C				
T _J max.	175 °C				
E _{AS}	1 mJ				
Package	D ² PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

FEATURES

- Very low forward voltage drop
- 175 °C T_J operation
- Center tap TO-220 package
- High frequency operation



FREE

- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 $^{\circ}\mathrm{C}$
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-40CTQ... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	А		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	1500	А		
V _F	20 A_{pk} , T_J = 125 °C (per leg)	0.71	V		
TJ		-55 to +175	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-40CTQ150S-M3 VS-40CTQ150-1-M3	UNITS
Maximum DC reverse voltage	V _R	150	V
Maximum working peak reverse voltage	V _{RWM}	150	V

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ABSOLUTE MAXIMUM RATINGS	;				
PARAMETER	SYMBOL	TEST COND	TIONS	VALUES	UNITS
Maximum average forward per leg	1	50 % duty cycle at T _C = 140 °	C rootongular wavaform	20	
current, see fig. 5 per device	I _{F(AV)}	50% ulty cycle at $1_{\rm C} = 140\%$	o, rectangular wavelorm	40	
		5 µs sine or 3 µs rect. pulse	Following any rated	1500	А
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	250	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 1.5 \ A, \ L = 0.5 \ A$	9 mH	1.0	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to z Frequency limited by T _J maxi typical		1.5	A

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
		20 A	T.I = 25 °C	0.93	v	
Maximum forward voltage drop per leg	V (1)	40 A	IJ=25 C	1.16		
See fig. 1	V _{FM} ⁽¹⁾	20 A	T 105 %C	0.71		
		40 A	T _J = 125 °C	0.85		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C		50	μA	
See fig. 2		T _J = 125 °C	$V_R = Rated V_R$	15	mA	
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal rang	ge 100 kHz to 1 MHz), 25 °C	450	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SP	ECIFICAT	IONS				
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperatu	ire range	T _J , T _{Stg}		-55 to +175	°C	
Maximum thermal resistance, junction to case per leg		P	DC operation See fig. 4	1.5		
Maximum thermal resistance, junction to case per package		– R _{thJC}	DC operation	0.75	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	ļ	
Approximate weight				2	g	
				0.07	oz.	
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm	
Mounting torque	maximum		Non-hubricated threads	12 (10)	(lbf · in)	
Marking davias	Madda da ta		Case style D ² PAK (TO-263AB)	40CTC	Q150S	
Marking device			Case style TO-262AA	40CTC	150-1	

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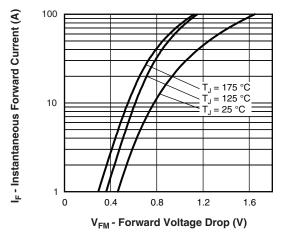


Fig. 1 - Maximum Forward Voltage Drop Characteristics

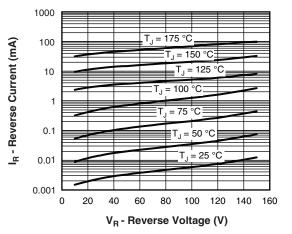


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

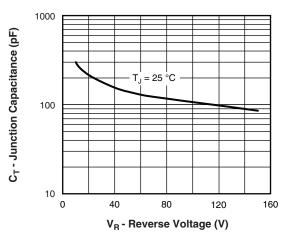


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

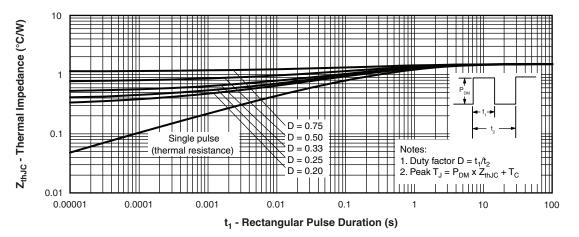
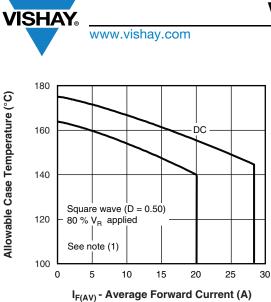


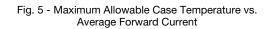
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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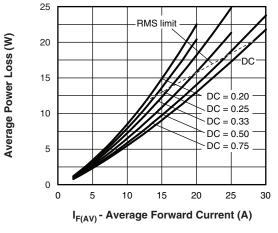


Fig. 6 - Forward Power Loss Characteristics

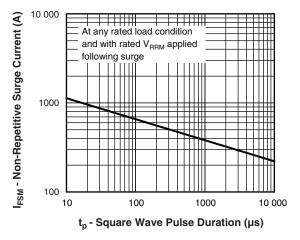


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % V_R applied

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Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	40	С	т	Q	150	S	TRL	-M3
	(1)	(2)	(3)	(4)	(5)	(6)	O	(8)	(9)
	1.	· Visl	nay Sen	niconduo	ctors pro	oduct			
	2	- Cur	rent rati	ng (40 A	۹)				
	3 - Circuit configuration:								
		C = common cathode							
	4	• T=	TO-220)					
	5	- Sch	ottky "C)" series					
	6	- Vol	tage rati	ing (150	= 150 \	/)			
	7.	• S	= D ² PA	K (TO-2	263AB)				
		• -1	= TO-2	62AA					
	8 ·	• N	one = tu	ıbe (50 j	oieces)				
		• TRL = tape and reel (left oriented - for D ² PAK (TO-263AB) only)							
		• T	RR = ta _l	pe and r	eel (righ	nt orient	ed - for	D ² PAK	(TO-26
	9 -	-M3	s = halog	gen-free	, RoHS	-complia	ant, and	d termin	ation le

ORDERING INFORMATION (Exar	ERING INFORMATION (Example)					
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-40CTQ150S-M3	50	Antistatic plastic tubes				
VS-40CTQ150STRL-M3	800	13" diameter plastic tape and reel				
VS-40CTQ150STRR-M3	800	13" diameter plastic tape and reel				
VS-40CTQ150-1-M3	50	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS						
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164				
	TO-262AA	www.vishay.com/doc?96165				
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444				
Part marking information	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?96424				
SPICE model		www.vishay.com/doc?95434				

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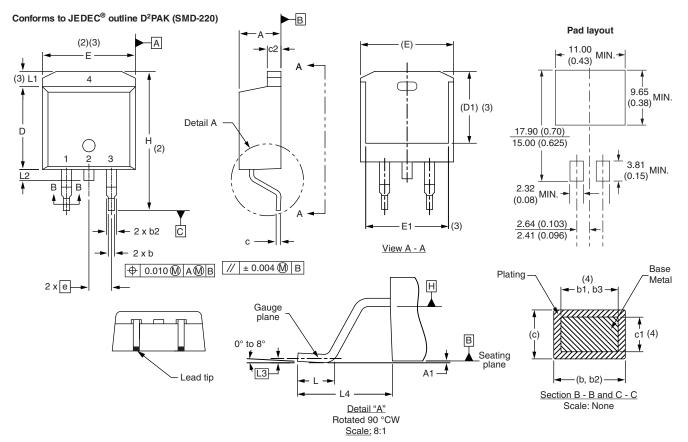


Outline Dimensions

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DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
с	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010) BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing per ASME Y14.5 M-1994 $\,$

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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1

Document Number: 95046

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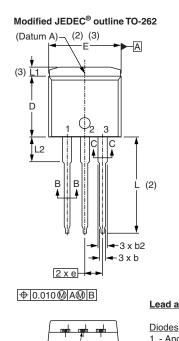


Outline Dimensions

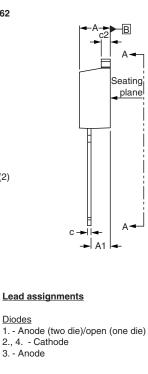
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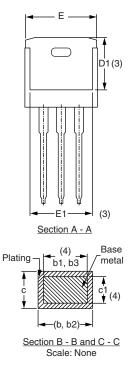
TO-262

DIMENSIONS in millimeters and inches



Lead tip





SYMBOL	MILLIN	IETERS	INC	INCHES			
	MIN.	MAX.	MIN.	MAX.	NOTES		
А	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	BSC	0.100	BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.36	3.71	0.132	0.146			

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

(4) Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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