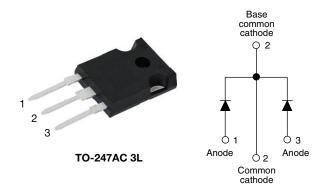


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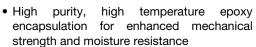
High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V_{R}	60 V				
V _F at I _F	0.62 V				
I _{RM} typ.	100 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	13 mJ				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-MBR4060WT... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	40	Α			
V _{RRM}		60	V			
I _{FSM}	t _p = 5 µs sine	1020	Α			
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.62	V			
T _J	Range	-55 to +150	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-MBR4060WT-N3	UNITS
Maximum DC reverse voltage	V_R	60	V
Maximum working peak reverse voltage	V_{RWM}	60	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average per leg forward current per device			T = 109 °C 50 % duty ovalors	ootongular waxafarm	20	
		I _{F(AV)}	T _C = 108 °C, 50 % duty cycle, rectangular waveform		40	
Maximum peak one cycle	Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	1020	A
non-repetitive surge current per leg		I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	265	
Non-repetitive avalanche energy per leg		mH	13	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.5	Α

Revision: 11-May-2020 1 Document Number: 96467





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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
Maximum forward voltage drep	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.72	V	
Maximum forward voltage drop	VFM (1)	20 A	T _J = 125 °C	0.62	V	
Maximum instantaneous reverse current	I _{RM}	T _J = 25 °C	Rated DC voltage	1.0	mA	
iviaximum instantaneous reverse current		T _J = 125 °C	hated DC voltage	160		
Typical reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	100	mA	
Maximum junction capacitance	C _T	V _R = 5 V _{DC} , (test signal range 100 kHz to 1 MHz) 25 °C		720	pF	
Typical series inductance	L _S	Measured from top of terminal to mounting plane		7.5	nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10			V/µs	

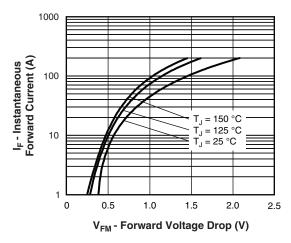
Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storag temperature range	е	T _J , T _{Stg}	, T _{Stg}		°C	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	2.20		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	1.10	°C/W	
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	50		
Approximate weight				6	g	
Approximate weight				0.21	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf·in)	
Marking device			Case style TO-247AC 3L	MBR40	D60WT	



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1000

T_J = 150 °C

T_J = 125 °C

T_J = 75 °C

T_J = 50 °C

T_J = 25 °C

V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

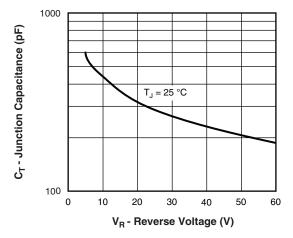


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

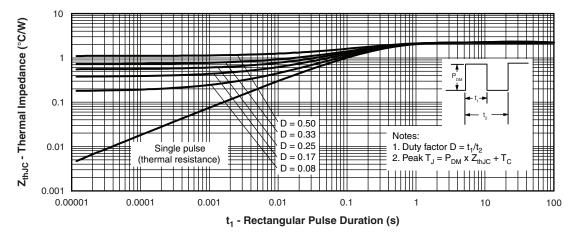


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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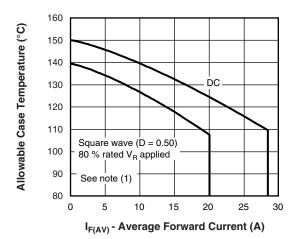


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

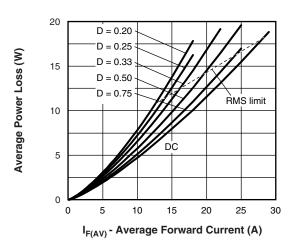


Fig. 6 - Forward Power Loss Characteristics

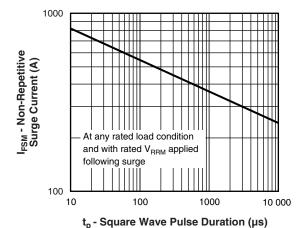


Fig. 7 - Maximum Non-Repetitive Surge Current

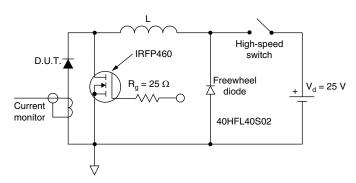


Fig. 8 - Unclamped Inductive Test Circuit

Note

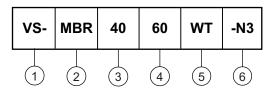
Revision: 11-May-2020 4 Document Number: 96467



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ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Schottky MBR series
- 3 Current rating (40 = 40 A)
- 4 Voltage rating (60 = 60 V)
- 5 Circuit configuration:
 Center tap (dual) TO-247
- 6 Environmental digit
 - -N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-MBR4060WT-N3	25	500	Antistatic plastic tube			

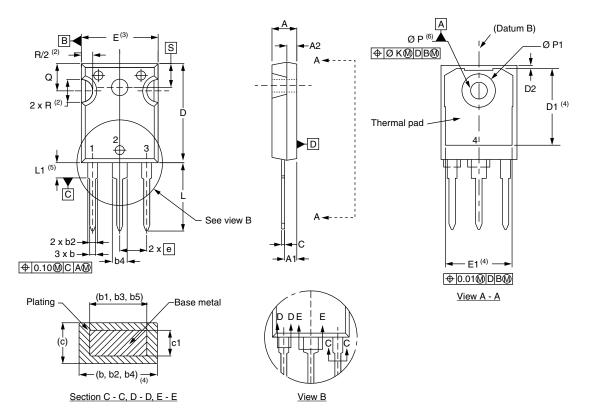
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96138</u>					
Part marking information	www.vishay.com/doc?95007				



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TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	0.254)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	BSC	
	•				•

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q

Revision: 20-Jun-17 1 Document Number: 96138

Legal Disclaimer Notice



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