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Vishay Semiconductors

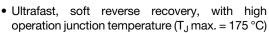
Insulated Ultrafast Rectifier Module, 130 A



PRIMARY CHARACTERISTICS						
V_{R}	600 V					
I _{F(AV)} per module at T _C = 92 °C	130 A					
t _{rr}	37 ns					
Туре	Modules - diode FRED Pt®					
Package	SOT-227					

FEATURES

- Two fully independent diodes
- Fully insulated package





RoHS COMPLIANT

- Low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- · Industry standard outline
- UL approved file E78996



- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-UFB130FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		600	V	
Continuous forward current per diode	I _F	T _C = 85 °C	82	Α	
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	750	A	
Maximum power dissipation per module	P _D	T _C = 85 °C	246	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	



ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	$I_R = 100 \mu A$	600	-	-	
	oltage V _{FM}	I _F = 60 A	-	1.43	1.80	
Forward voltage		I _F = 60 A, T _J = 125 °C	-	1.23	1.48	V
Torward voltage		I _F = 120 A	-	1.66	2.10	
		I _F = 120 A, T _J = 125 °C	-	1.50	1.82	
Reverse leakage current		$V_R = V_R$ rated	-	0.1	50	μA
neverse leakage current	I _{RM}	$T_J = 175 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	0.20	1	mA
Junction capacitance	C _T	V _R = 600 V	-	43	-	pF

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	I _F = 1.0 A, dI _F /dt =		= 200 A/μs, V _R = 30 V	-	37	-	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	79	-	ns
		T _J = 125 °C		-	164	-	
Peak recovery current I _{RRM}	T _J = 25 °C	$I_F = 50 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	6	-	Α	
	T _J = 125 °C		-	15	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	230	-	nC
		T _J = 125 °C		-	1220	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction-to-case, single leg conducting	D		-	-	0.73	
Junction-to-case, both leg conducting	R_{thJC}		-	-	0.365	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.10	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				S	OT-227	



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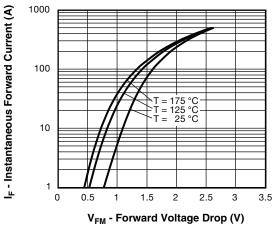


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

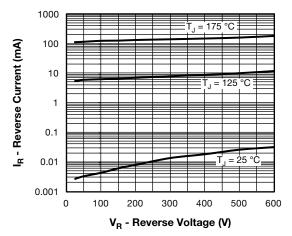


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

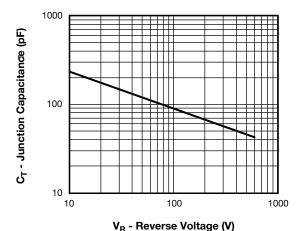


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

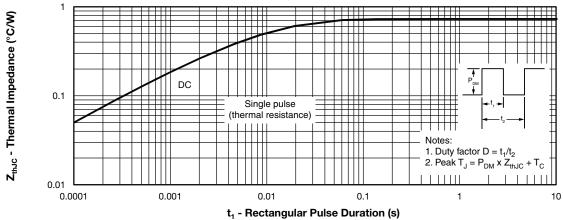


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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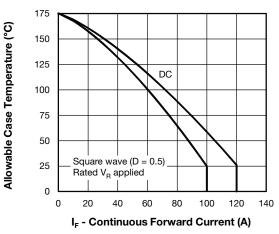


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

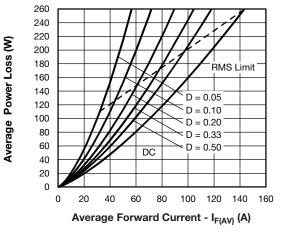


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

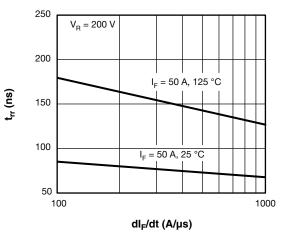


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

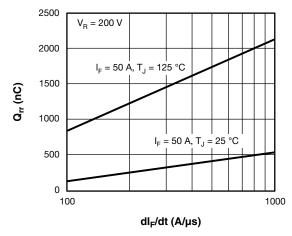


Fig. 8 - Typical Stored Charge vs. dl_F/dt

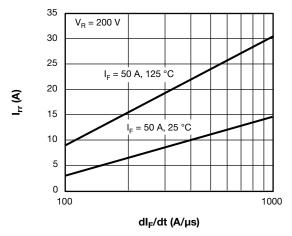


Fig. 9 - Typical Stored Current vs. dl_F/dt

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



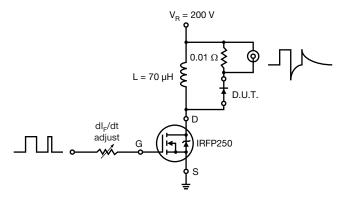
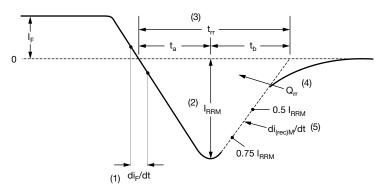


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

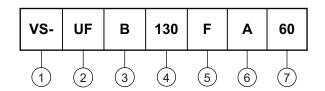
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



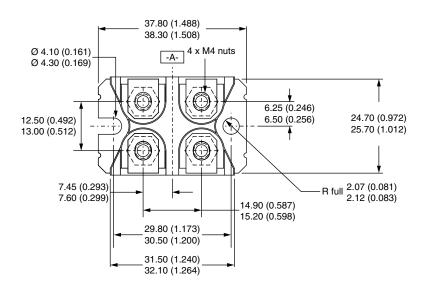
- 1 Vishay Semiconductors product
- 2 Ultrafast rectifier
- Ultrafast Pt diffused
- 4 Current rating (130 = 130 A)
- 5 Circuit configuration (two separate diodes, parallel pin-out)
- 6 Package indicator (SOT-227 standard insulated base)
- 7 Voltage rating (60 = 600 V)

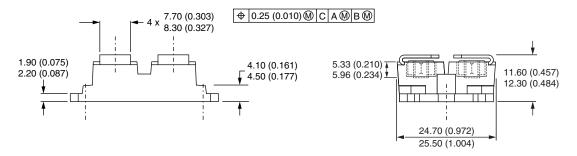
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment 4 0 0 3 4 1 0 0 2 1			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter

Revision: 19-May-2020 1 Document Number: 95423

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