

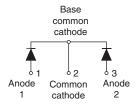
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Vishay Semiconductors

COMPLIANT

### High Performance Schottky Rectifier, New Generation 3, D-61 Package, 2 x 40 A

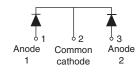




VS-80CNQ...ASMPbF



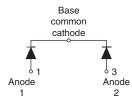
D-61-8-SM



VS-80CNQ...ASLPbF







PRODUCT SUMMARY					
Package	D-61				
I <sub>F(AV)</sub> 2 x 40 A					
V <sub>R</sub>	35 V, 40 V, 45 V				
V <sub>F</sub> at I <sub>F</sub>	0.51 V				
I <sub>RM</sub> max.	250 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	54 mJ				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Center tap module
- · Very low forward voltage drop
- High frequency operation
- · High power discrete
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term
- · New fully transfer-mould low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION**

The center tap Schottky rectifier module series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	80	A		
V <sub>RRM</sub>	Range	Range 35 to 45			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	$t_p = 5 \mu s \text{ sine}$ 5800			
V <sub>F</sub>	40 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.47	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-80CNQ035APbF	VS-80CNQ040APbF	VS-80CNQ045APbF	UNITS
Maximum DC reverse voltage	$V_{R}$	35	40	45	V
Maximum working peak reverse voltage	$V_{RWM}$	35	40	40	٧

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg	1	50 % duty cycle at T <sub>C</sub> = 114 °C, rectangular waveform		40	
See fig. 5	per device	I <sub>F(AV)</sub>			80	Α
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	5800	A
			10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	750	
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 8 A, L = 1.7 mH		54	mJ
Repetitive avalanche current per leg I <sub>AR</sub>		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		8	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		40 A	T <sub>J</sub> = 25 °C	0.52	
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	80 A		0.66	V
See fig. 1	V <sub>FM</sub> (*)	40 A	T <sub>J</sub> = 125 °C	0.47	V
		80 A		0.61	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	5	mA
See fig. 2		T <sub>J</sub> = 125 °C		250	IIIA
Threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> = T <sub>J</sub> maximum		0.26	V
Forward slope resistance	r <sub>t</sub>			3.93	mΩ
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2600	рF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		5.5	nΗ
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs	

#### Note

 $<sup>^{(1)}</sup>$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	•	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C	
Maximum thermal resistance,	per leg	B.,	DC operation (see fig. 4)	0.85		
junction to case	per package	R <sub>thJC</sub>	DC operation	0.42	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	C/ VV	
Approximate weight				7.8	g	
Approximate weight				0.28	OZ.	
Mounting torque —	minimum			40 (35)	kgf · cm	
	maximum			58 (50)	(lbf · in)	
				80CNQ	035A	
Marking device			Case style D-61	80CNQ	80CNQ040A	
					80CNQ045A	
		Case style D-61-8-SM  Case style D-61-8-SL	80CNQ035ASM			
			80CNQ040ASM			
				80CNQ04	80CNQ045ASM	
			Case style D-61-8-SL	80CNQ0	80CNQ035ASL	
				80CNQ0	80CNQ040ASL	
				80CNQ0	45ASL	

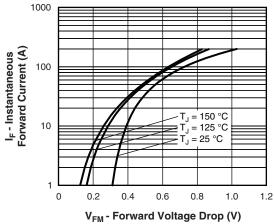


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

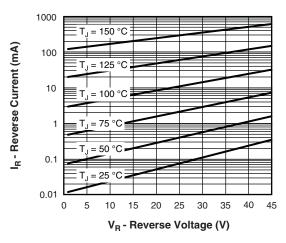


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

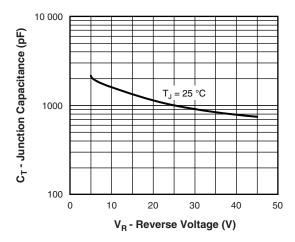


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

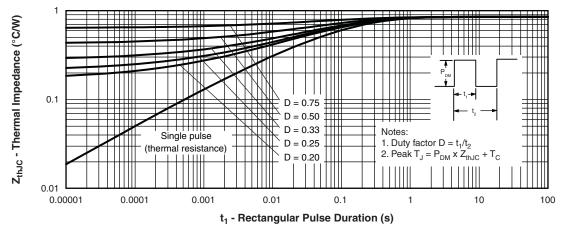


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



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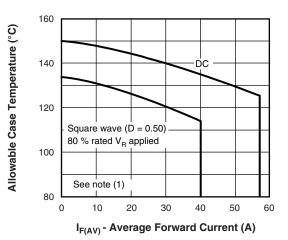


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

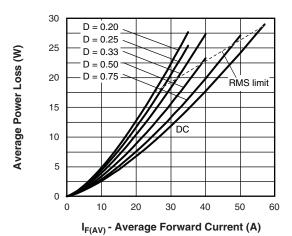


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

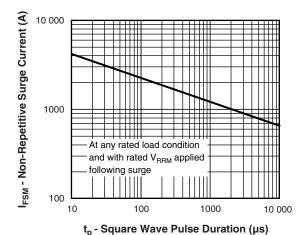


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

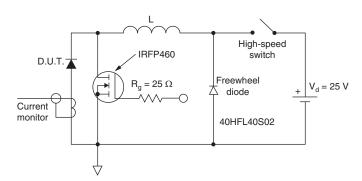


Fig. 8 - Unclamped Inductive Test Circuit

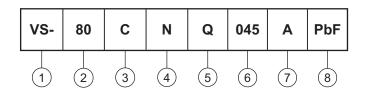
#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (80 A)

3 - Circuit configuration:

C = common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

035 = 35 V

6 - Voltage ratings -

040 = 40 V 045 = 45 V

7 - Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = standard production

• PbF = lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-80CNQ035PBF	10	200	Antistatic plastic tubes			
VS-80CNQ035ASLPBF	20	400	Antistatic plastic tubes			
VS-80CNQ035ASMPBF	20	400	Antistatic plastic tubes			
VS-80CNQ040APBF	10	200	Antistatic plastic tubes			
VS-80CNQ040ASLPBF	20	400	Antistatic plastic tubes			
VS-80CNQ040ASMPBF	20	400	Antistatic plastic tubes			
VS-80CNQ035APBF	10	200	Antistatic plastic tubes			
VS-80CNQ035ASLPBF	20	400	Antistatic plastic tubes			
VS-80CNQ035ASMPBF	20	400	Antistatic plastic tubes			

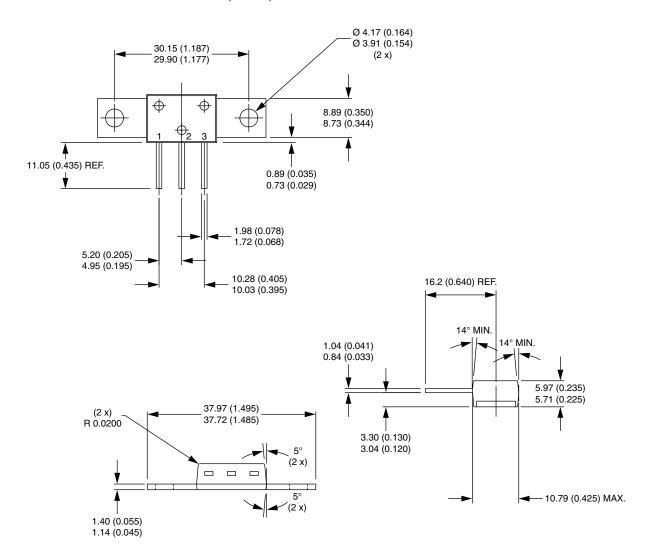
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95354				
Part marking information <u>www.vishay.com/doc?95356</u>				



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# D-61-8, D-61-8-SM, D-61-8-SL

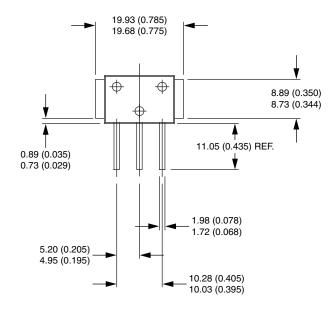
#### **DIMENSIONS - D-61-8** in millimeters (inches)

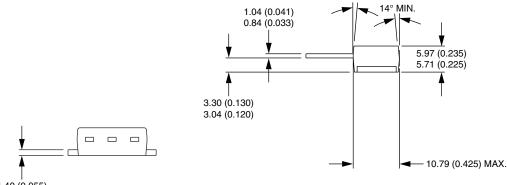




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#### **DIMENSIONS - D-61-8-SM** in millimeters (inches)



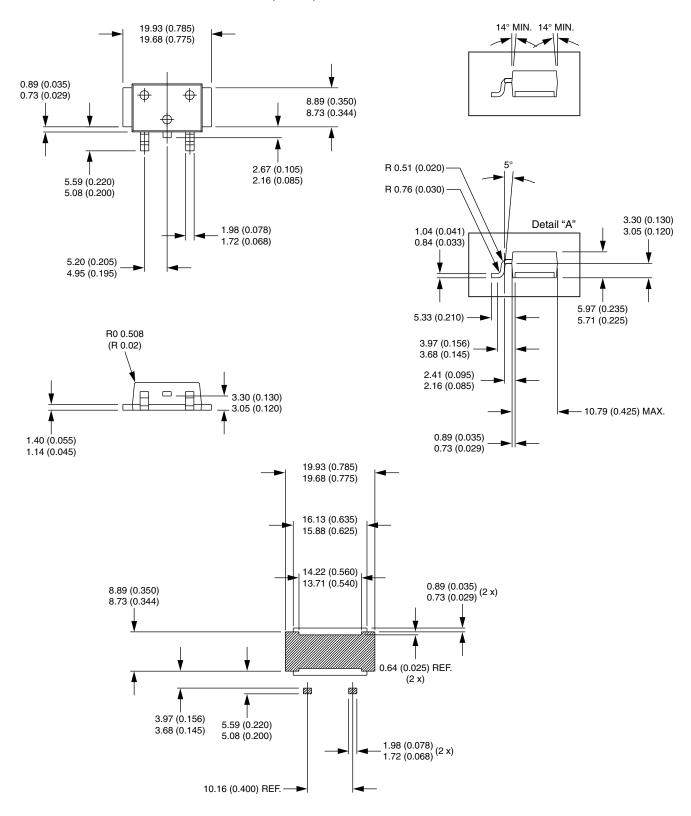


14° MIN.



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#### **DIMENSIONS - D-61-8-SL** in millimeters (inches)



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