Vishay Semiconductors

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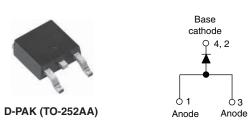
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# High Performance Schottky Rectifier, 3.5 A



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PRODUCT SUMMARY					
Package	D-PAK (TO-252AA)				
I <sub>F(AV)</sub>	3.5 A				
V <sub>R</sub>	40 V				
V <sub>F</sub> at I <sub>F</sub>	See Electrical table				
I <sub>RM</sub>	24 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Single die				
E <sub>AS</sub>	8 mJ				

### FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-30WQ04FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U			
I <sub>F(AV)</sub>	Rectangular waveform	3.5	А		
V <sub>RRM</sub>		40	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	500	А		
V <sub>F</sub>	3 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.49	V		
TJ		-40 to +150	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-30WQ04FNHM3	UNITS			
Maximum DC reverse voltage	V <sub>R</sub>	40	V			
Maximum working peak reverse voltage	V <sub>RWM</sub>	40	v			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS					
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 135 °C, rectangular waveform		3.5				
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	500	А			
surge current. See fig. 7		10 ms sine or 6 ms rect. pulse V <sub>RRM</sub> applied		80				
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 16 mH		8.0	mJ			
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero Frequency limited by $T_J$ maximum	1.0	А				





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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
		3 A	T. = 25 °C	0.53	V	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	6 A	1j=23 0	0.67		
See fig. 1	VFM (*)	3 A	T,I = 125 °C	0.49		
		6 A	1j = 125 C	0.62		
Maximum reverse leakage current	I <sub>BM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B} = Rated V_{\rm B}$	2	mA	
See fig. 2	IRM ("	T <sub>J</sub> = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	24		
Threshold voltage	V <sub>F(TO)</sub>			0.34	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J maximum$		37.33	mΩ	
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range	189	pF		
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.0			nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000			V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

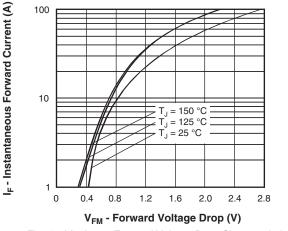
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-40 to +150	°C	
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	4.7	°C/W	
Approvimate weight			0.3	g	
Approximate weight			0.01	oz.	
Marking device		Case style D-PAK	30WQ(	04FNH	

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink



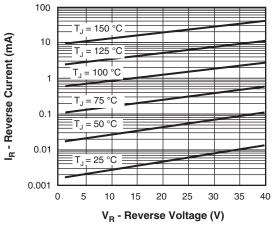
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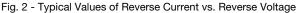


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Fig. 1 - Maximum Forward Voltage Drop Characteristics





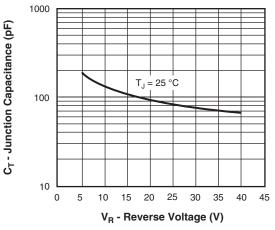


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

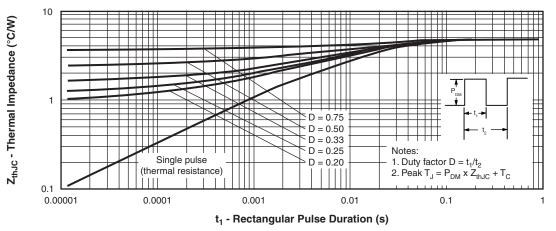
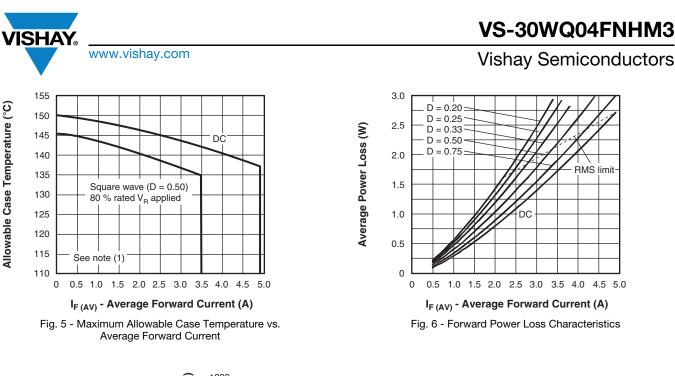
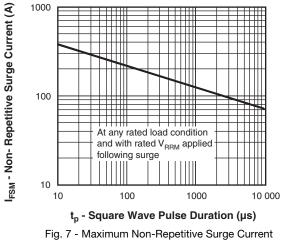


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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#### Note

 $^{(1)} \mbox{ Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6); } \\ Pd_{REV} = \mbox{Inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = 80 \ \% \mbox{ rated } V_R \mbox{ }$ 

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### **ORDERING INFORMATION TABLE**

Device code	VS-	30	w	Q	04	FN	TRL	н	М3
	1	2	3	4	5	6	7	8	9
	1 -	Visl	nay Sem	niconduc	ctors pro	oduct			
	2 -	Cur	rent rati	ng (3.5 /	A)				
	3 -	Pac	kage id	entifier:					
	_	W =	D-PAK						
	4 -	Sch	ottky "C	" series					
	5 -	Volt	age rati	ng (04 =	= 40 V)				
	6 -	FN	= TO-25	52AA (D	-PAK)				
	7 -	• N	one = Ti	ube					
		• TI	R = Tap	e and re	el				
		• TF	RL = Taj	pe and r	eel (left	oriente	ed)		
		• TF	RR = Ta	pe and	reel (rigl	nt orien	ted)		
	8 -	H =	AEC-Q	101 qua	alified				
	9.	- Env	/ironmei	ntal digit	:				
	_	М3	= Halog	jen-free	, RoHS-	complia	ant, and	termina	tions lea

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-30WQ04FN-M3	75	3000	Antistatic plastic tube				
VS-30WQ04FNTRHM3	2000	2000	13" diameter reel				
VS-30WQ04FNTRRHM3	3000	3000	13" diameter reel				
VS-30WQ04FNTRLHM3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95519					
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				
SPICE model	www.vishay.com/doc?95630				



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