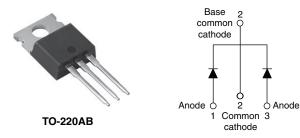


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PRODUCT SUMMARY				
Package	TO-220AB			
I <sub>F(AV)</sub>	2 x 20 A			
V <sub>R</sub>	30 V			
V <sub>F</sub> at I <sub>F</sub>	0.38 V			
I <sub>RM</sub> max.	183 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Common cathode			
E <sub>AS</sub>	13 mJ			

### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long
   term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICSL					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	40	A		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1100	A		
V <sub>F</sub>	20 $A_{pk}$ , $T_J$ = 125 °C (per leg)	0.38	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-42CTQ030PbF	VS-142TQ030-N3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	30	30	V
Maximum working peak reverse voltage	V <sub>RWM</sub>	50	50	v

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS	
Maximum average per leg		50 % duty cycle at $T_{C}$ = 121 °C, rectangular waveform		20		
See fig. 5 per device	IF(AV)			40	Α	
Maximum peak one cycle non-repetitive surge current per leg		5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	1100	~	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	360		
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3 A, L = 2.90 mH		13	mJ	
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	А	

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RoHS

COMPLIANT



Vishay Semiconductors

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		20 A	T <sub>.1</sub> = 25 °C	0.48	
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=23 0	0.57	V
See fig. 1	VFM (")	20 A	T <sub>.1</sub> = 125 °C	0.38	v
		40 A	1j = 125 C	0.51	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V - Poted V	3	mA
See fig. 2	'RM \''	T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	183	ША
Threshold voltage	V <sub>F(TO)</sub>	T T mavimum		0.22	V
Forward slope resistance	r <sub>t</sub>	T <sub>J</sub> = T <sub>J</sub> maximum		6.76	mΩ
Maximum junction capacitance per leg	CT	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	ge 100 kHz to 1 MHz) 25 °C	2840	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	m from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

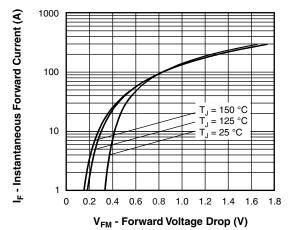
THERMAL - MECHAN	THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	9	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C		
Maximum thermal resistance, junction to case per leg		Р		2.0			
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.0	°C/W		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50			
Approvimate weight				2	g		
Approximate weight				0.07	oz.		
Mounting torque minimum				6 (5)	kgf ⋅cm		
				12 (10)	(lbf ⋅ in)		
Marking device			Case style TO-220AB	42CT	Q030		

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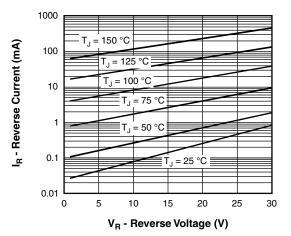


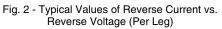
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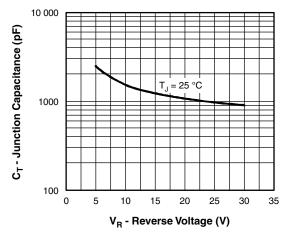
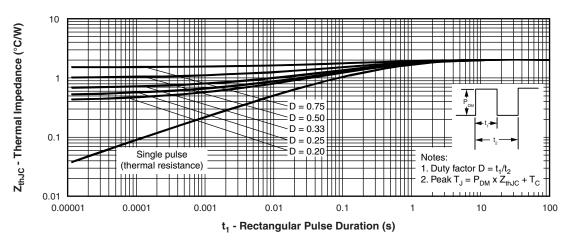


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

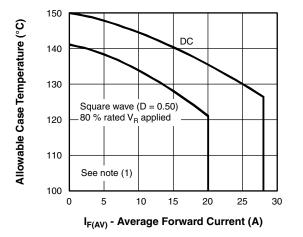


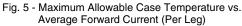


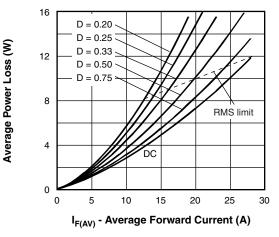
Revision: 29-Aug-11 3 Document Number: 94220 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

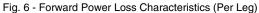


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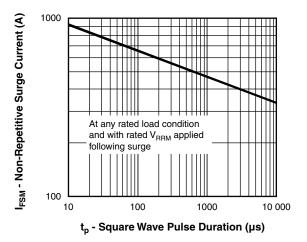


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

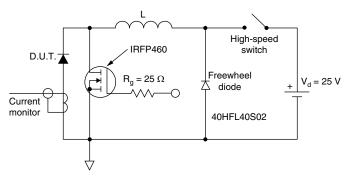


Fig. 8 - Unclamped Inductive Test Circuit

### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ at \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ at \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$ 

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## Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code	VS-	42	С	т	Q	030	PbF
	1	2	3	4	5	6	7
1	-	,		onductor	s produ	ct	
3	-	<ul> <li>Current rating (40 A)</li> <li>Circuit configuration</li> </ul>					
4	-	C = Co Packa		cathode			
		T = TC	)-220				
5	-	Schott	ky "Q" s	eries			
6	-	Voltage	e rating	(030 = 3	30 V)		
7	-		nmental = Lead	digit (Pb)-fre	e and R	RoHS co	ompliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-42CTQ030PbF	50	1000	Antistatic plastic tube			
VS-42CTQ030-N3	50	1000	Antistatic plastic tube			

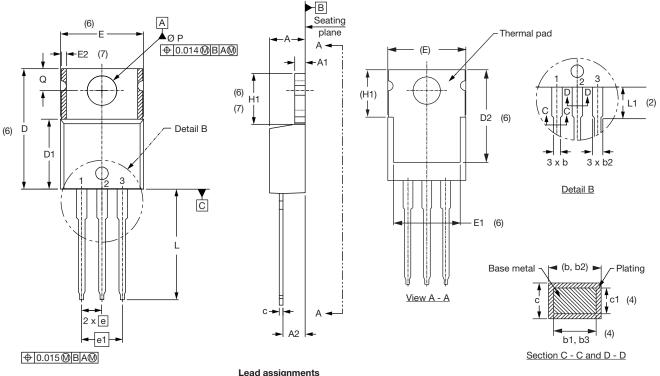
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95222				
Davit manul in a information	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		

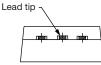


**Vishay Semiconductors** 

**TO-220AB** 

### **DIMENSIONS** in millimeters and inches





.ead	assignments

**Diodes** 

3. - Anode

1. - Anode/open 2. - Cathode

SYMBOL	MILLIMETERS INCHES			NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed  $0.127 \text{ mm} (0.005^{\circ})$  per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left( 4\right) }$  Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	
- H	90° t	0.93	90° t	09	3-

Conforms to JEDEC outline TO-220AB

- $^{(7)}$  Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- Outline conforms to JEDEC TO-220, except A2 (maximum) and (8) D2 (minimum) where dimensions are derived from the actual package outline

Document Number: 95222 Revision: 08-Mar-11

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