

Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.52$ V at $I_F = 5$ A

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses



- ROHS COMPLIANT
- High efficiency operation
- Low thermal resistance
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in high frequency inverters, switching power supplies, freewheeling diodes, OR-ing diode, dc-to-dc converters and reverse battery protection.

MECHANICAL DATA

Case: TO-247AD (TO-3P)

Epoxy meets UL 94V-0 flammability rating

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V60200PG	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	200	V	
Maximum average forward rectified current (Fig. 1)	per device per diode	I _{F(AV)}	60 30	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	300	А	
Operating junction and storage temperature range		T _J , T _{STG}	- 40 to + 150	٥C	

² TO-247AD (TO-3P)

TMBS[®]

PIN 1 O PIN 2 PIN 3 O CASE

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 30 A			
V _{RRM}	200 V			
I _{FSM}	300 A			
V _F at I _F = 30 A	0.73 V			
T _J max.	150 °C			

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V60200PG





ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Breakdown voltage	I _R = 1.0 mA	T _A = 25 °C	V _{BR}	200 (minimum)	-	V	
Instantaneous forward voltage per diode ⁽¹⁾	I _F = 5 A I _F = 15 A I _F = 30 A	T _A = 25 °C	V _F	0.69 0.90 1.28	- - 1.48	v	
	I _F = 5 A I _F = 15 A I _F = 30 A	T _A = 125 °C		0.52 0.63 0.73	- - 0.81		
Reverse current per diode ⁽²⁾	V _R = 180 V	T _A = 25 °C T _A = 125 °C	I _R	3.4 4.6	-	μA mA	
	V _R = 200 V	T _A = 25 °C T _A = 125 °C		- 7.5	200 20	μA mA	

Notes:

(1) Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V60200PG	UNIT	
Typical thermal resistance per diode	$R_{ ext{ heta}JC}$	1.5	°C/W	

ORDERING INFORMATION (Example)						
PREFERRED P/N	REFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE		
V60200PG-E3/45	6.06	45	30/tube	Tube		

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

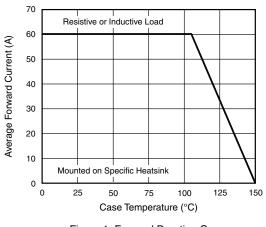


Figure 1. Forward Derating Curve

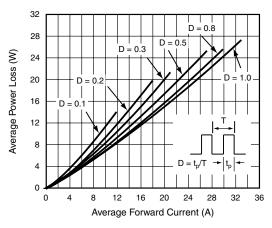


Figure 2. Forward Power Loss Characteristics Per Diode

For technical questions within your region, please contact one of the following: <u>PDD-Americas@vishay.com</u>, <u>PDD-Asia@vishay.com</u>, <u>PDD-Europe@vishay.com</u>



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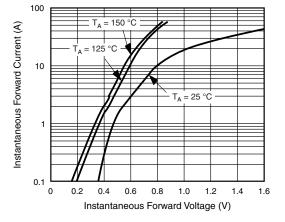


Figure 3. Typical Instantaneous Forward Characteristics Per Diode

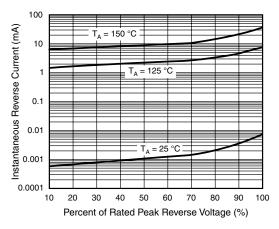


Figure 4. Typical Reverse Characteristics Per Diode

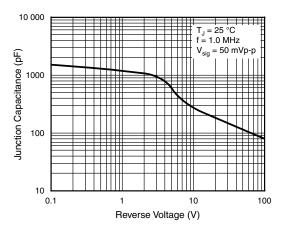
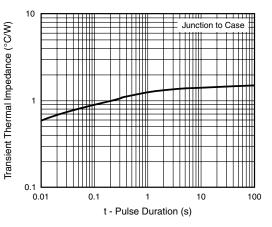
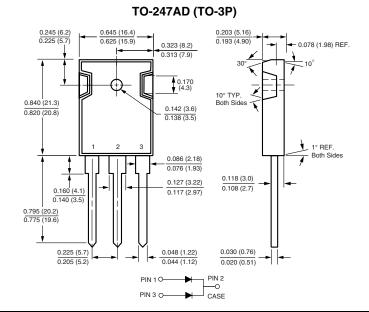


Figure 5. Typical Junction Capacitance Per Diode





PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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