Vishay High Power Products

Schottky Rectifier, 2 x 15 A



- 150 °C T_J operation
- Center tap configuration
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	2 x 15	A				
V _{RRM}		30	V				
V _F	15 Apk, $T_J = 125 \text{ °C}$ (per leg)	0.37	V				
TJ	Range	- 55 to 150	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-STPS30L30CGPbF	UNITS			
Maximum DC reverse voltage	V _R	30	V			
Maximum working peak reverse voltage	V _{RWM}		V			

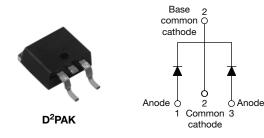
ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average	Maximum average per device				30		
forward current	per leg	'F(AV)	$I_{F(AV)}$ 50 % duty cycle at $I_C = 140$ °C, rectangu		15		
Maximum peak one cycle non-repetitive surge current		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1450	A	
			10 ms sine or 6 ms rect. pulse	V _{RRM} applied	220		
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 7.5 mH		15	mJ	
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	А	



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2 x 15 A

30 V

PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

HALOGEN FREE

RoHS

COMPLIANT

VS-STPS30L30CGPbF

Vishay High Power Products Schottky Rectifier, 2 x 15 A



ELECTRICAL SPECIFICATIONS						
PARAMETER	RAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS		
		15 A	т ос ос	0.46	V	
Maximum forward voltage drep per leg	V _{FM} ⁽¹⁾	30 A	T _J = 25 °C	0.57		
Maximum forward voltage drop per leg		15 A	T 105 %O	0.37		
		30 A	T _J = 125 °C	0.50		
Maximum reverse leakage current per leg	I _{RM}	$T_J = 25 \ ^\circ C$	$V_{\rm B}$ = Rated $V_{\rm B}$	1.50	mA	
Maximum reverse leakage current per leg		T _J = 125 °C	VR = naleu VR	350		
Maximum junction capacitance per leg	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 $^{\circ}\text{C}$		1500	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

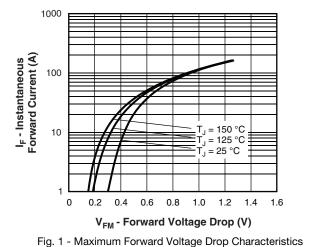
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance,		P	DC energian	1.5	°C/W	
junction to case per leg		R _{thJC}	DC operation	0.8	C/ W	
Approvimate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device			Case style D ² PAK	STPS30	L30CG	



VS-STPS30L30CGPbF

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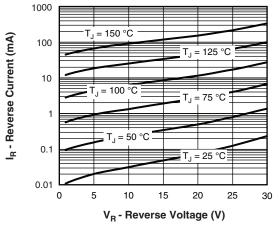


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

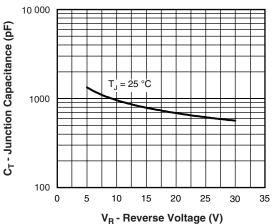


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

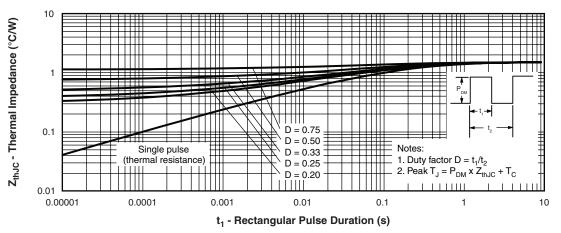
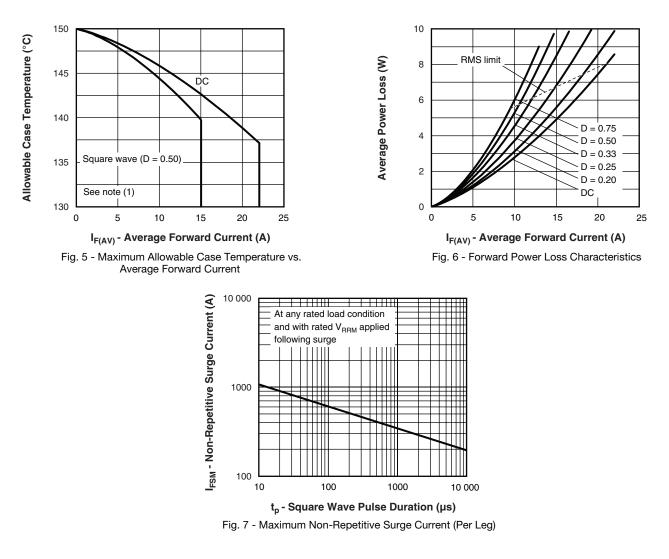


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

VS-STPS30L30CGPbF

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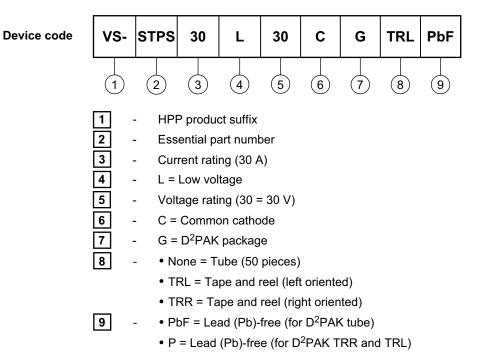
Note

- ⁽¹⁾ Formula used: $T_C = T_J Pd + R_{thJC}$;
 - $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6)



Schottky Rectifier, 2 x 15 A Vishay High Power Products

ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95054				
Packaging information	www.vishay.com/doc?95032				
SPICE model	www.vishay.com/doc?95287				

Outline Dimensions

Vishay Semiconductors

D²PAK



Conforms to JEDEC outline D²PAK (SMD-220) в Pad layout (2)(3)A 11.00 MIN.→ (E) F (0.43)ŧ (3) L1 4 (0.38)^{MIN.} (D1) (3) Detail A D 17.90 (0.70) Н 15.00 (0.625) (2) З 0.01 MIN. Ľ L2 Ĥ ţ В В 2.32 MIN. -(0.08) 2.64 (0.103) 2.41 (0.096) (3)Ċ 2 x b2 С View A - A 2 x h // ± 0.004 M B ⊕ 0.010 M A M B Base Plating (4) Metal 2 x e Н b1, b3 Gauge plane c1 (4) (c) В 0° to 8° ŧ. Seating Lead assignments plane L3 A1 Lead tip (b, b2) Diodes Section B - B and C - C 1. - Anode (two die)/open (one die) Scale: None 2., 4. - Cathode Detail "A" 3. - Anode Rotated 90 °CW

Scale: 8:1

DIMENSIONS in millimeters and inches

SYMBOL	MILLIM	IETERS	INC	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
с	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	MILLIMETERS		INCHES			
STNDUL	MIN.	MAX.	MIN.	MAX.	NOTES		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54 BSC		0.100 BSC				
Н	14.61	15.88	0.575	0.625			
L	1.78	2.79	0.070	0.110			
L1	-	1.65	-	0.066	3		
L2	1.27	1.78	0.050	0.070			
L3	0.25 BSC		0.010	BSC			
L4	4.78	5.28	0.188	0.208			

Notes

 $^{(1)}\,$ Dimensioning and tolerancing per ASME Y14.5 M-1994 $\,$

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC outline TO-263AB

Document Number: 95046
For technical questions within your region, please contact one of the following:

DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesAsia@vishay.com,

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