

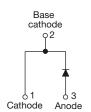
VS-6TQ035-M3, VS-6TQ040-M3, VS-6TQ045-M3

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Vishay Semiconductors

High Performance Schottky Rectifier, 6 A





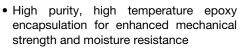
TO-220AC 2L

PRIMARY CHARACTERISTICS I_{F(AV)} 6 A V_R 35 V, 40 V, 45 V

V _R	35 V, 40 V, 45 V
V _F at I _F	0.53 V
I _{RM} max.	7 mA at 125 °C
T _J max.	175 °C
E _{AS}	8 mJ
Package	2L TO-220AC
Circuit configuration	Single

FEATURES

- 175 °C T_J operation
- High frequency operation
- Low forward voltage drop





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-6TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS VALUES U							
I _{F(AV)}	Rectangular waveform	6	Α					
V _{RRM}	Range	35 to 45	V					
I _{FSM}	$t_p = 5 \mu s sine$	690	Α					
V _F	6 A _{pk} , T _J = 125 °C	0.53	V					
T _J	Range	-55 to +175	°C					

VOLTAGE RATINGS								
PARAMETER SYMBOL VS-6TQ035-M3 VS-6TQ040-M3 VS-6TQ045-M3 UNITS								
Maximum DC reverse voltage	V_R	35	40	45	V			
Maximum working peak reverse voltage	V _{RWM}	ან	40	45	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS			
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 164 °	6	Α			
Maximum peak one cycle non-repetitive surge current	o pe em e i e pe i esti pare i i onovini gariy ratea lead		690	Α			
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse		140			
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH		8	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1.20	Α		

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
Maximum forward voltage drop See fig. 1		6 A	T _{.1} = 25 °C	0.60				
	V _{FM} ⁽¹⁾	12 A	1)=25 C	0.73	V			
	V _{FM} ('')	6 A	T 105 °C	0.53				
		12 A	T _J = 125 °C	0.64				
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dated V	0.8	mA			
See fig. 2		T _J = 125 °C	V _R = Rated V _R	7				
Threshold voltage	V _{F(TO)}	T T		0.35	V			
Forward slope resistance	r _t	IJ = IJ maximum	$T_J = T_J$ maximum		mΩ			
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range	400	pF				
Typical series inductance	L _S	Measured lead to lead 5 m	8	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C			
Maximum thermal resistance, junction to case		R_{thJC}	DC operation See fig. 4	2.2	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Mounting toyour	minimum			6 (5)	kgf ⋅ cm			
Mounting torque	maximum			12 (10)	(lbf \cdot in)			
Marking device				6TQ035				
			Case style 2L TO-220AC	6TQ040				
				6TQ045				

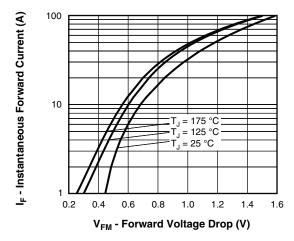
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100

T_J = 175 °C

T_J = 150 °C

T_J = 100 °C

T_J = 100 °C

T_J = 100 °C

T_J = 50 °C

T_J = 25 °C

T_J = 25 °C

V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

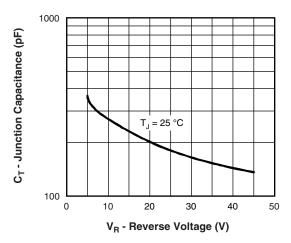


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

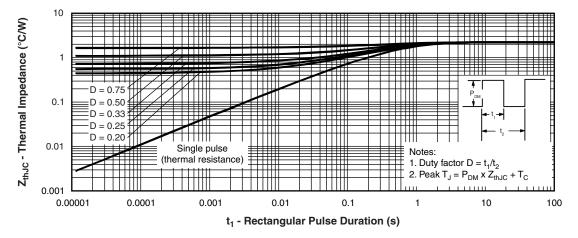


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





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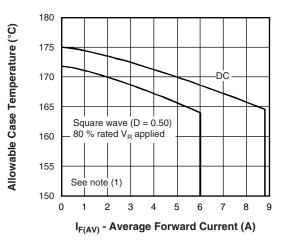


Fig. 5 - Maximum Allowable Case Temperature vs.

Average Forward Current

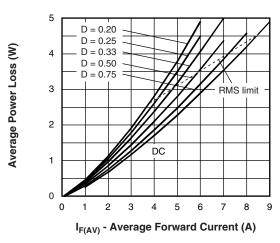


Fig. 6 - Forward Power Loss Characteristics

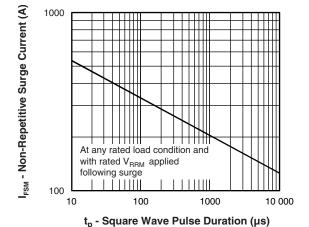


Fig. 7 - Maximum Non-Repetitive Surge Current

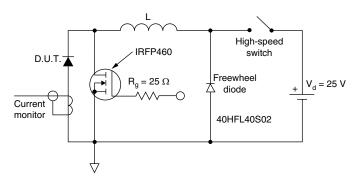


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = & \text{forward power loss} = \text{I}_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (\text{I}_{\text{F(AV)}}/\text{D}) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = & \text{inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; \text{I}_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \% \text{ rated V}_{\text{R}} \\ \end{array}$

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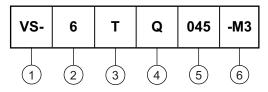


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ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product

- Current rating (6 = 6 A)

3 - Package:

T = TO-220

4 - Schottky "Q" series

035 = 35 V

5 - Voltage ratings

040 = 40 V 045 = 45 V

Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-6TQ035-M3	50	Antistatic plastic tubes						
VS-6TQ040-M3	50	Antistatic plastic tubes						
VS-6TQ045-M3	50	Antistatic plastic tubes						

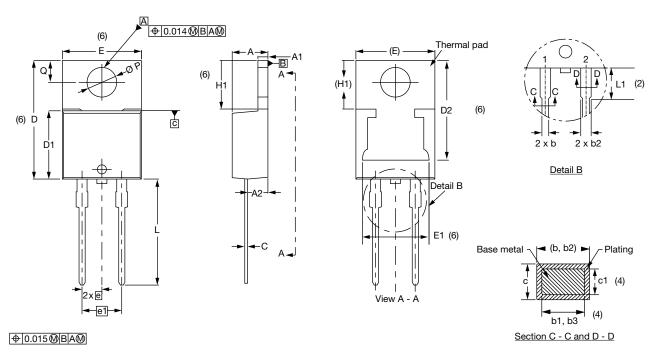
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96156					
Part marking information	www.vishay.com/doc?95391					

Revision: 22-Dec-2021 5 Document Number: 96264 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>

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TO-220AC 2L

DIMENSIONS in millimeters and inches



Lead tip

Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIMETERS		INCHES		HES NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355							•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- $^{(7)}\,$ Outline conforms to JEDEC® TO-220, except D2

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