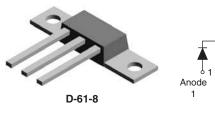
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VS-82CNQ030APbF



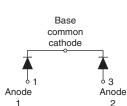
VS-82CNQ030ASMPbF







D-61-8-SI



Base common

cathode

62

62

Common

cathode

Common

cathode

01

Anode

1

γ3

Anode

2

↓ 3 Anode

2

PRODUCT SUMMARY					
Package	D-61				
I _{F(AV)}	2 x 40 A				
V _R	30 V				
V _F at I _F	0.47				
I _{RM} max.	280 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	36 mJ				

FEATURES

- 150 °C T_J operation
- Dual center tap module
- Very low forward voltage drop
- High frequency operation
- High power discrete
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mould low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

Note

* This datasheet provides information about parts that are RoHS-compliant and/or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information/tables in this datasheet for details.

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U				
I _{F(AV)}	Rectangular waveform	80	А			
V _{RRM}		30	V			
I _{FSM}	t _p = 5 μs sine	5100	A			
V _F	40 A_{pk} , T_J = 125 °C (per leg)	40 A_{pk} , $T_J = 125 \ ^{\circ}C$ (per leg) 0.37				
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-82CNQ030APbF	UNITS			
Maximum DC reverse voltage	V _R	30	V			
Maximum working peak reverse voltage	V _{RWM}		v			

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Document Number: 94258

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS			
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 119 °C, rectangular waveform		80		
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	5100	А	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	880		
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 8 A, L = 1.12 mH 36		36	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 8		А		

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS VALUES U			UNITS	
		40 A	T ₁ = 25 °C	0.47	V	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	80 A	1j=25 0	0.55		
See fig. 1		40 A	T ₁ = 125 °C	0.37		
		80 A	1j=125 0	0.47		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	5	mA	
See fig. 2		T _J = 125 °C	VR - naleu VR	280		
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		3700	pF	
Typical series inductance per leg	LS	Measured lead to lead 5 mm from package body 5.5		5.5	nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/			V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C	
Maximum thermal resistance,	per leg	Б	DC operation (see fig. 4)	0.85		
junction to case	per package	R _{thJC}	DC operation	0.42	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils			
Approximate weight				7.8	g	
				0.28	oz.	
Mounting torque	minimum			40 (35)	kgf · cm	
Mounting torque maximum				58 (50)	(lbf · in)	
Marking device			Case style D-61	82CNQ	030A	
			Case style D-61-8-SM	82CNQ030ASM		
			Case style D-61-8-SL	82CNQ0	30ASL	



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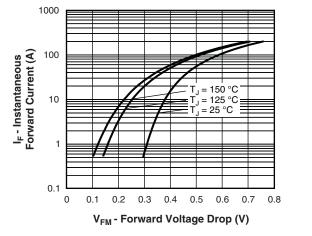
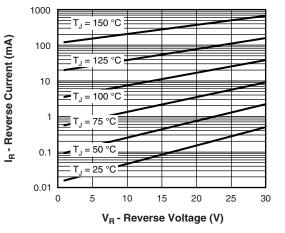
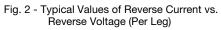


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





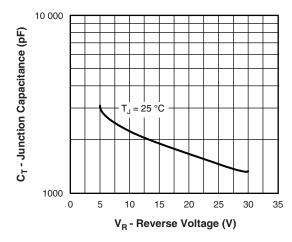
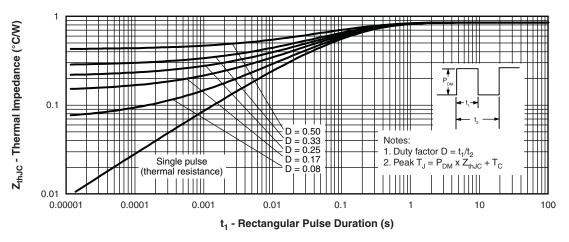


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

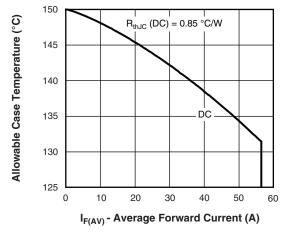


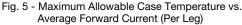


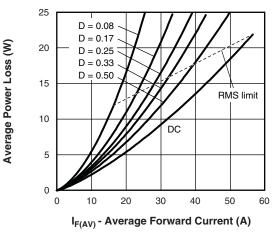
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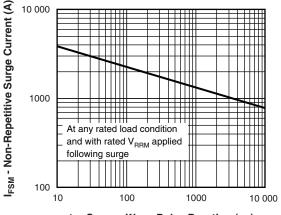
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t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

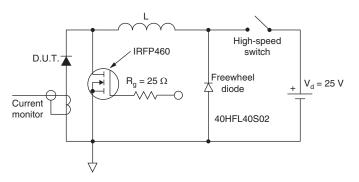


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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www.vishay.com

VISHA

Device code	vs-	82	С	Ν	Q	030	Α	PbF
	1	2	3	4	5	6	(7)	8
	1 - 2 - 3 - 4 - 5 - 6 - 7 -	Curi Circ C = Pac N = Sch Volt Pac	nay Sem rent ratin common kage: D-61 ottky "Q age ratin kage sty = D-61-8	ng (80 A guratior n cathoo " series ngs (030 /le:	.) i: de			
	8 -	• AS • No	SM = D- SL = D-6 one = sta oF = lead	31-8-SL andard p	oroducti	on		

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

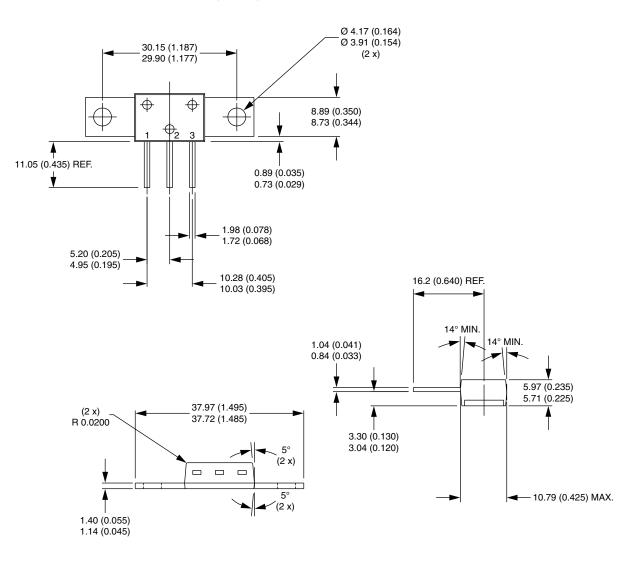
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95354					
Part marking information	www.vishay.com/doc?95356				

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D-61-8, D-61-8-SM, D-61-8-SL

DIMENSIONS - D-61-8 in millimeters (inches)

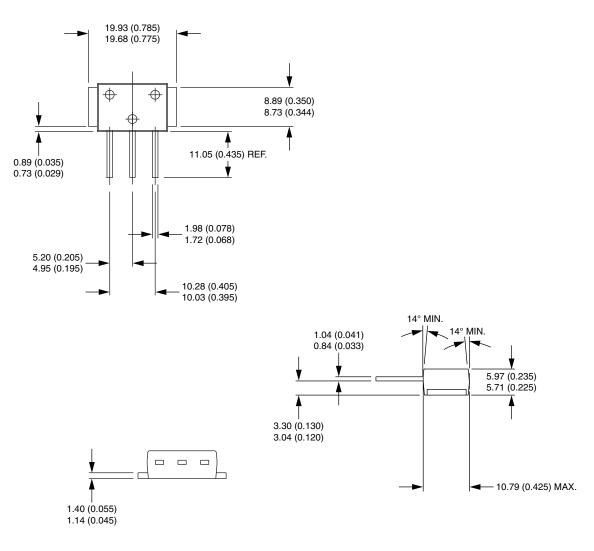


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DIMENSIONS - D-61-8-SM in millimeters (inches)

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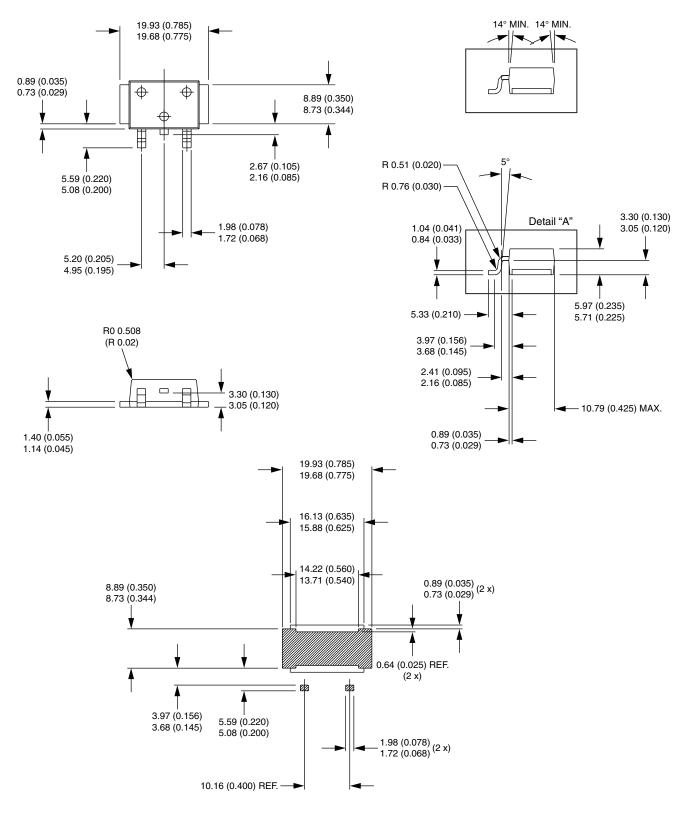
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DIMENSIONS - D-61-8-SL in millimeters (inches)

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