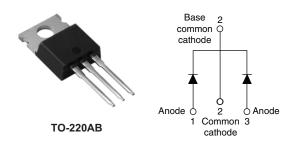


Vishay High Power Products

Ultrafast Rectifier, 10 A FRED Pt®



PRODUCT SUMMARY				
t _{rr}	25 ns			
I _{F(AV)}	2 x 5 A			
V_{R}	200 V			

FEATURES

- · Ultrafast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level



RoHS³

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage		V_{RRM}		200	V	
Average rectified forward current	per leg	1		5		
Average rectified forward current	total device	I _{F(AV)}	Rated V _R , T _C = 149 °C	10		
Non-repetitive peak surge current per leg		I _{FSM}		50	Α	
Peak repetitive forward current per leg		I _{FM}	Rated V_R , square wave, 20 kHz $T_C = 149$ °C	10		
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	200	-	-	
		I _F = 5 A, T _J = 125 °C	-	0.87	0.99	V
Forward voltage V _F	I _F = 10 A, T _J = 125 °C	-	1.02	1.20		
		I _F = 10 A	-	1.12	1.25	
Davaga laakaga ayuwant		$V_R = V_R$ rated	-	-	10	
Reverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	-	250	μΑ
Junction capacitance	C _T	V _R = 200 V	-	8	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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MUR1020CTPbF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
D	I _F = 0.5 A, I _R = 1.0 A, I _{REC} = 0.25 A		-	-	25		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	24	-	ns
		T _J = 125 °C		-	35	-	Í
Peak recovery current	I _{RRM}	T _J = 25 °C	$I_F = 5 \text{ A}$	-	3.3	-	Α
reak recovery current		T _J = 125 °C	$dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	5.0	-	A
Reverse recovery charge Q _{rr}	Q _{rr}	T _J = 25 °C		-	33	-	nC
		T _J = 125 °C		-	76	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	5	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
vveigni			-	0.07	=	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-220AB		MUR1	1020CT	

For technical questions, contact: diodestech@vishay.com

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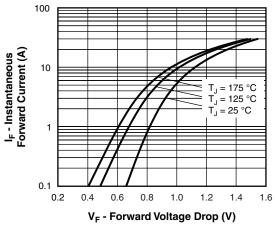


Fig. 1 - Typical Forward Voltage Drop Characteristics

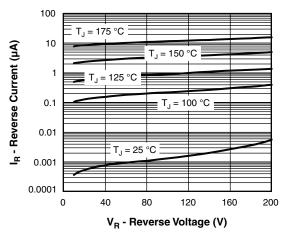


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

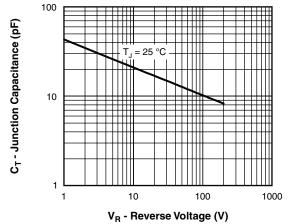


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

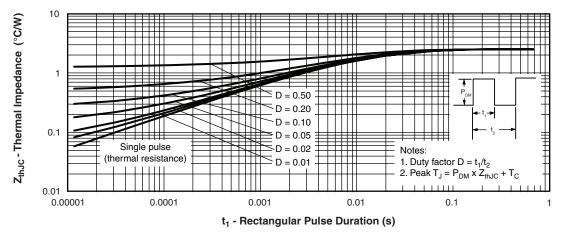


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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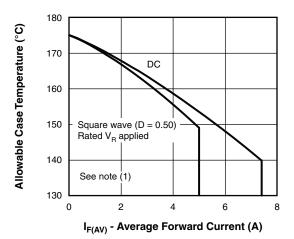


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

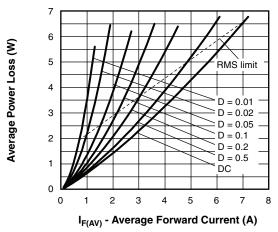


Fig. 6 - Forward Power Loss Characteristics

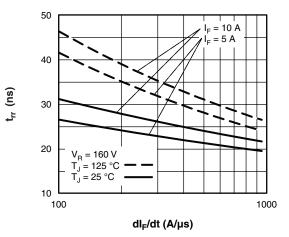


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

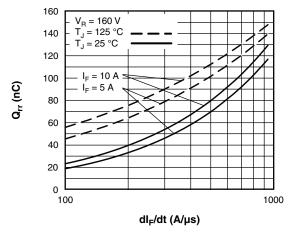


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

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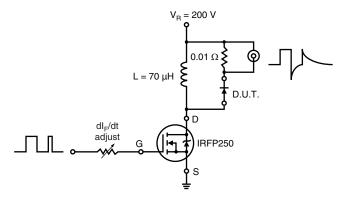
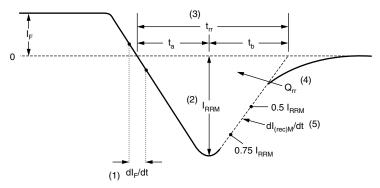


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

MUR1020CTPbF

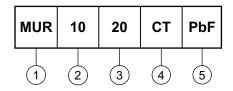
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ORDERING INFORMATION TABLE

Device code



- 1 Ultrafast MUR series
- 2 Current rating (10 = 10 A)
- 3 Voltage rating (20 = 200 V)
- CT = Center tap (dual) TO-220/D²PAK/TO-262
- 5 • None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95222</u>					
Part marking information	www.vishay.com/doc?95225				

Shay.com Document Number: 94076 Revision: 21-Jul-08





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