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Vishay General Semiconductor

Low V_F Surface-Mount Schottky Rectifier



SMA (DO-214AC)

Cathode O Anode

LINKS TO ADDITIONAL RESOURCES

3D Models

PRIMARY CHARACTERISTICS				
I _{F(AV)}	1.5 A			
V _{RRM}	20 V, 30 V			
I _{FSM}	50 A			
V _F	0.34 V			
T _J max.	125 °C			
Package	SMA (DO-214AC)			
Circuit configuration	Single			

FEATURES

- Low profile package
- Ideal for automated placement
- Guardring for overvoltage protection
- Low power losses, high efficiency
- Very low forward voltage drop
- High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMA (DO-214AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3_X - RoHS-compliant and AEC-Q101 qualified ("_X" denotes revision code e.g. A, B,)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SL12	SL13	UNIT		
Device marking code	SL2 SL3					
Maximum repetitive peak reverse voltage	V _{RRM} 20 30		30	V		
Maximum RMS voltage	Itage V _{RMS} 14		21	V		
Maximum DC blocking voltage	V _{DC}	20 30		V		
Maximum average forward rectified current at $T_L = 105 \text{ °C}$ (fig. 1)	I _{F(AV)}	1.5		А		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	50		А		
Voltage rate of change (rated V _R)	dV/dt	10 000		V/µs		
Operating junction temperature range	TJ	-55 to +125		°C		
Storage temperature range	T _{STG}	-55 to	°C			

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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	SL12	SL13	UNIT	
Maximum instantaneous forward voltage at ⁽¹⁾	I _F = 0.1 A	T _A = 125 °C	V _F	0.230		V	
		T _A = 25 °C		0.360			
	I _F = 1.0 A	T _A = 125 °C		0.340 0.445			
		T _A = 25 °C					
Maximum DC reverse current at rated DC blocking voltage ⁽¹⁾		T _A = 25 °C	1	0.2		mA	
		T _A = 100 °C	IR	6	.0	ШA	

Note

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SL12	SL13	UNIT	
Maximum thermal resistance ⁽¹⁾	R _{θJA}	88		°C/W	
	R _{θJL}	28			

Note

 $^{(1)}\,$ PCB mounted on 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pad areas

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SL13-E3/61T	0.064	61T	1800	7" diameter plastic tape and reel		
SL13-E3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel		
SL13HE3_B/H (1)	0.064	Н	1800	7" diameter plastic tape and reel		
SL13HE3_B/I (1)	0.064	Ι	7500	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

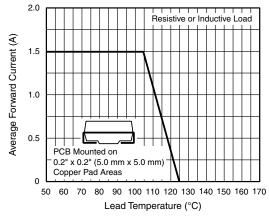


Fig. 1 - Forward Current Derating Curve

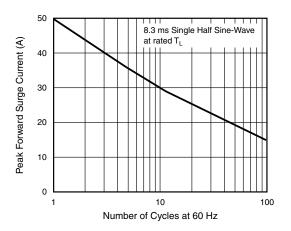
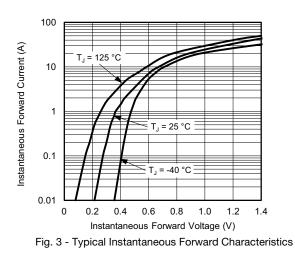


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current



100 T_J = 100 °C T. = 25 °C = -40 °C 0.000001 0 20 40 60 100 80 Percent of Rated Peak Reverse Voltage (%) Fig. 4 - Typical Reverse Characteristics 1000 T, = 25 °C 1.0 MHz Π $V_{sig} = 50 \text{ mV}$ Junction Capacitance (pF) 100

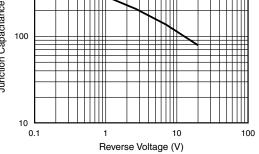


Fig. 5 - Typical Junction Capacitance

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0.074 (1.88)

MAX.

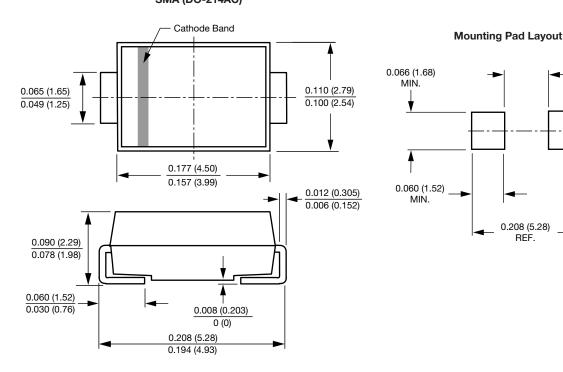
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REF.

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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