

### Vishay General Semiconductor

# **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.52 \text{ V}$  at  $I_F = 5 \text{ A}$ 



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 20 A				
V <sub>RRM</sub>	170 V				
I <sub>FSM</sub>	200 A				
V <sub>F</sub> at I <sub>F</sub> = 20 A	0.68 V				
T <sub>J</sub> max.	175 °C				
Package	TO-220AB				
Circuit configuration	Common cathode				

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

• High efficiency operation

• Solder dip 275 °C max. 10 s, per JESD 22-B106

 Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

# Ph

ROHS COMPLIANT HALOGEN

FREE

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	V40170C	UNIT		
Maximum repetitive peak reverse voltage		$V_{RRM}$	170	V		
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	40	Α		
	per diode		20			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	200	А		
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +175	°C		

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.66	-	V	
	I <sub>F</sub> = 10 A			0.75	-		
	I <sub>F</sub> = 20 A			0.86	1.20		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.52	-		
	I <sub>F</sub> = 10 A			0.59	-		
	I <sub>F</sub> = 20 A			0.68	0.76		
Reverse current per diode	V <sub>R</sub> = 136 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	1.3	-	μA	
		T <sub>A</sub> = 125 °C		2.2	-	mA	
	V <sub>R</sub> = 170 V	T <sub>A</sub> = 25 °C		-	250	μA	
		T <sub>A</sub> = 125 °C		4.2	50	mA	

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 20 ms



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THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V40170C	UNIT	
Typical thermal resistance	per diode	$R_{ heta JC}$	1.2	°C/W	
	per device		0.85	- C/VV	

ORDERING INFORMATION (Example)						
PACKAGE	ACKAGE PREFERRED P/N UNIT WEIGHT (g) PACKAGE CO			BASE QUANTITY	DELIVERY MODE	
TO-220AB	V40170C-M3/4W	1.85	4W	50/tube	Tube	

### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

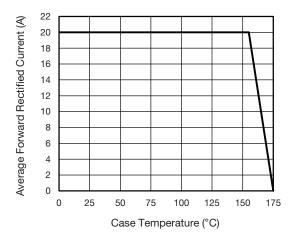


Fig. 1 - Maximum Forward Current Derating Curve

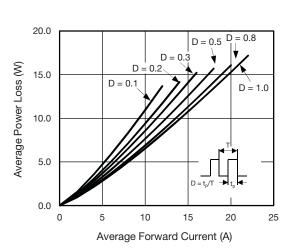


Fig. 2 - Forward Power Loss Characteristics Per Diode

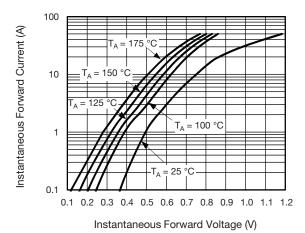
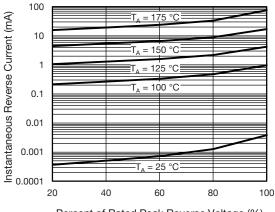


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

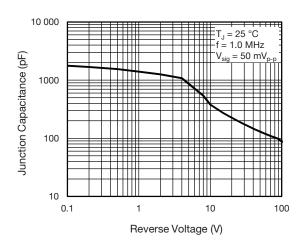


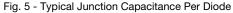
Percent of Rated Peak Reverse Voltage (%)

Fig. 4 - Typical Reverse Characteristics Per Diode



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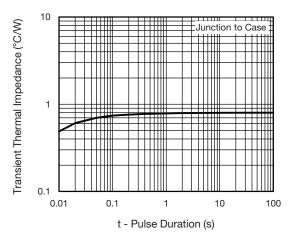


Fig. 6 - Typical Transient Thermal Impedance Per Device

#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

#### **TO-220AB** 0.415 (10.54) 0.380 (9.65) 0.185 (4.70) 0.161 (4.08) 0.175 (4.44) 0.139 (3.53) 0.055 (1.39) 0.113 (2.87) 0.045 (1.14) 0.103 (2.62) 0.603 (15.32) 0.635 (16.13) 0.573 (14.55) 0.625 (15.87) PIN 0.350 (8.89) 2 0.330 (8.38) 0.160 (4.06) 1.148 (29.16) 0.140 (3.56) 1.118 (28.40) 0.110 (2.79) 0.100 (2.54) 0.057 (1.45) 0.045 (1.14) 0.560 (14.22) 0.530 (13.46) 0.035 (0.90) 0.028 (0.70) 0.104 (2.65) 0.022 (0.56) 0.096 (2.45) 0.205 (5.20) 0.014 (0.36) 0.195 (4.95)

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