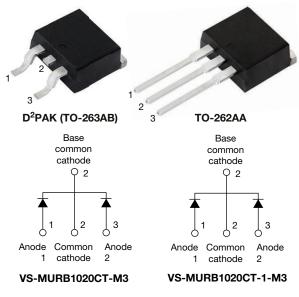


Vishay Semiconductors

Ultrafast Rectifier, 2 x 5 A FRED Pt®



PRIMARY CHARACTERISTICS						
I _{F(AV)}	2 x 5 A					
V _R	200 V					
V _F at I _F	0.87 V					
t _{rr}	25 ns					
T _J max.	175 °C					
Package	D ² PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Peak repetitive reverse voltage		V _{RRM}		200	V		
Average rectified forward current	per leg	I		5			
Average rectilied forward current	total device	IF(AV)	Rated V _R , T _C = 149 °C	10	А		
Non-repetitive peak surge current per leg		I _{FSM}		50	A		
Peak repetitive forward current per	leg	I _{FM}	Rated V_R , square wave, 20 kHz, T_C = 149 °C	10			
Operating junction and storage tem	peratures	T _J , T _{Stg}		-65 to +175	°C		

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
Forward voltage		I _F = 5 A, T _J = 25 °C	-	0.99	1.08		
	V _F	$I_F = 5 \text{ A}, T_J = 125 \text{ °C}$ - 0.		0.87	0.99	V	
r of ward voltage		I _F = 10 A, T _J = 25 °C	-	1.12	1.25	.25	
		I _F = 10 A, T _J = 125 °C	-	1.02	1.20		
Reverse leakage current	I _R	$V_{R} = V_{R}$ rated	-	-	10		
neverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA	
Junction capacitance	CT	V _R = 200 V	-	8	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	_	8.0	-	nH	

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50$	0 A/µs, V _R = 30 V	-	-	35		
	t _{rr}	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ V}$		-	-	25		
		T _J = 25 °C		-	24	-	ns	
		T _J = 125 °C		-	35	-		
Deals receiver a current		T _J = 25 °C	$I_F = 5 A$	-	3.3	-	٨	
Peak recovery current	I _{RRM}	T _J = 125 °C	dI _F /dt = 200 A/µs V _R = 160 V	-	5.0	-	A	
Poweree recevery charge	0	T _J = 25 °C		-	33	-	nC	
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	76	-	10	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	MIN. TYP. MAX.		UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	-	5		
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W	
Thermal resistance, case to heatsink R _{thCS}		Mounting surface, flat, smooth, and greased	-	0.5	-		
Weight			-	2.0	-	g	
weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking davias		Case style D ² PAK (TO-263AB)) MURB1020CT				
Marking device		Case style TO-262		MURB1	020CT-1		

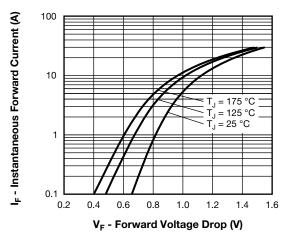


Fig. 1 - Typical Forward Voltage Drop Characteristics

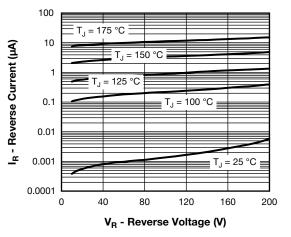


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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2



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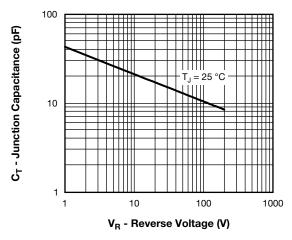


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

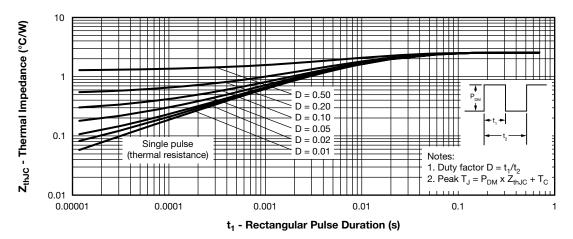
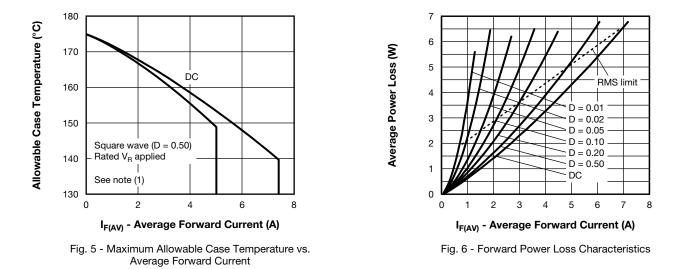


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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3



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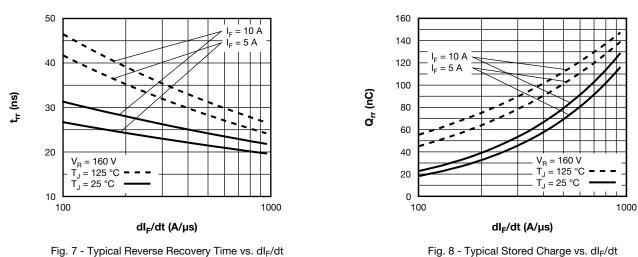


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt



(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

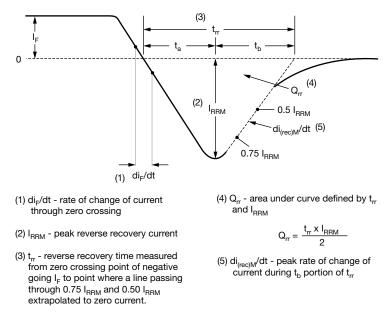


Fig. 9 - Reverse Recovery Waveform and Definitions

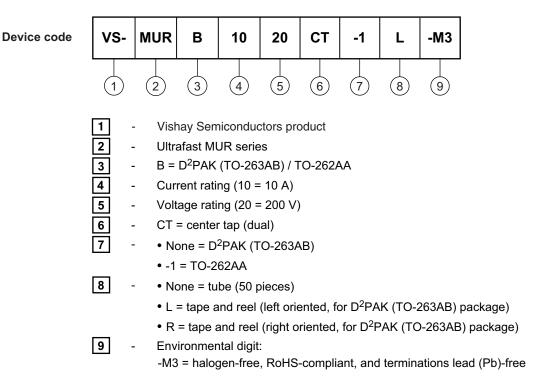
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4



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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-MURB1020CTL-M3	800	13" diameter plastic tape and reel				
VS-MURB1020CT-M3	50	Antistatic plastic tubes				
VS-MURB1020CTR-M3	800	13" diameter plastic tape and reel				
VS-MURB1020CT-1-M3	50	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS						
Dimensions —	D ² PAK (TO-263AB)	www.vishay.com/doc?96164				
	TO-262AA	www.vishay.com/doc?96165				
Dort marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444				
Part marking information —	TO-262AA	www.vishay.com/doc?95443				
Packaging information	D ² PAK (TO-263AB)	www.vishay.com/doc?96424				

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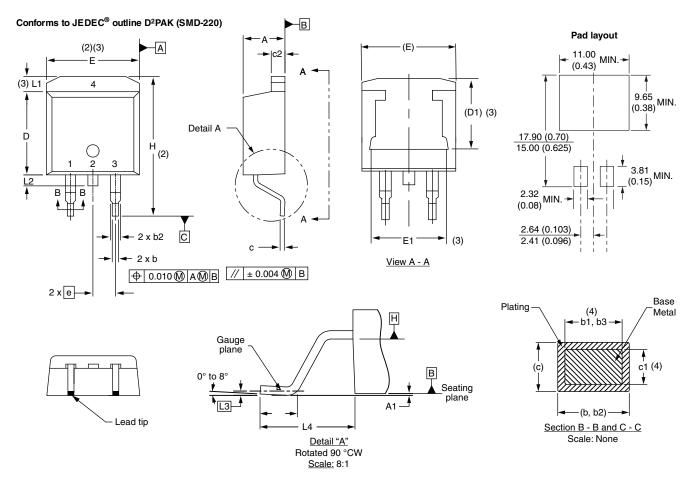


Outline Dimensions

Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL MIL	MILLIM	ETERS	INC	HES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBUL	MIN.	MAX.	MIN.	MAX.	NOTES	STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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Document Number: 96164

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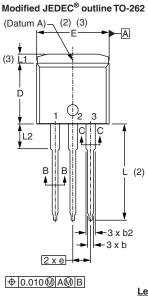


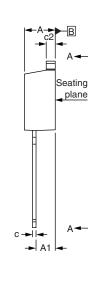
Outline Dimensions

Vishay Semiconductors

TO-262AA

DIMENSIONS in millimeters and inches



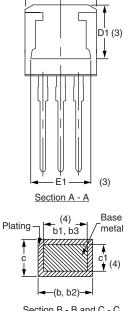


Lead assignments

Lead tip



1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode



F

Section B - B and C - C Scale: None

SYMBOL	MILLIN	IETERS	INC	INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	D 8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100) BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

 (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches Outline conform to JEDEC[®] TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), (6) L1 (max.), L2 (min., max.)

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