Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.42$ V at $I_F = 5$ A

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation
- · Low thermal resistance
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106
- AEC-Q101 gualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER		SYMBOL	V40100G	VI40100G	UNIT		
Maximum repetitive peak reverse voltage		V _{RRM}	100		V		
Maximum average forward rectified current	per device	I _{F(AV)}	40		Δ		
(fig. 1)	per diode		2	0	A		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	200		А		
Voltage rate of change (rated V _R)		dV/dt	10 000		V/µs		
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150		°C		

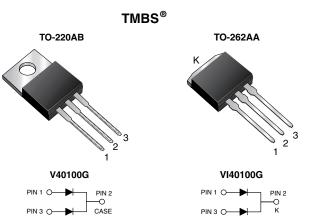




COMPLIANT

HALOGEN

FREE



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PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _{RRM}	100 V			
I _{FSM}	200 A			
V_F at $I_F = 20$ A	0.67 V			
T _J max.	150 °C			
Package	TO-220AB, TO-262AA			
Diode variations	Dual common cathode			

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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	- V _F (1)	0.49	-	- V	
	I _F = 10 A			0.59	-		
	I _F = 20 A			0.75	0.81		
	I _F = 5 A	T _A = 125 °C		0.42	-		
	I _F = 10 A			0.54	-		
	I _F = 20 A			0.67	0.73		
Reverse current per diode	V _B = 70 V	T _A = 25 °C	I _R ⁽²⁾	12	-	μA	
	$v_{\rm R} = 70$ v	T _A = 125 °C		8	-	mA	
	V _B = 100 V	T _A = 25 °C		55	500	μA	
	v _R = 100 v	T _A = 125 °C		21	35	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V40100G	VI40100G	UNIT	
Typical thermal resistance per diode	$R_{\theta JC}$	2	°C/W		

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	V40100G-M3/4W	1.88	4W	50/tube	Tube		
TO-262AA	VI40100G-M3/4W	1.45	4W	50/tube	Tube		
TO-220AB	V40100GHM3/4W (1)	1.88	4W	50/tube	Tube		
TO-262AA	VI40100GHM3/4W (1)	1.45	4W	50/tube	Tube		

Note

(1) AEC-Q101 qualified



RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

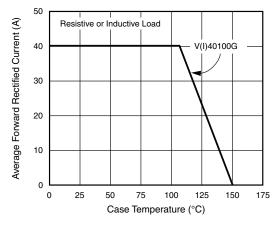


Fig. 1 - Maximum Forward Current Derating Curve

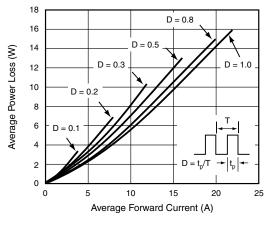


Fig. 2 - Forward Power Loss Characteristics

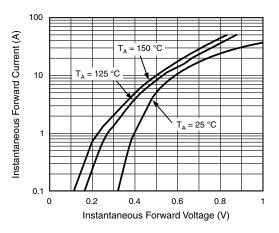


Fig. 3 - Typical Instantaneous Forward Characteristics

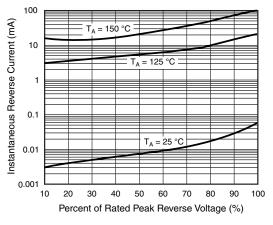


Fig. 4 - Typical Reverse Characteristics

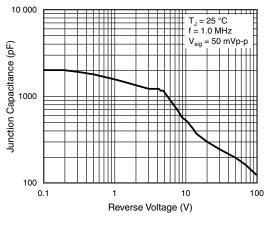


Fig. 5 - Typical Junction Capacitance

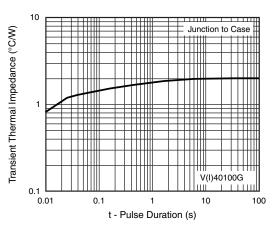


Fig. 6 - Typical Transient Thermal Impedance

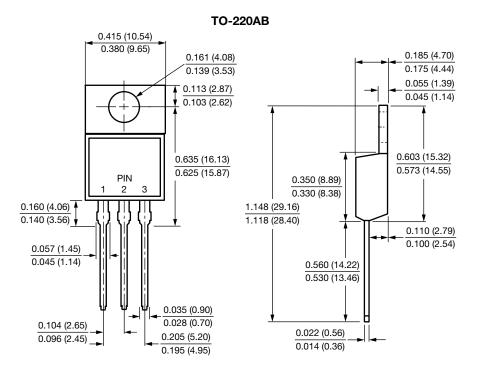
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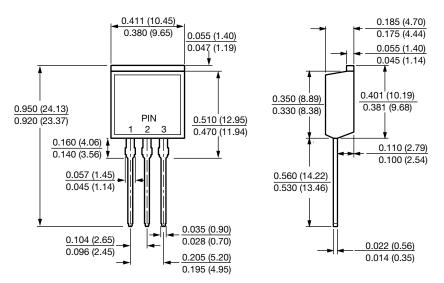
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



TO-262AA



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