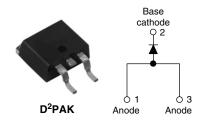


Vishay High Power Products

Input Rectifier Diode, 25 A



PRODUCT SUMMARY				
V _F at 10 A	< 1 V			
I _{FSM}	300 A			
V _{RRM}	800/1200 V			

DESCRIPTION/FEATURES

The 25ETS..S rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 $^{\circ}$ C junction temperature.

Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

This product series has been designed and qualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS			
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	20	23	A			

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	Sinusoidal waveform	25	А					
V _{RRM}		800/1200	V					
I _{FSM}		300	А					
V _F	10 A, T _J = 25 °C	1.0	V					
TJ		- 40 to 150	۵°					

VOLTAGE RATINGS								
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA					
25ETS08S	800	900	1					
25ETS12S	1200	1300	I					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS VALUES UN						
Maximum average forward current	I _{F(AV)}	$T_C = 106 \ ^{\circ}C$, 180° conduction half sine wave	25					
Maximum peak one cycle	1	10 ms sine pulse, rated V _{RRM} applied		А				
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	300					
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	316	A ² s				
Maximum 1-t for fusing		10 ms sine pulse, no voltage reapplied	442	A-5				
Maximum I ² √t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied 4420		A²√s				

25ETS..S High Voltage Series

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum forward voltage drop	V _{FM}	25 A, T _J = 25 °C		1.14	V		
Forward slope resistance	r _t	T 150 %C		9.62	mΩ		
Threshold voltage	V _{F(TO)}	T _J = 150 °C		0.87	V		
	1	$T_J = 25 ^{\circ}C$		0.1	mA		
Maximum reverse leakage current	IRM	T _J = 150 °C	V _R = Rated V _{RRM}	1.0	mA		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 150	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.9		
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
			Cooperatula D ² DAK (CMD 200)	25ETS08S		
Marking device			Case style D ² PAK (SMD-220) 25E		S12S	



25ETS..S High Voltage Series

Input Rectifier Diode, 25 A Vishay High Power Products

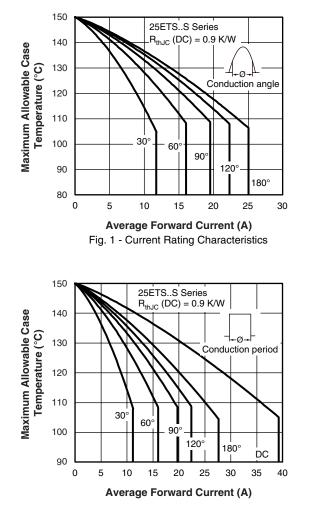


Fig. 2 - Current Rating Characteristics

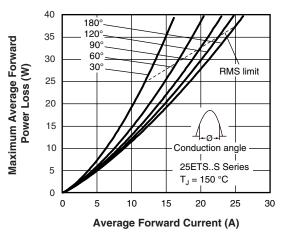


Fig. 3 - Forward Power Loss Characteristics

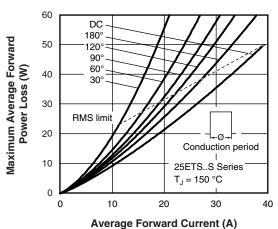
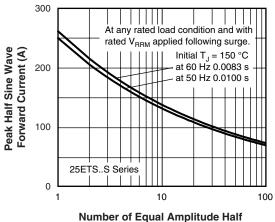
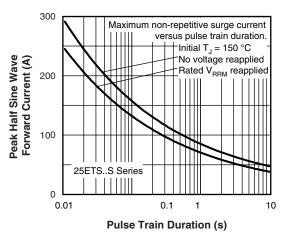


Fig. 4 - Forward Power Loss Characteristics



Cycle Current Pulsed (N) Fig. 5 - Maximum Non-Repetitive Surge Current





25ETS..S High Voltage Series

Vishay High Power Products Input Rectifier Diode, 25 A



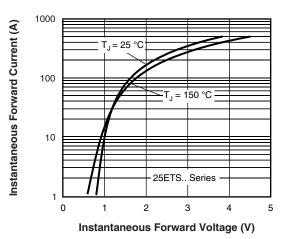


Fig. 7 - Forward Voltage Drop Characteristics

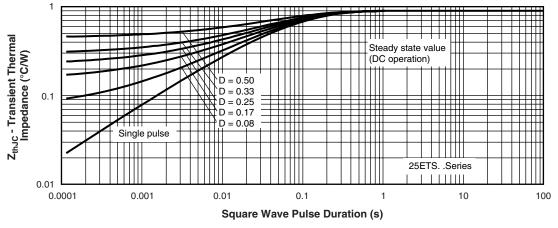


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



Input Rectifier Diode, 25 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code	25	Е	т	S	12	S	TRL	-	
		(2)	(3)	(4)	(5)	(6)	(7)		
	<u>п</u> -	Cur	rent ratir	ng (25 =	25 A)	\bigcirc	\bigcirc	\bigcirc	
	2 -			guration	-				
		E =	Single of	liode					
	3 -	Pac	kage:						
		T =	TO-220	AC					
	4 -	- Type of silicon:							
	_			rd recov	ery rect	ifier	ſ	08 = 80	0 \
	5 -		age ratii	0				12 = 120	
	6 -	S =	S = TO-220 D ² PAK (SMD-220) version						
	7 -	• No	• None = Tube						
		• TF	RL = Tap	be and r	eel (left	oriente	d)		
		• TF	RR = Ta	pe and	reel (rig	ht orier	nted)		
	8 -	• No	one = St	andard	product	ion			
		• Pł	oF = Lea	ad (Pb)-1	free				

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95046				
Part marking information	http://www.vishay.com/doc?95054			
Packaging information http://www.vishay.com/doc?95032				



Vishay

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