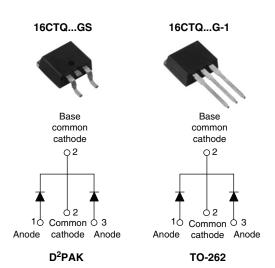


Vishay High Power Products

Schottky Rectifier, 2 x 8 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 8 A			
V _R	60/100 V			

FEATURES

- 175 °C T_J operation
- Center tap configuration
- · Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	16	Α		
V _{RRM}		60/100	V		
I _{FSM}	t _p = 5 μs sine	650	A		
V _F	8 Apk, T _J = 125 °C (per leg)	0.58	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	16CTQ060GS 16CTQ060G-1	16CTQ080GS 16CTQ080G-1	16CTQ100GS 16CTQ100G-1	UNITS
Maximum DC reverse voltage	V_{R}	60	80	100	V
Maximum working peak reverse voltage	V_{RWM}	60	60	100	v

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per forward current	· .	I _{F(AV)} 50 % duty cycle at T _C = 148 °C, rectangular waveform -		8	Α
See fig. 5 per dev				16	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	650	Α
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	210	A
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25$ °C, $I_{AS} = 0.50$ A, L = 60 mH		7.50	mJ		
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \text{ x } V_R$ typical		0.50	Α

16CTQ...GS/16CTQ...G-1

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	8 A	T _J = 25 °C	0.72	V
		16 A		0.88	
		8 A	T _J = 125 °C	0.58	
		16 A		0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.28	mA
See fig. 2	IRM ('')	T _J = 125 °C		7.0	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.415	V
Forward slope resistance	r _t			11.07	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		R_{thJC}	DC operation See fig. 4	3.25	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	0/11
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
wounting torque	maximum			12 (10)	(lbf \cdot in)
				16CTQ	060GS
			Case style D ² PAK	16CTQ080GS	
Marking device			16CTQ	100GS	
			16CTQ060G-1		
			Case style TO-262	16CTQ080G-1	
				16CTQ	100G-1



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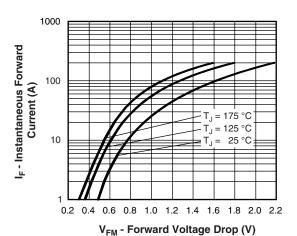


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

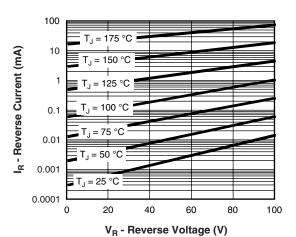


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

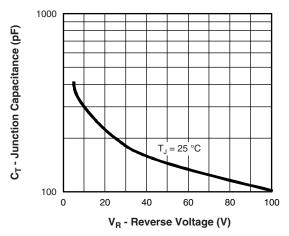


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

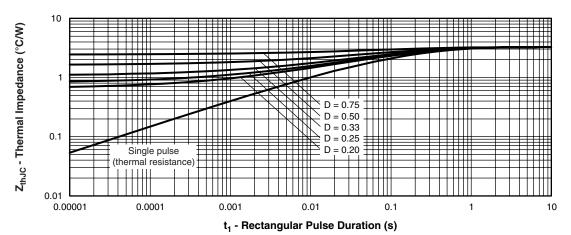


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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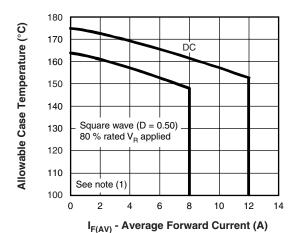


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

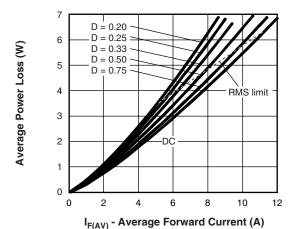


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

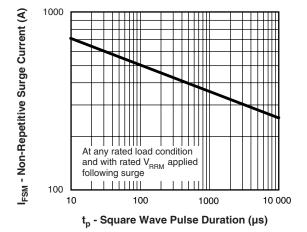


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

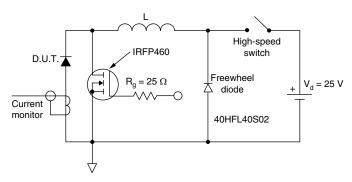


Fig. 8 - Unclamped Inductive Test Circuit

Note

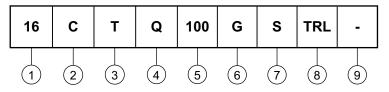
 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 10 \text{ V} \\ \end{array}$



Schottky Rectifier, 2 x 8 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



- 1 Current rating (16 = 16 A)
- C = Common cathode
- $T = TO-220, TO-262, D^2PAK$
- Q = Schottky "Q" series 060 = 60 V

 5 Voltage ratings 080 = 80 V

 6 G = Schottky generation 100 = 100 V
- G = Schottky generation

 None = TO-220
 - -1 = TO-262
 - S = D²PAK
- None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented for D²PAK only)
 - TRR = Tape and reel (right oriented for D²PAK only)
- 9 • None = Standard production
 - PbF = Lead (Pb)-free (for D²PAK tube and TO-262)
 - P = Lead (Pb)-free (for D²PAK TRL and TRR)

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95014			
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			
SPICE model	http://www.vishay.com/doc?95279			



Vishay

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