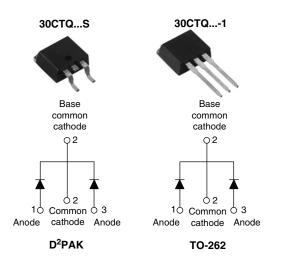


Vishay High Power Products

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 15 A				
V_{R}	80/100 V			

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- · Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for Q101 level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	30	A		
V_{RRM}		80/100	V		
I _{FSM}	t _p = 5 μs sine	850	Α		
V _F	15 Apk, T _J = 125 °C (per leg)	0.67	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER SYMBOL		30CTQ080S 30CTQ080-1	30CTQ100S 30CTQ100-1	UNITS
Maximum DC reverse voltage	V_R	80	100	V
Maximum working peak reverse voltage	V_{RWM}	6 0	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	DL TEST CONDITIONS VALUE		VALUES	UNITS
Maximum average per device forward current		I _{F(AV)} 50 % duty cycle at T _C = 129 °C, rectangular waveform		30	
See fig. 5 per leg				15	_
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	850	- A -
See fig. 7		10 ms sine or 6 ms rect. pulse		275	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 0.50		0.50	А

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30CTQ...S/30CTQ...-1

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	L TEST CONDITIONS VALU		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	0.86	V
		30 A		1.05	
		15 A	T _J = 125 °C	0.67	
		30 A		0.82	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.55	mA
See fig. 2		T _J = 125 °C		7.0	IIIA
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHAN	THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		D	DC operation	3.25	°C/W
Maximum thermal resistance, junction to case per package		R_{thJC}		1.63	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Account to the contrate				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
Mounting torque —	maximum			12 (10)	(lbf ⋅ in)
Marking device			0 1 52544	30CTQ080S	
			Case style D ² PAK	30CTQ100S	
			0 11 70 000	30CTQ080-1	
			Case style TO-262	30CTQ100-1	



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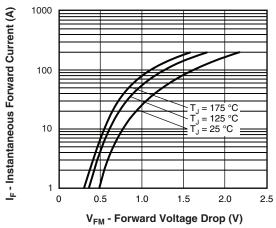


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

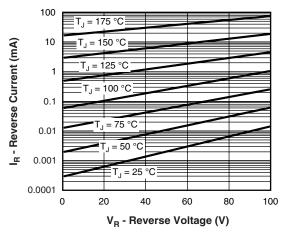


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

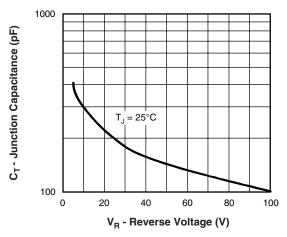


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

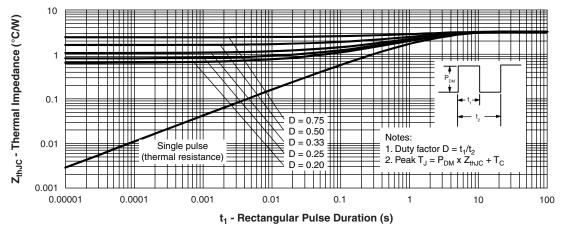


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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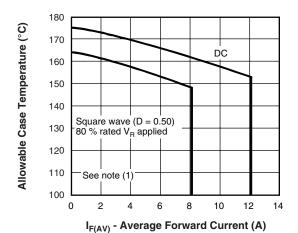


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

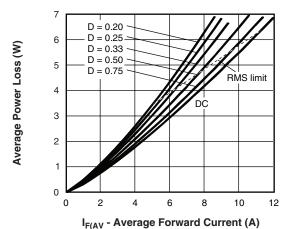


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

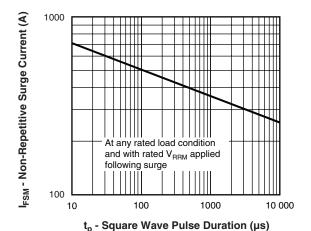


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

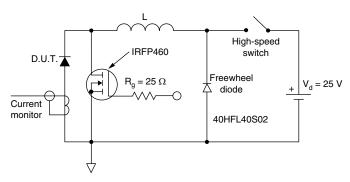


Fig. 8 - Unclamped Inductive Test Circuit

Note

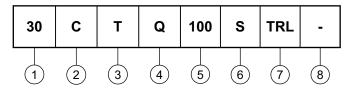
 $\begin{array}{ll} \text{(1)} \;\; \text{Formula used:} \; T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \; x \; R_{\text{thJC}}; \\ \text{Pd} = \text{Forward power loss} = I_{\text{F(AV)}} \; x \; V_{\text{FM}} \; \text{at} \; (I_{\text{F(AV)}}/D) \; (\text{see fig. 6}); \\ \text{Pd}_{\text{REV}} = \text{Inverse power loss} = V_{\text{R1}} \; x \; I_{\text{R}} \; (1 - D); \; I_{\text{R}} \; \text{at} \; V_{\text{R1}} = 10 \; \text{V} \\ \end{array}$



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (30 A)

2 - Circuit configuration:

C = Common cathode

3 - T = TO-220

4 - Schottky "Q" series

5 - Voltage ratings - 080 = 80 V 100 = 100 V

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95014					
Part marking information http://www.vishay.com/doc?95008					
Packaging information	http://www.vishay.com/doc?95032				

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