Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.54$ V at $I_F = 5$ A

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- HALOGEN Solder dip 275 °C max. 10 s, per JESD 22-B106 FREE
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

MECHANICAL DATA

Case: TO-3PW

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V60200PGW	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	200	V	
Maximum average forward rectified curret (fig. 1)	per device	I _{F(AV)}	60	A	
	per diode		30	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	300	А	
Non-repetitive avalanche energy at $T_J = 25$ °C, L = 60 mH per diode		E _{AS}	150	mJ	
Peak repetitive reverse current at $t_p = 2 \ \mu s$, 1 kHz, T _J = 38 °C ± 2 °C per diode		I _{RRM}	0.5	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	



PIN 3 O

PRIMARY CHARACTERISTICS

I_{F(AV)}

V_{RRM}

IFSM

 E_{AS} at L = 60 mH

 V_F at $I_F = 30 A$

T_J max.

Package

Diode variations

CASE

2 x 30 A

200 V

300 A

150 mJ

0.77 V

150 °C

TO-3PW

Dual common cathode









V60200PGW

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V60200PGW

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Breakdown voltage	I _R = 1.0 mA	T _A = 25 °C	V _{BR}	200 (minimum)	-	V	
Instantaneous forward voltage per diode	I _F = 10 A	T _A = 25 °C	- V _F (1)	0.69	-	V	
	I _F = 15 A			0.90	-		
	I _F = 30 A			1.28	1.48		
	I _F = 10 A	T _A = 125 °C		0.54	-		
	I _F = 15 A			0.66	-		
	I _F = 30 A			0.77	0.85		
Reverse current per diode	V _R = 180 V	T _A = 25 °C	I _R ⁽²⁾	3.4	-	μA	
		T _A = 125 °C		4.6	-	mA	
	V _R = 200 V	T _A = 25 °C		-	210	μA	
		T _A = 125 °C		7.5	27	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	V60200PGW	UNIT		
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	1.5	°C/W		
	per device		0.8	0/10		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-3PW	V60200PGW-M3/4W	4.5	4W	30/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

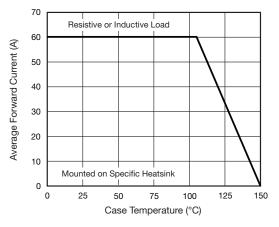


Fig. 1 - Forward Current Derating Curve

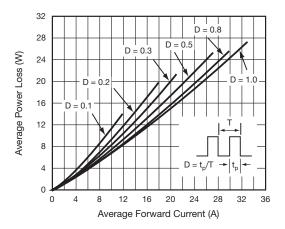


Fig. 2 - Forward Power Loss Characteristics Per Diode

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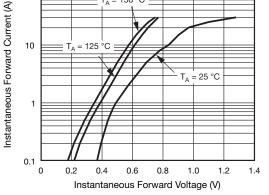


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

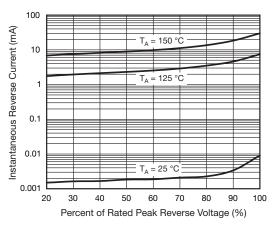


Fig. 4 - Typical Reverse Characteristics Per Diode

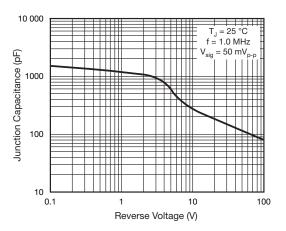


Fig. 5 - Typical Junction Capacitance Per Diode

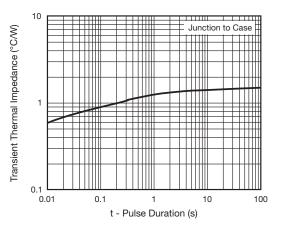
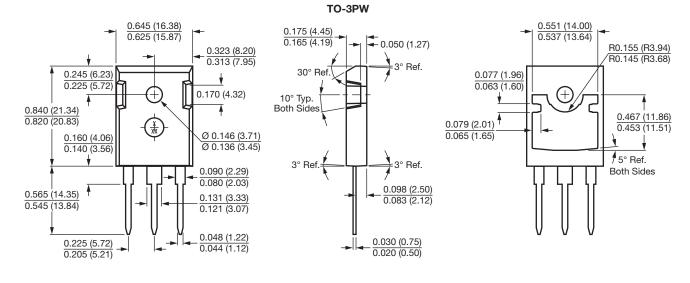


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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