# V15PL50

Vishay General Semiconductor

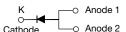
## High Current Density Surface Mount TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.28$  V at  $I_F = 5$  A



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#### **SMPC (TO-277A)**



### LINKS TO ADDITIONAL RESOURCES



SHAY

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	15 A			
V <sub>RRM</sub>	50 V			
I <sub>FSM</sub>	200 A			
$V_F$ at $I_F$ = 15 A ( $T_A$ = 125 °C)	0.40 V			
T <sub>J</sub> max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

### **FEATURES**

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V15PL50	UNIT	
Device marking code		15L5		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	V	
Maximum average forward rectified current (fig. 1)	Ι <sub>F</sub> <sup>(1)</sup>	15	Α	
	I <sub>F</sub> <sup>(2)</sup>	6.0		
Maximum DC reverse voltage	V <sub>DC</sub>	35	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	200	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

Notes

<sup>(1)</sup> Mounted on 30 mm x 30 mm pad areas aluminum PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

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1



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.40	-	V
	I <sub>F</sub> = 7.5 A			0.42	-	
	I <sub>F</sub> = 15 A			0.49	0.57	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.28	-	
	I <sub>F</sub> = 7.5 A			0.31	-	
	I <sub>F</sub> = 15 A			0.40	0.48	
Reverse current	V 25.V	$T_A = 25 \text{ °C}$	I <sub>R</sub> (2)	35	-	μA
	V <sub>R</sub> = 35 V	T <sub>A</sub> = 125 °C		20	-	mA
	$V_{\rm P} = 50 V$	T <sub>A</sub> = 25 °C		-	800	μA
		T <sub>A</sub> = 125 °C		35	80	mA

Notes

SHAY

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}\,$  Pulse test: Pulse width  $\leq 5\mbox{ ms}$ 

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V15PL50	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	70	°C/W	
	R <sub>0JM</sub> <sup>(3)</sup>	4		

Notes

 $^{(1)}\,$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(3)</sup> Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R<sub>0JM</sub> - junction to mount measured at cathode side

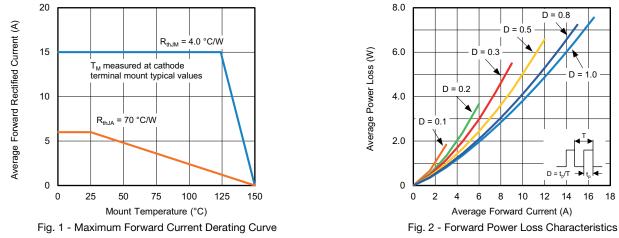
ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V15PL50-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V15PL50-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	

2

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### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)



#### Notes

- <sup>(1)</sup> Mounted on 30 mm x 30 mm aluminum PCB;  $T_M$  measured at the terminal of cathode band ( $R_{\theta JM}$  = 4 °C/W)
- $^{(2)}$  Free air, mounted on recommended copper pad area (R\_{\theta JA} = 70 °C/W)

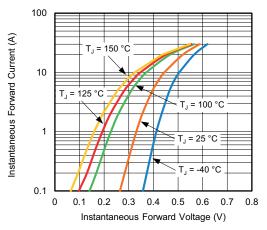


Fig. 3 - Typical Instantaneous Forward Characteristics

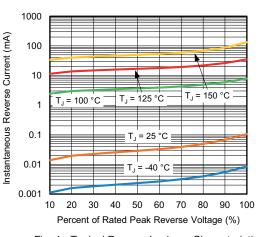


Fig. 4 - Typical Reverse Leakage Characteristics

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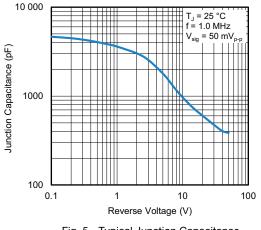
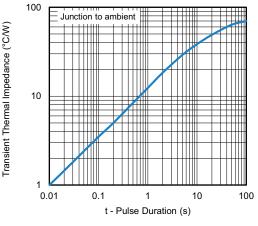


Fig. 5 - Typical Junction Capacitance





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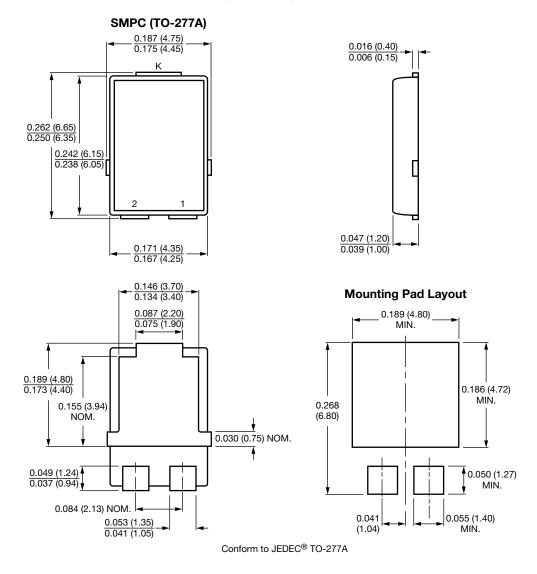
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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4





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