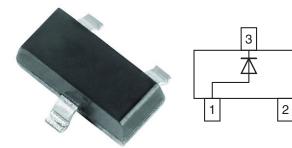
# **MMBD6050**

**Vishay Semiconductors** 



**Small Signal Switching Diode** 



### DESIGN SUPPORT TOOLS click logo to get started

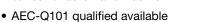


#### MECHANICAL DATA

Case: SOT-23 Weight: approx. 8.8 mg Packaging codes / options: 18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

## FEATURES

- Silicon epitaxial planar diode
- Fast switching diode in case SOT-23, especially suited for automatic insertion



- Base P/N-E3 RoHS-compliant, commercial 
   **RoHS** compliant
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE					
PART	ORDERING CODE CIRCUIT CONFIGURATION TYPE MAR		TYPE MARKING	REMARKS	
MMBD6050	MMBD6050-E3-08 or MMBD6050-E3-18	Single	5AM	Tape and reel	
	MMBD6050-HE3-08 or MMBD6050-HE3-18	Single	SAM		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Continuous reverse voltage		V <sub>R</sub>	70	V	
Forward current		١ <sub>F</sub>	200	mA	
Peak forward surge current		I <sub>FSM</sub>	500	mA	
Maximum power dissipation on FR-5 board <sup>(1)</sup>		P <sub>tot</sub>	225	mW	
Maximum power dissipation of FR-5 board (*)	Derate above 25 °C	P <sub>tot</sub>	1.8	mW/°C	
Maximum power dissipation on alumina		P <sub>tot</sub>	300	mW	
substrate <sup>(2)</sup>	Derate above 25 °C	P <sub>tot</sub>	2.4	mW/°C	

#### Notes

<sup>(1)</sup> FR-5 = 1.0" x 0.75" x 0.062"

(2) Alumina = 0.4" x 0.3" x 0.024" 99.5 % alumina

<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance FR-5		R <sub>thJA</sub>	556	°C/W	
Junction to ambient alumina		R <sub>thJA</sub>	417	°C/W	
Maximum junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	°C	

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# MMBD6050

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ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 100 μΑ	V <sub>(BR)</sub>	70			V
Forward voltage	I <sub>F</sub> = 1 mA	VF	0.55		0.7	V
Forward voltage	I <sub>F</sub> = 100 mA	V <sub>F</sub>	0.85		1.1	V
Reverse leakage current	V <sub>R</sub> = 50 V	I <sub>R</sub>			100	nA
Reverse recovery time	$I_F = I_R = 10 \text{ mA},  i_R = 1 \text{ mA}$	t <sub>rr</sub>			4	ns
Diode capacitance	V <sub>R</sub> = 0	CD			2.5	pF

TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

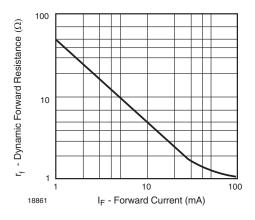


Fig. 1 - Dynamic Forward Resistance vs. Forward Current

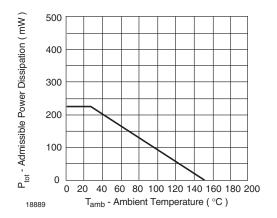


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

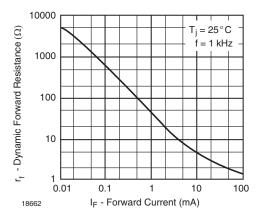


Fig. 3 - Dynamic Forward Resistance vs. Forward Current

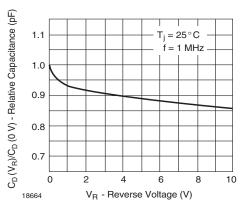


Fig. 4 - Relative Capacitance vs. Reverse Voltage



# **MMBD6050**

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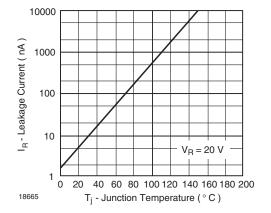


Fig. 5 - Leakage Current vs. Junction Temperature

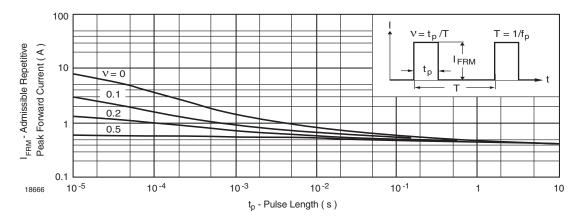


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

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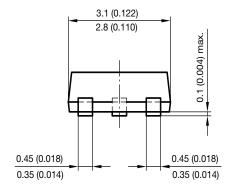
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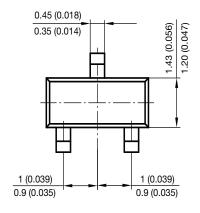
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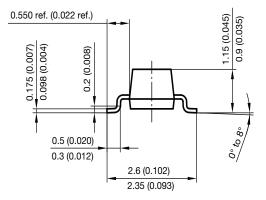


## **Vishay Semiconductors**

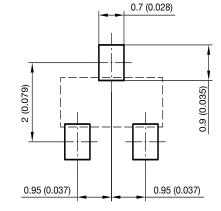
### PACKAGE DIMENSIONS in millimeters (inches): SOT-23







Foot print recommendation:



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