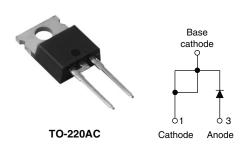


Vishay High Power Products

Ultrafast Rectifier, 15 A FRED PtTM



PRODUCT SUMMARY				
t _{rr}	40 ns			
I _{F(AV)}	15 A			
V_{R}	300 V			

FEATURES

- · Ultrafast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level



RoHS'

DESCRIPTION

300 V series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Repetitive peak reverse voltage	V _{RRM}		300	V		
Average rectified forward current	I _{F(AV)}	T _C = 142 °C	15	Δ.		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	140	А		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C		

PARAMETER	SYMBOL	ATIONS (T _J = 25 °C unless otherwise specifications) TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} ,	Ι _R = 100 μΑ	MIN. 300	-	-	
Forward voltage V _F	I _F = 15 A	-	1.05	1.25	V	
	I _F = 15 A, T _J = 125 °C	-	0.85	1.00		
Barrara la da sa		$V_R = V_R$ rated	-	0.05	40	
Reverse leakage current I _R	I'R	$T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{ rated}$	-	12	400	μΑ
Junction capacitance	C _T	V _R = 300 V	-	45	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

15ETH03PbF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	40	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	32	-	ns
		T _J = 125 °C	$I_F = 15 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	45	-	
Peak recovery current I _{RRM}	I _{RRM}	T _J = 25 °C		-	2.4	-	Α
		T _J = 125 °C		-	6.1	-	_ ^
Reverse recovery charge Q _{rr}	Q _{rr}	T _J = 25 °C		-	38	-	nC
		T _J = 125 °C		-	137	-	I IIC

THERMAL - MECHANICAL SPECIFICATIONS (T _J = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	1.02	2.0	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	
Majaht			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220AC		15E	TH03	•

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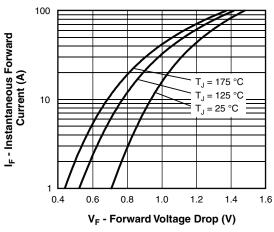


Fig. 1 - Typical Forward Voltage Drop Characteristics

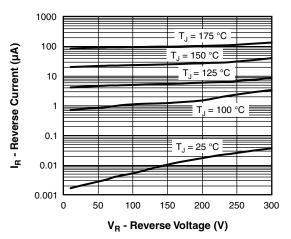


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

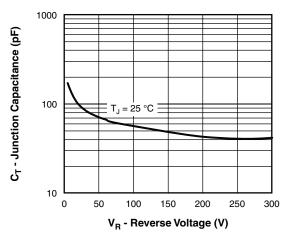


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

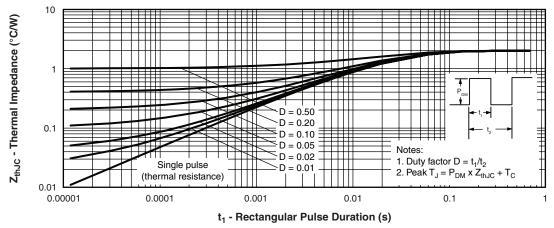


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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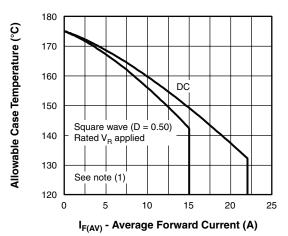


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

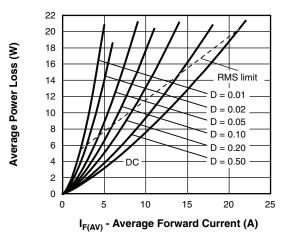


Fig. 6 - Forward Power Loss Characteristics

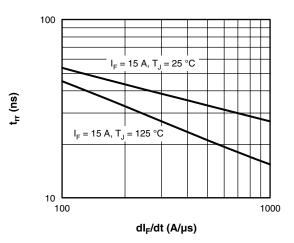


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

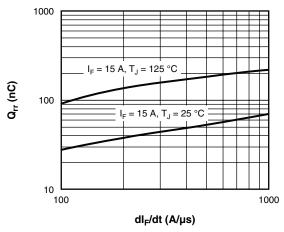


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

 $\begin{array}{ll} \text{(1)} \;\; \text{Formula used:} \; T_C = T_J - (Pd + Pd_{REV}) \; x \; R_{thJC}; \\ \;\; \text{Pd} = \text{Forward power loss} = I_{F(AV)} \; x \; V_{FM} \; \text{at} \; (I_{F(AV)}/D) \; (\text{see fig. 6}); \\ \;\; Pd_{REV} = \text{Inverse power loss} = V_{R1} \; x \; I_R \; (1 - D); \; I_R \; \text{at} \; V_{R1} = \text{Rated} \; V_R \\ \end{array}$



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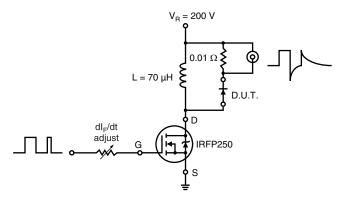
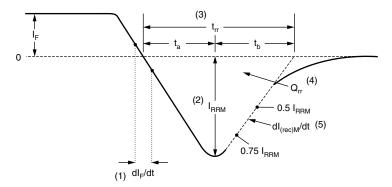


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) $\rm I_{RRM}$ peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

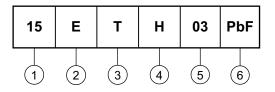
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ORDERING INFORMATION TABLE

Device code



1 - Current rating (15 = 15 A)

2 - E = Single diode

3 - Package:

T = TO-220

4 - H = Hyperfast recovery

5 - Voltage rating (03 = 300 V)

6 - • None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95221				
Part marking information	http://www.vishay.com/doc?95224			

www.vishay.com

For technical questions, contact: diodes-tech@vishay.com

Document Number: 94000 Revision: 24-Nov-08





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