

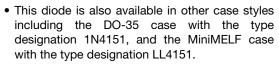
Vishay Semiconductors

Small Signal Fast Switching Diode



FEATURES

- · Silicon epitaxial planar diode
- · Fast switching diode





RoHS

- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <u>www.vishav.com/doc?99912</u>

MECHANICAL DATA

Case: SOD-123

Weight: approx. 10.3 mg
Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	PART ORDERING CODE		INTERNAL CONSTRUCTION	REMARKS	
1N4151W-V	1N4151W-V-GS18 or 1N4151W-V-GS08	A5	Single diode	Tape and reel	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	50	V	
Repetitive peak reverse voltage		V_{RRM}	75	V	
Average rectified current half wave rectification with resistive load (1)	f ≥ 50 Hz	I _{F(AV)}	150	mA	
Surge current	$t < 1 \text{ s and } T_j = 25 ^{\circ}\text{C}$	I _{FSM}	I _{FSM} 500		
Power dissipation (1)		P _{tot}	410	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air (1)		R _{thJA}	450	K/W		
Junction temperature		Tj	150	°C		
Storage temperature range		T _{stg}	- 65 to + 150	°C		

Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature.



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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Forward voltage	I _F = 50 mA	V _F			1.0	V	
Lookaga aurrant	$V_R = 50 \text{ V}$	I _R			50	nA	
Leakage current	$V_R = 20 \text{ V}, T_j = 150 ^{\circ}\text{C}$	I _R			50	μA	
Reverse breakdown voltage	$I_R = 5 \mu A \text{ (pulsed)}$	V _(BR)	75			V	
Diode capacitance	$V_F = V_R = 0 V$	C _D			2	pF	
Reverse recovery time	$I_F = 10 \text{ mA}, I_R = 10 \text{ mA}$ $I_R = 1 \text{ mA}$	t _{rr}			4	ns	
neverse recovery time	I_F = 10 mA, I_R = 1 mA V_R = 6 V, R_L = 100 Ω	t _{rr}			2	ns	

TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)

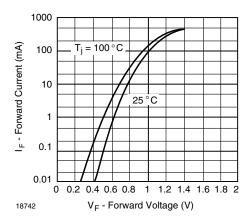


Fig. 1 - Forward Current vs. Forward Voltage

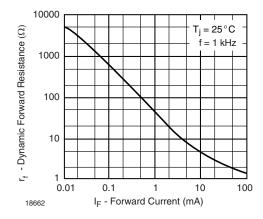


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

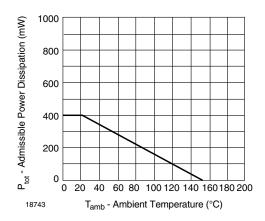


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

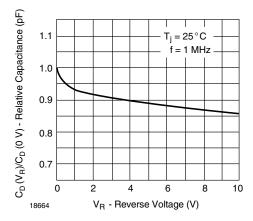


Fig. 4 - Relative Capacitance vs. Reverse Voltage



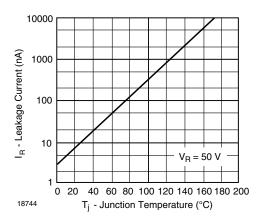


Fig. 5 - Leakage Current vs. Junction Temperature

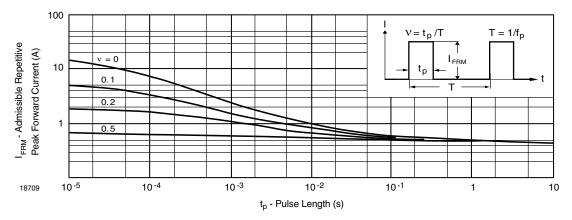
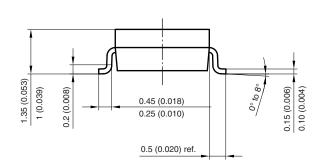


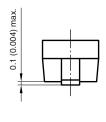
Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration



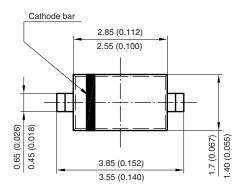
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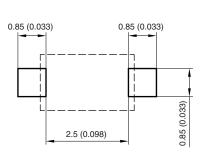
PACKAGE DIMENSIONS in millimeters (inches):SOD-123





Mounting Pad Layout





Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4)

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