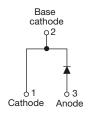


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High Performance Schottky Rectifier, 10 A





Single

TO-220AC 2L

Circuit configuration

PRIMARY CHARACTERISTICS I_{F(AV)} 10 A V_R 35 V, 40 V, 45 V V_F at I_F 0.49 V I_{RM} 15 mA at 125 °C T_J max. 175 °C E_{AS} 13 mJ Package 2L TO-220AC

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-10TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES							
I _{F(AV)}	Rectangular waveform	10	А				
V_{RRM}		35 to 45	V				
I _{FSM}	t _p = 5 μs sine	1050	А				
V _F	10 A _{pk} , T _J = 125 °C	0.49	V				
T _J	Range	-55 to +175	°C				

VOLTAGE RATINGS								
PARAMETER SYMBOL VS-10TQ035-M3 VS-10TQ040-M3 VS-10TQ045-M3								
Maximum DC reverse voltage	V_{R}	35	40	45	V			
Maximum working peak reverse voltage	V_{RWM}	33	40	43	V			

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 151 °C	10						
Maximum peak one cycle non-repetitive surge current	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1050	Α				
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	280					
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 6.5 mH		13	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	Α				

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
Maximum forward voltage drop See fig. 1		10 A	T _ 25 °C	0.57	V			
	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.67				
	VFM (1)	10 A	T.ı = 125 °C	0.49				
		20 A	1J= 125 C	0.61				
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	2	mΛ			
See fig. 2		T _J = 125 °C	V _R = nateu V _R	15	mA mA			
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		pF			
Typical series inductance	L _S	Measured lead to lead 5	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

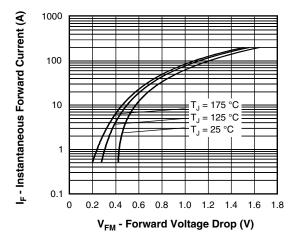
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C			
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.0	20044			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.50	°C/W			
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf • in)			
Marking device			Case style 2L TO-220AC	10T0				
			Case style ZL 10-220AC	10TQ045				





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1000
100
100
T_J = 175 °C
T_J = 125 °C
T_J = 100 °C
T_J = 75 °C
0.001
0.0001
0 5 10 15 20 25 30 35 40 45
V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

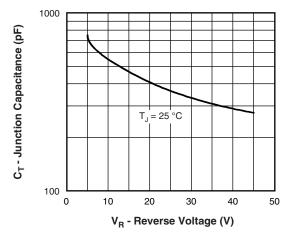


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

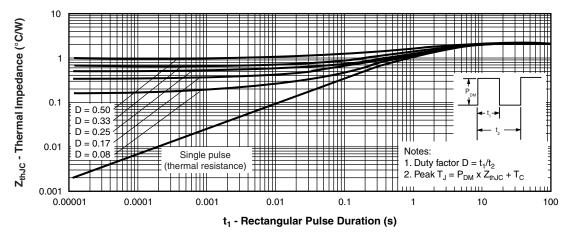


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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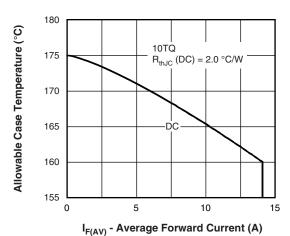


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

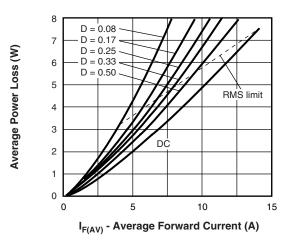


Fig. 6 - Forward Power Loss Characteristics

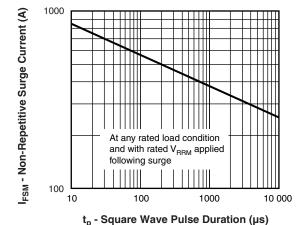


Fig. 7 - Maximum Non-Repetitive Surge Current

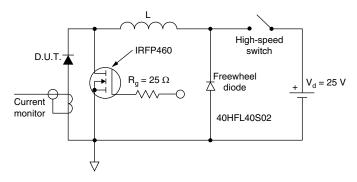


Fig. 8 - Unclamped Inductive Test Circuit

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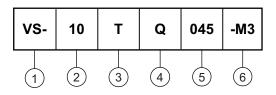


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ORDERING INFORMATION TABLE

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Device code



1 - Vishay Semiconductors product

2 - Current rating (10 = 10 A)

3 - Package:

T = TO-220

4 - Schottky "Q" series

035 = 35 V 040 = 40 V

Voltage ratings

045 = 45 V

Environmental digit

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-10TQ035-M3	50	Antistatic plastic tubes						
VS-10TQ040-M3	50	Antistatic plastic tubes						
VS-10TQ045-M3	50	Antistatic plastic tubes						

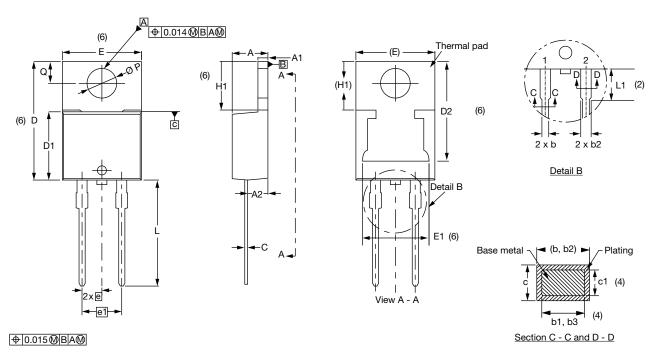
LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?96156						
Part marking information	www.vishay.com/doc?95391					

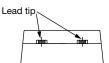
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Vishay Semiconductors

TO-220AC 2L

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIN	IETERS	INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	123	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355				•	•		•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- $^{(7)}$ Outline conforms to JEDEC $^{\!(\!R\!)}$ TO-220, except D2

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