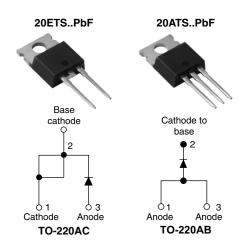


Vishay High Power Products

Input Rectifier Diode, 20 A



PRODUCT SUMMARY			
V _F at 10 A	1 V		
I _{FSM}	300 A		
V _{RRM}	800/1200 V		

DESCRIPTION/FEATURES

The 20ETS..PbF/20ATS..PbF rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.



RoHS*

Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

This product series has been designed and qualified for industrial level.

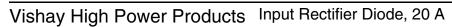
Compliant to RoHS directive 2002/95/EC.

OUTPUT CURRENT IN TYPICAL APPLICATIONS							
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS							
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W	16.3	21	А				

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Sinusoidal waveform	20	А			
V _{RRM}		800/1200	V			
I _{FSM}		300	A			
V _F	10 A, T _J = 25 °C	1.0	V			
T _J		- 40 to 150	°C			

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA			
20ETS08PbF, 20ATS08PbF	800	900	1			
20ETS12PbF, 20ATS12PbF	1200	1300	ı			

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply





ABSOLUTE MAXIMUM RATINGS							
PARAMETER SYMBOL TEST CONDITIONS				UNITS			
Maximum average forward current	I _{F(AV)}	T _C = 105 °C, 180° conduction half sine wave	20				
Maximum peak one cycle		10 ms sine pulse, rated V _{RRM} applied	250	А			
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	300				
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	316	A ² s			
Maximum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied 4		A-5			
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s			

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
Maximum forward voltage drop	V _{FM}	20 A, T _J = 25 °C		1.1	V		
Forward slope resistance	r _t	T _ 150 °C	10.4	mΩ			
Threshold voltage	V _{F(TO)}	1j=150 C	T _J = 150 °C				
Maximum reverse leakage current	1	T _J = 25 °C	V - Potod V	0.1	mA		
iviaximum reverse leakage current	I _{RM}	$T_{J} = 150 ^{\circ}\text{C}$ $V_{R} = \text{Rated } V_{RRM}$		1.0	IIIA		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 150	°C		
Maximum thermal resistance junction to case),	R_{thJC}	DC operation	1.3			
Typical thermal resistance, case to heatsink		R_{thCS}	Mounting surface, smooth and greased	0.5	°C/W		
Approximate weight				2	g		
Approximate weight				0.07	OZ.		
Mounting toyour	minimum			6 (5)	kgf · cm		
Mounting torque	maximum			12 (10)	(lbf · in)		
Marking device			Consist de TO 2004 C	20ETS08			
			Case style TO-220AC	20ETS12			
			Coop at the TO 200AB	20ATS08			
			Case style TO-220AB	20ATS12			





Input Rectifier Diode, 20 A Vishay High Power Products

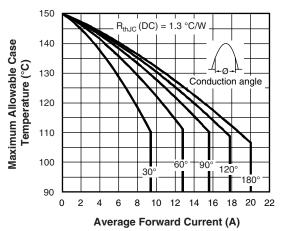


Fig. 1 - Current Rating Characteristics

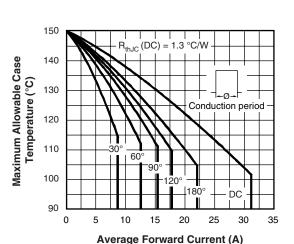


Fig. 2 - Current Rating Characteristics

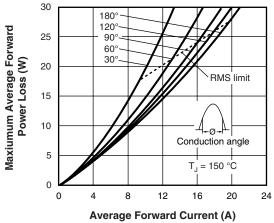


Fig. 3 - Forward Power Loss Characteristics

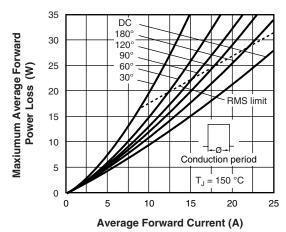


Fig. 4 - Forward Power Loss Characteristics

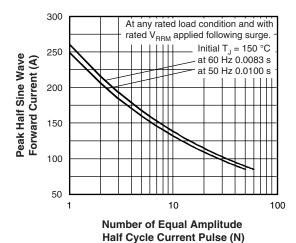


Fig. 5 - Maximum Non-Repetitive Surge Current

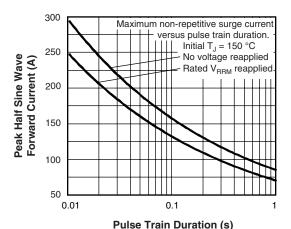


Fig. 6 - Maximum Non-Repetitive Surge Current

Vishay High Power Products Input Rectifier Diode, 20 A



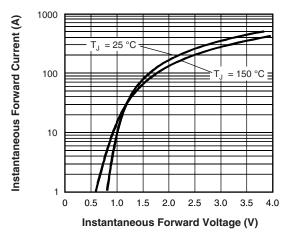


Fig. 7 - Forward Voltage Drop Characteristics

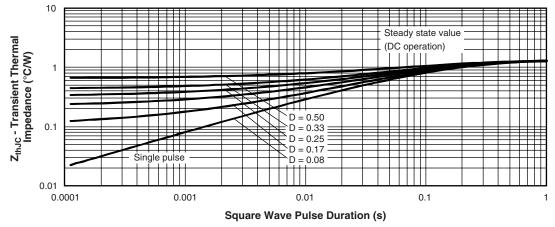
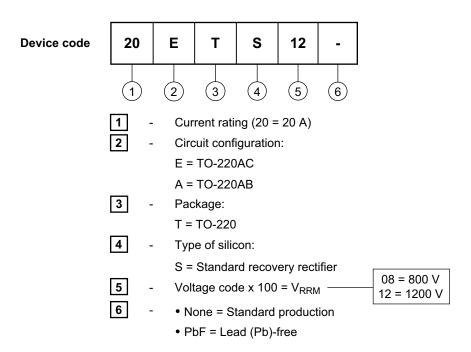


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



Input Rectifier Diode, 20 A Vishay High Power Products

ORDERING INFORMATION TABLE

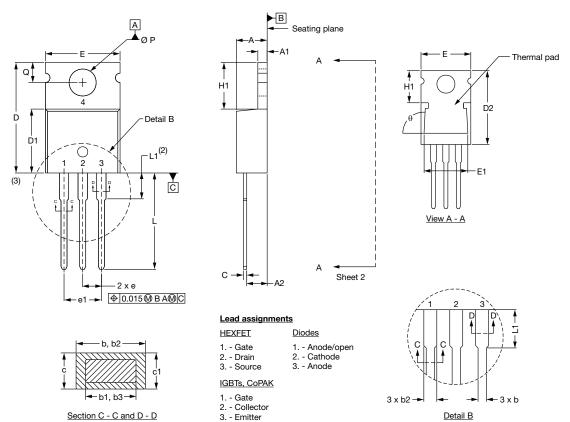


LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95180</u>				
Part marking information	www.vishay.com/doc?95181			

Vishay Semiconductors

TO-220AB, TO-220AC

DIMENSIONS FOR TO-220AB in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS		INCHES		
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.25	4.65	0.167	0.183		
A1	1.14	1.40	0.045	0.055		
A2	2.56	2.92	0.101	0.115		
b	0.69	1.01	0.027	0.040		
b1	0.38	0.96	0.015	0.038	4	
b2	1.20	1.73	0.047	0.068		
b3	1.15	1.73	0.045	0.068		
С	0.36	0.61	0.014	0.024		
c1	0.36	0.56	0.014	0.022	4	
c2	0.31	1.14	0.012	0.045		
D	14.85	15.25	0.585	0.600	3	
D1	8.38	9.02	0.330	0.355		

CAMBOI	SYMBOL MILLIMETERS INCHES		HES	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	12.19	12.88	0.480	0.507	
Е	10.11	10.51	0.398	0.414	3
E1	8.38	8.89	0.330	0.350	
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° to 93°		
			•	•	•

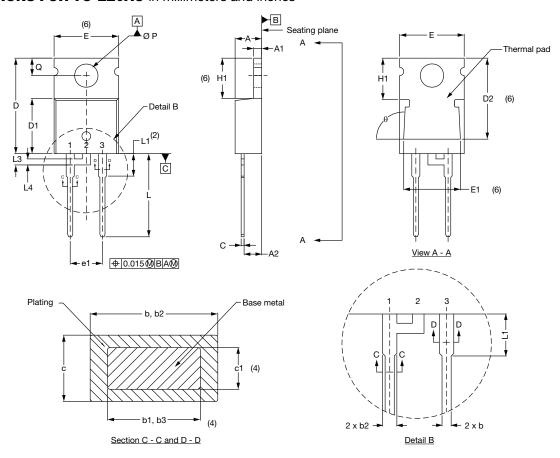
Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimensions: inches

TO-220AB, TO-220AC



DIMENSIONS FOR TO-220AC in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.96	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.15	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	12.19	12.88	0.480	0.507	6

SYMBOL	MILLIMETERS		INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	8.38	8.89	0.330	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.188	
θ	90° t	o 93°	90° t	o 93°	

Notes

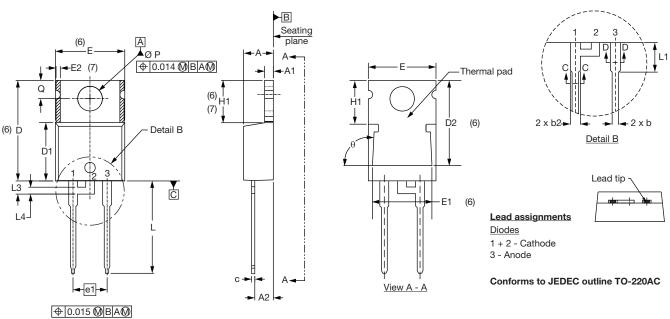
- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Outline conforms are derived from the actual package outline



Vishay Semiconductors

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° to 93°		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline

Document Number: 95221 Revision: 07-Mar-11

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Vishay

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