Vishay High Power Products

## Hyperfast Rectifier, 2 x 10 A FRED Pt<sup>™</sup>



- · Hyperfast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

#### **DESCRIPTION/APPLICATIONS**

300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

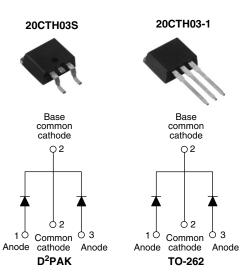
The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage		V <sub>RRM</sub>		300	V	
Average rectified forward current	per diode	I <sub>F(AV)</sub>	T <sub>C</sub> = 160 °C	10		
	per device			20	А	
Non-repetitive peak surge current		I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120		
Operating junction and storage temperatures		T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	300	-	-		
Formula voltana	N	I <sub>F</sub> = 10 A	-	1.05	1.25	V	
Forward voltage V <sub>F</sub>		I <sub>F</sub> = 10 A, T <sub>J</sub> = 125 °C	-	0.85	0.95		
Reverse leakage current		$V_{R} = V_{R}$ rated	-	-	20		
	IR	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	6	200	μA	
Junction capacitance	CT	V <sub>R</sub> = 300 V	-	30	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH	



35 ns

2 x 10 A

300 V

**PRODUCT SUMMARY** 

t<sub>rr</sub> (maximum)

I<sub>F(AV)</sub>

 $V_{\mathsf{R}}$ 



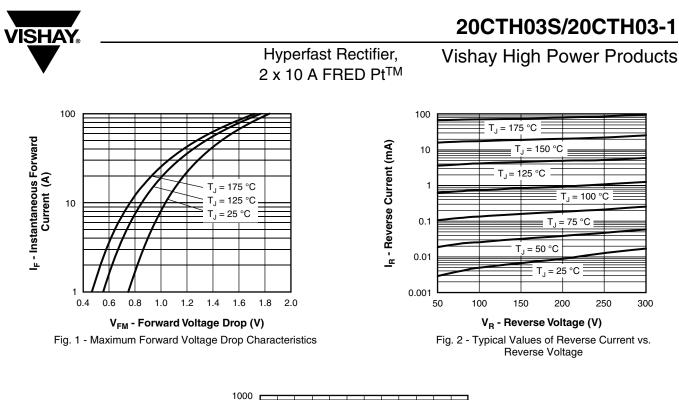
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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_c = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	50 A/µs, V <sub>R</sub> = 30 V	-	-	35	
	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	-	30	
		T <sub>J</sub> = 25 °C		-	31	-	
		T <sub>J</sub> = 125 °C		-	42	-	
Peak recovery current	1	11=23 0	I <sub>F</sub> = 10 A dI <sub>F</sub> /dt = 200 A/μs	-	2.4	-	- A
	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$V_{\rm B} = 200 \text{ V}$	-	5.6	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	36	-	- ns
	Qrr	T <sub>J</sub> = 125 °C		-	120	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per diode	R <sub>thJC</sub>		-	-	1.5	°C/W
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style D <sup>2</sup> PAK		20CTH03S		
		Case style TO-262		20CTH03-1		



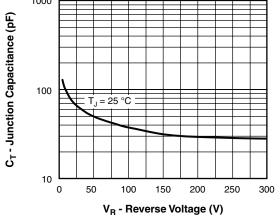


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

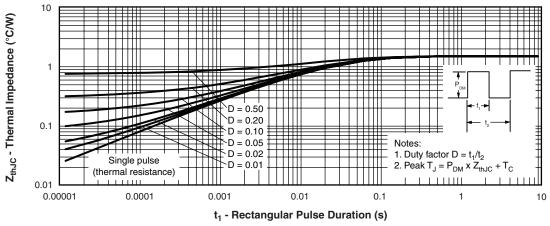
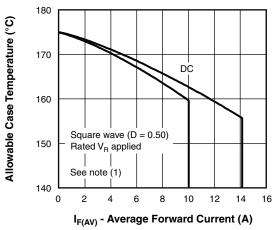


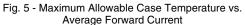
Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

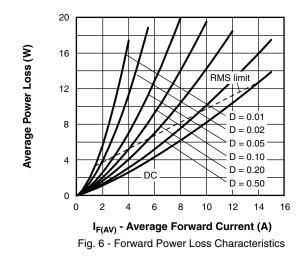
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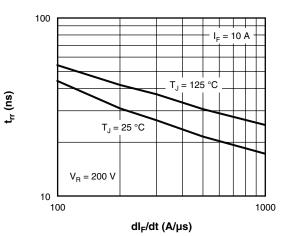






#### Note

- $^{(1)} \mbox{ Formula used: } T_C = T_J (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \mbox{ Forward power loss } = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \mbox{ Inverse power loss } = V_{R1} \ x \ I_R \ (1 D); \ I_R \ at \ V_{R1} = \ Rated \ V_R$



SHA

Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

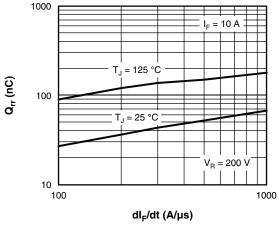


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



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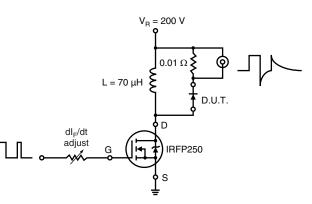


Fig. 9 - Reverse Recovery Parameter Test Circuit

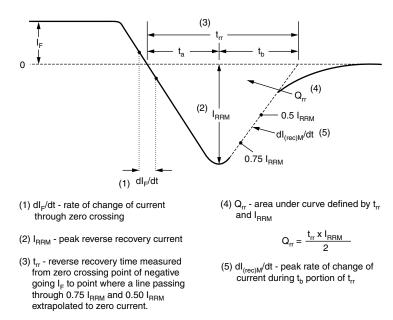


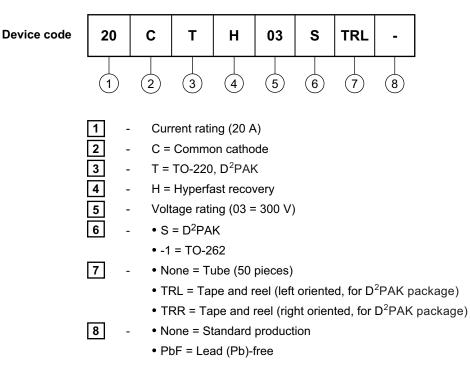
Fig. 10 - Reverse Recovery Waveform and Definitions



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#### ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			



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