please see www.vishay.com/doc?99912

MECHANICAL DATA

Case: SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test Polarity: as marked

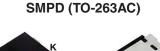
MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V60DM45C	UNIT	
Device marking code			V60DM45C		
Maximum repetitive peak reverse voltage		V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	60	Α	
	per diode		30	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	320	А	
Operating junction temperature range		T _J ⁽²⁾	-40 to +175	°C	
Storage temperature range		T _{STG}	-55 to +175		

Notes

⁽¹⁾ Mounted on infinite heatsink $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0,JA}

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eSMP[®] Series



Top View **Bottom View**



DESIGN SUPPORT TOOLS AVAILABLE



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 30 A			
V _{RRM}	45 V			
I _{FSM}	320 A			
V _F at I _F = 30 A (T _A = 125 °C)	0.55 V			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			

FEATURES

Dual Low-Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier Ultra Low V_F = 0.34 V at I_F = 5.0 A

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL J-STD-020. level 1, per LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

V60DM45C

Vishay General Semiconductor





HALOGEN FREE



V60DM45C



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.46	-	- V	
	I _F = 15 A			0.53	-		
	I _F = 30 A			0.61	0.68		
	$I_F = 5 A$	T _A = 125 °C		0.34	-		
	I _F = 15 A			0.43	-		
	I _F = 30 A			0.55	0.61		
Reverse current at rated V_R per diode	V _B = 45 V	T _A = 25 °C T _A = 125 °C	I _R ⁽²⁾	-	1.1	mA	
	$v_{\rm R} = 45 v$			7	25		
Typical junction capacitance	4.0 V, 1 MHz		CJ	4400	-	pF	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V60DM45C	UNIT	
Typical thermal resistance per device	R _{0JC} ⁽¹⁾	0.8	°C/W	
	R _{0JA} (2)(3)	50		

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$ - junction-to-ambient

⁽³⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V60DM45C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V60DM45CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified

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V60DM45C



Vishay General Semiconductor

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

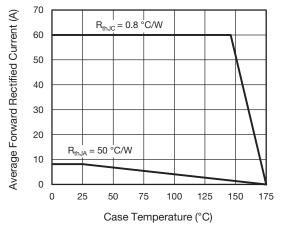


Fig. 1 - Maximum Forward Current Derating Curve

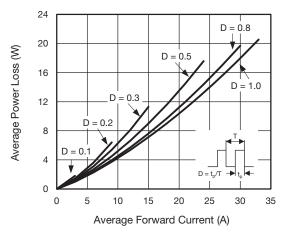


Fig. 2 - Average Power Loss Characteristics

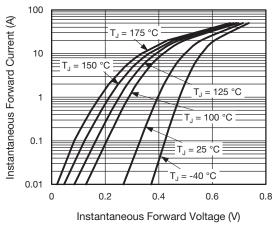


Fig. 3 - Typical Instantaneous Forward Characteristics

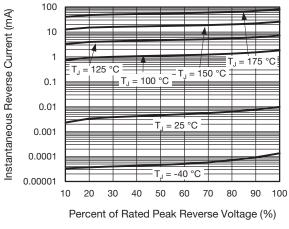


Fig. 4 - Typical Reverse Leakage Characteristics

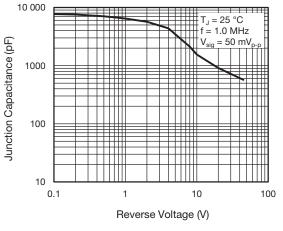


Fig. 5 - Typical Junction Capacitance

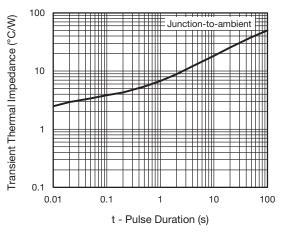


Fig. 6 - Typical Transient Thermal Impedance

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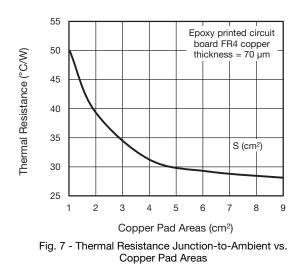
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V60DM45C

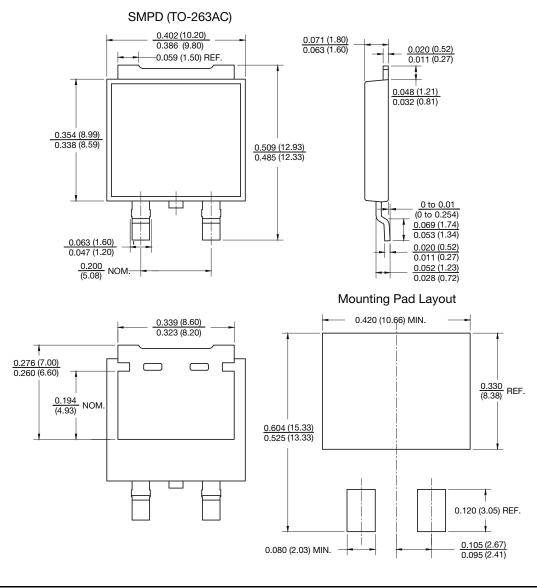




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