AUTOMOTIVE GRADE

COMPLIANT

HALOGEN



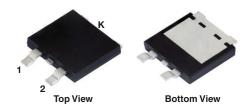
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# Vishay General Semiconductor

# Dual Low-Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.33 \text{ V}$  at  $I_F = 10 \text{ A}$ 

## eSMP<sup>®</sup> Series SMPD (TO-263AC)





### **ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 30 A			
$V_{RRM}$	45 V			
I <sub>FSM</sub>	320 A			
V <sub>F</sub> at I <sub>F</sub> = 30 A	0.48 V			
T <sub>J</sub> max.	150 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			

#### **FEATURES**

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

#### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Base  $P/NHM3_X$  - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

("\_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V60D45C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	45	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	60	А	
	per diode		30	A	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	320	А	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 10 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.44	-	. V	
	I <sub>F</sub> = 15 A			0.47	-		
	I <sub>F</sub> = 30 A			0.55	0.64		
	I <sub>F</sub> = 10 A	T <sub>A</sub> = 125 °C		0.33	-		
	I <sub>F</sub> = 15 A			0.37	-		
	I <sub>F</sub> = 30 A			0.48	0.56		
Reverse current per diode	V 45.V	T <sub>A</sub> = 25 °C	1 (2)	-	2500	μΑ	
	$V_R = 45 \text{ V}$ $T_A = 45 \text{ V}$	T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	19	60	mA	

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width  $\leq 5$  ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V60D45C	UNIT	
	per diode	- R <sub>θJC</sub>	1.5	°C/W	
Typical thermal resistance	per device		0.8		
	per device	R <sub>0</sub> JA (1)(2)	45		

#### **Notes**

 $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

(2) Free air, without heatsink

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
V60D45C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel			
V60D45CHM3 A/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel			

### Note

(1) AEC-Q101 qualified

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

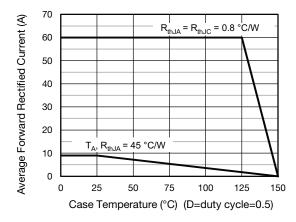


Fig. 1 - Forward Current Derating Curve

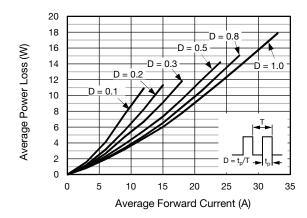


Fig. 2 - Forward Power Loss Characteristics Per Diode



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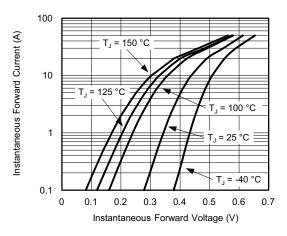


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

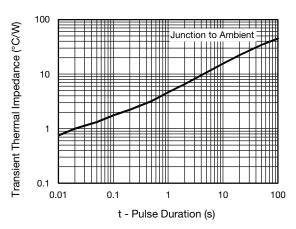


Fig. 6 - Typical Transient Thermal Impedance Per Diode

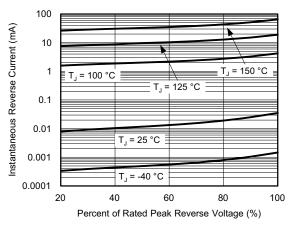


Fig. 4 - Typical Reverse Characteristics Per Diode

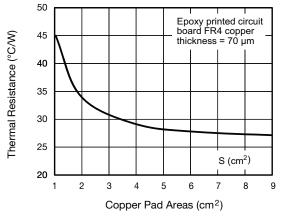


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

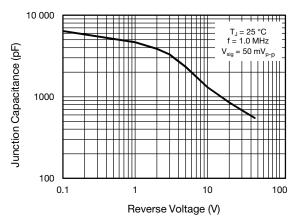


Fig. 5 - Typical Junction Capacitance Per Diode



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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## SMPD (TO-263AC) 0.402 (10.20) 0.071 (1.80) 0.063 (1.60) 0.386 (9.80) 0.020 (0.52) 0.011 (0.27) 0.059 (1.50) REF. 0.048 (1.21) 0.032 (0.81) 0.354 (8.99) 0.509 (12.93) 0.485 (12.33) 0.338 (8.59) 0 to 0.01 (0 to 0.254) 0.069 (1.74) 0.053 (1.34) 0.063 (1.60) 0.020 (0.52) 0.047 (1.20) 0.011 (0.27) 0.200 NOM. 0.052 (1.23) 0.028 (0.72) **Mounting Pad Layout** 0.420 (10.66) MIN. 0.339 (8.60) 0.323 (8.20) 0.276 (7.00) 0.260 (6.60) $\frac{0.330}{(8.38)}$ REF. $\frac{0.194}{(4.93)}$ NOM. 0.604 (15.33) 0.525 (13.33) 0.120 (3.05) REF. 0.105 (2.67) 0.095 (2.41) 0.080 (2.03) MIN.

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