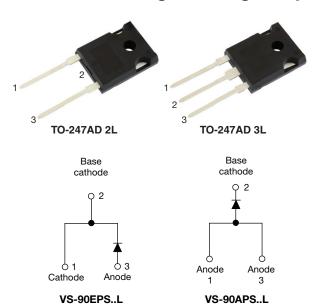


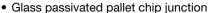
## High Voltage, Input Rectifier Diode, 90 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	90 A				
$V_{R}$	800 V to 1200 V				
V <sub>F</sub> at I <sub>F</sub>	1.20 V				
I <sub>FSM</sub>	1100 A				
T <sub>J</sub> max.	150 °C				
Package	TO-247AD 2L, TO-247AD 3L				
Circuit configuration	Single				

#### **FEATURES**

- Very low forward voltage drop
- 150 °C max. operating junction temperature



 Designed and qualified according to JEDEC®-JESD 47

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

# ROHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- Input rectification for single and three phase bridge configurations
- Off-board EV/HEV battery chargers
- Renewable energy inverters
- Input rectification for single and three phase bridge configurations
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

#### **DESCRIPTION**

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UN							
I <sub>F(AV)</sub>	Sinusoidal waveform	90	А				
V <sub>RRM</sub>	Range	800 to 1200	V				
I <sub>FSM</sub>		1100	Α				
V <sub>F</sub>	90 A, T <sub>J</sub> = 25 °C	1.20	V				
TJ		-40 to +150	°C				

VOLTAGE RATINGS						
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> AT 150 °C mA			
VS-90EPS08L-M3, VS-90APS08L-M3	800	900	1.5			
VS-90EPS12L-M3, VS-90APS12L-M3	1200	1300	1.5			



# VS-90EPS..L-M3, VS-90APS..L-M3

## Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum average forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 112 °C, 180° conduction half sine wave	90				
Maximum peak one cycle		10 ms sine pulse, rated V <sub>RRM</sub> applied	915	Α			
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine pulse, no voltage reapplied	1100				
Maximum I <sup>2</sup> t for fusing	I <sup>2</sup> t	10 ms sine pulse, rated V <sub>RRM</sub> applied	4185 A <sup>2</sup> s				
Maximum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied 605		A-S			
Maximum I <sup>2</sup> √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied	60 500	A²√s			

ELECTRICAL SPECIFICATIONS							
PARAMETER SYMBOL TEST CONDITIONS VALUES							
Maximum forward voltage drop	$V_{FM}$	90 A, T <sub>J</sub> = 25 °C	1.20	V			
Forward slope resistance	r <sub>t</sub>	T 150 °C	3.17	mΩ			
Threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> = 150 °C		0.73	V		
Maximum reverse leakage current	I	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>BBM</sub>	0.1	mA		
waxiiiuiii reverse leakaye current	I <sub>RM</sub>	T <sub>J</sub> = 150 °C	VR = nated VRRM	1.5	IIIA		

THERMAL - MECHANICA	AL SPECI	FICATIO	NS		
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-40 to +150	°C
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	0.2	
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		40	°C/W
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	0.25	
Approximate weight				6	g
Approximate weight				0.21	OZ.
Mounting toward	minimum			6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf $\cdot$ in)
Marking davice			Case style TO-247AD 2L	90EPS08L, 9	00EPS12L
Marking device			Case style TO-247AD 3L	90APS08L, 9	00APS12L

#### www.vishay.com

## Vishay Semiconductors

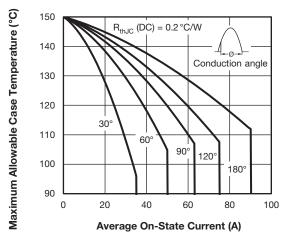


Fig. 1 - Current Rating Characteristics

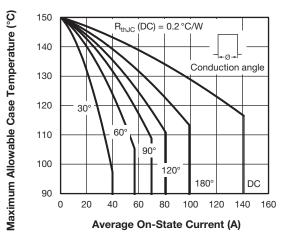


Fig. 2 - Current Rating Characteristics

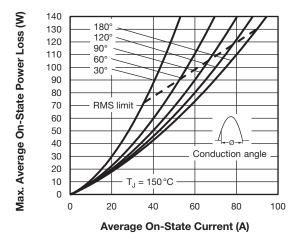


Fig. 3 - Forward Power Loss Characteristics

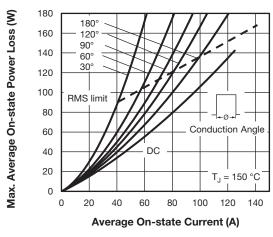


Fig. 4 - Forward Power Loss Characteristics

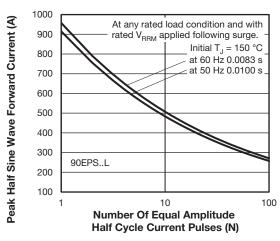


Fig. 5 - Maximum Non-Repetitive Surge Current

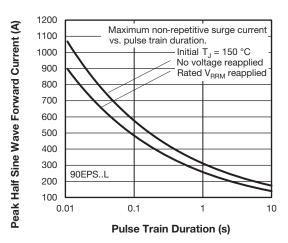


Fig. 6 - Maximum Non-Repetitive Surge Current

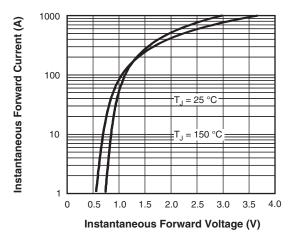


Fig. 7 - Forward Voltage Drop Characteristics

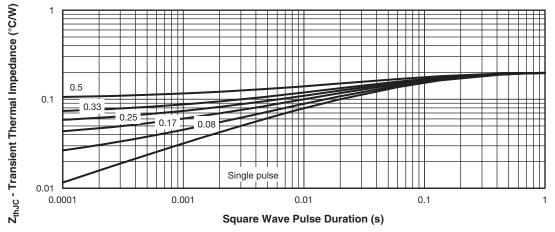
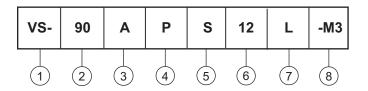


Fig. 8 - Thermal Impedance  $Z_{thJC}$  Characteristics



#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Current rating (90 = 90 A)

3 - Circuit configuration:

E = single diode, 2 pins

A = single diode, 3 pins

4 - Package:

P = TO-247AD

5 - Type of silicon:

S = standard recovery rectifier

7 - L = long leads

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

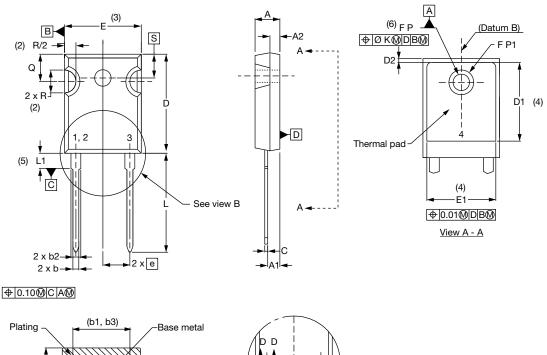
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-90EPS08L-M3	25	500	Antistatic plastic tubes			
VS-90APS08L-M3	25	500	Antistatic plastic tubes			
VS-90EPS12L-M3	25	500	Antistatic plastic tubes			
VS-90APS12L-M3	25	500	Antistatic plastic tubes			

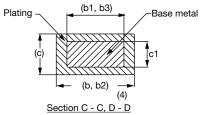
LINKS TO RELATED DOCUMENTS				
Dimensions	TO-247AD 2L	www.vishay.com/doc?95536		
Differisions	TO-247AD 3L	www.vishay.com/doc?95626		
Dort marking information	TO-247AD 2L	www.vishay.com/doc?95648		
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007		

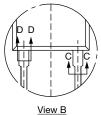


### **TO-247AD 2L**

#### **DIMENSIONS** in millimeters and inches







SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4
D2	0.51	1.35	0.020	0.053	

SYMBOL	MILLIMETERS		INC	NOTES	
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	0.254		)10	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	5.51 BSC		'BSC	
		•	•	•	•

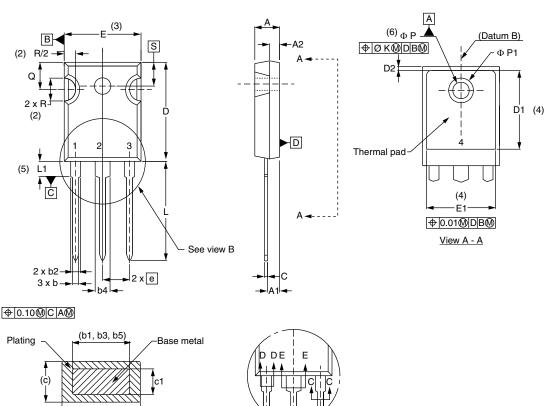
#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



### **TO-247AD 3L**

#### **DIMENSIONS** in millimeters and inches



View B

	MILLIM	IETERS	INC	HES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

Section C - C, D - D, E - E

SYMBOL	MILLIN	IETERS	INC	INCHES		
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.30	0.020	0.051		
E	15.29	15.87	0.602	0.625	3	
E1	13.46	=.	0.53	-		
е	5.46	BSC	0.215	BSC		
ØK	0.2	0.254		0.010		
L	19.81	20.32	0.780	0.800		
L1	3.71	4.29	0.146	0.169		
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	6.98	-	0.275		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51	BSC	0.217	'BSC		
	•			•		

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
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