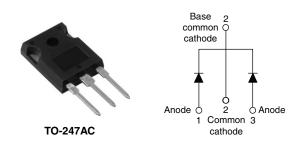
Vishay High Power Products

High Performance Schottky Generation 5.0, 2 x 15 A



PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

V_F at 15 A at 125 °C

FEATURES

- 175 °C high performance Schottky diode
- · Very low forward voltage drop
- · Extremely low reverse leakage
- Optimized V_F vs. I_R trade off for high efficiency
- · Increased ruggedness for reverse avalanche capability
- RBSOA available
- · Negligible switching losses
- · Submicron trench technology
- · Full lead (Pb)-free and RoHS compliant devices
- · Designed and qualified for industrial level

APPLICATIONS

- High efficiency SMPS
- Automotive
- High frequency switching
- · Output rectification
- · Reverse battery protection
- Freewheeling
- · Dc-to-dc systems
- · Increased power density systems

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES UNI									
V _{RRM}		100	N/						
V _F	15 Apk, T _J = 125 °C (typical, per leg)	0.63	v						
TJ	Range	- 55 to 175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	30CPT100	UNITS				
Maximum DC reverse voltage	VR	T _J = 25 °C	100	V				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS				
Maximum average per leg		1	50 % duty cycle at $T_{\rm C}$ = 158 °C,	15					
forward current	per device	I _{F(AV)}	50% utily cycle at $1^\circ_{\rm C} = 150\%$ C,	30	A				
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated		920			
non-repetitive surge current		IFSM	10 ms sine or 6 ms rect. pulse	V_{RRM} applied	240				
Non-repetitive avalanche er	ergy	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 1.1 \ A, \ L = 60 \ m$	36	mJ				
Repetitive avalanche current		I _{AR}	Limited by frequency of operation and time pulse duration so that $T_J < T_J$ max. I_{AS} at T_J max. as a function of time pulse See fig. 8		I _{AS} at T _J max.	А			

COMPLIANT

2 x 15 A

100 V

0.67 V

30CPT100

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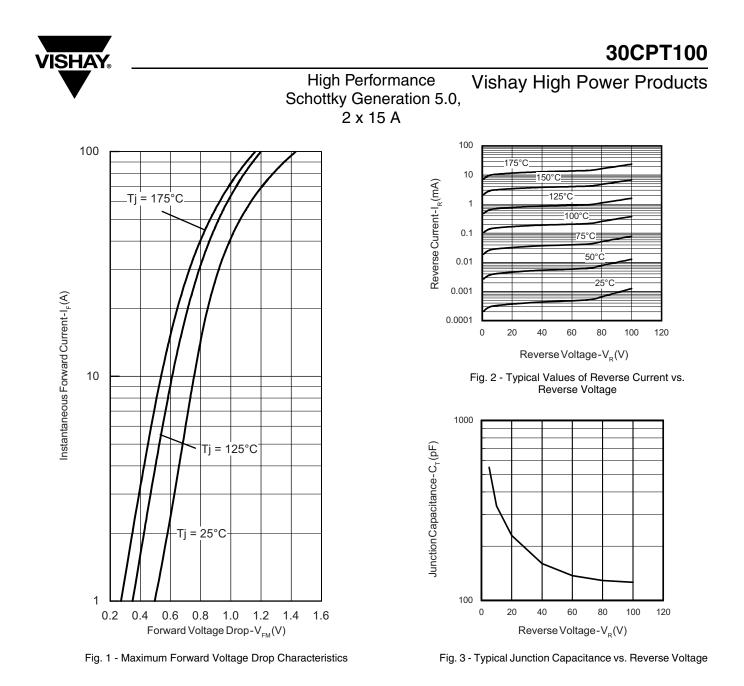


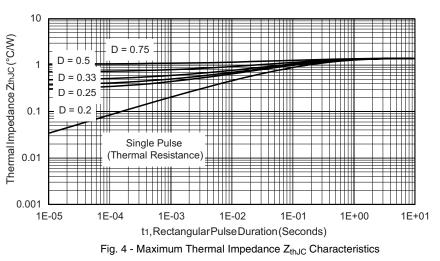
ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITION	TYP.	MAX.	UNITS				
		15 A	T.I = 25 °C	-	0.81	v			
Forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	1J=25 C	-	0.92				
Forward voltage drop per leg	V FM (*)	15 A	T.I = 125 °C	-	0.67				
		30 A	1j=125 C	-	0.79				
Reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	-	120	μA			
neverse leakage current per leg	IRM (1)	T _J = 125 °C	V _R = naleu V _R	-	5	mA			
Junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 10)	550	-	pF				
Series inductance per leg	L _S	Measured lead to lead 5 mm fro	7.5	-	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	-	10 000	V/µs				

Note

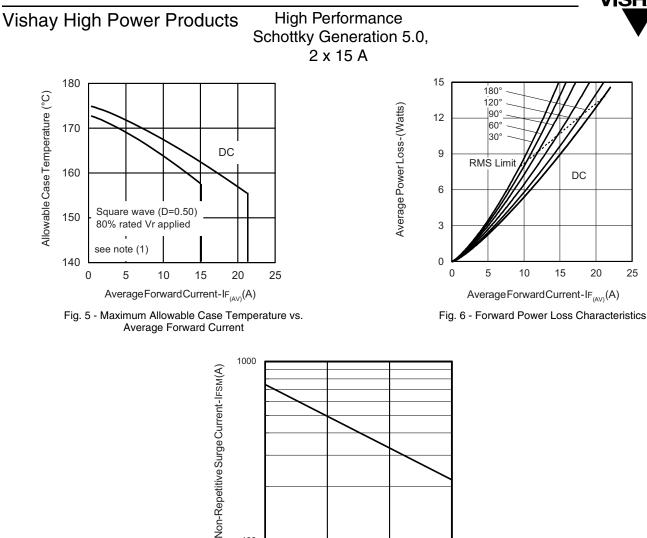
 $^{(1)}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C				
Maximum thermal resistance, junction to case per leg		D		1.4					
Maximum thermal resistance, junction to case per device		R _{thJC}	DC operation	0.8	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25					
Approvimate weight				6	g				
Approximate weight				0.21	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm				
Mounting torque	maximum			12 (10)	(lbf · in)				
Marking device			Case style TO-247AC	30CP	T100				





30CPT100



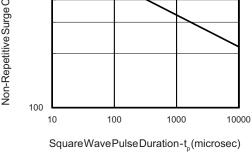
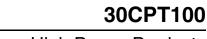


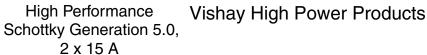
Fig. 7 - Maximum Non-Repetitive Surge Current

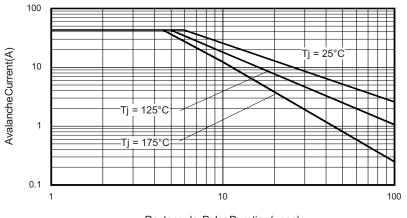
Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = 80 \%$ rated V_R

25







RectangularPulseDuration(µsec)

Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

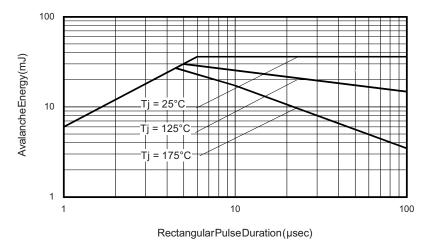
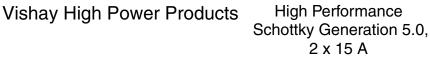


Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)

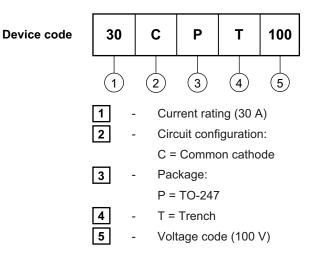
SHAY

30CPT100





ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

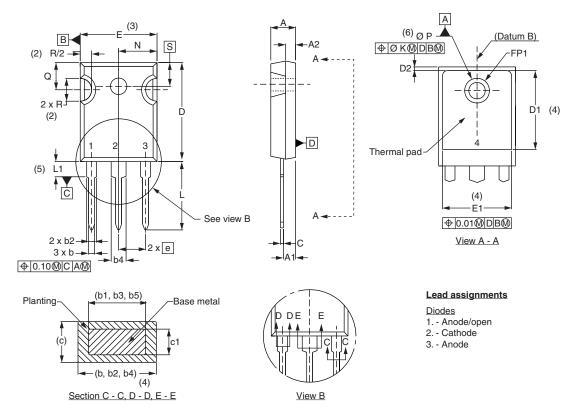
LINKS TO RELATED DOCUMENTS					
Dimensions	http://www.vishay.com/doc?95223				
Part marking information	http://www.vishay.com/doc?95226				

Outline Dimensions





DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		TERS INCHES		NOTES SYMBOL		MILLIN	IETERS	INC	HES	NOTES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIDOL	MIN.	MAX.	MIN.	MAX.	NUTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			FK	2.	54	0.0	010	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0.3		
b5	2.59	3.38	0.102	0.133			ΦP	3.56	3.66	0.14	0.144	
С	0.38	0.86	0.015	0.034			Φ P1	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	1.78	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	

Notes

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1
- ⁽⁵⁾ Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- ⁽⁷⁾ Outline conforms to JEDEC outline TO-247 with exception of dimension c

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



Vishay

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