Vishay High Power Products

Ultrafast Rectifier, 2 x 8 A FRED PtTM



- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

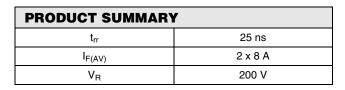
Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage		V _{RRM}		200	V
Average rectified forward current	per leg	1		8.0	
Average rectined forward current	total device	I _{F(AV)}	Rated V _R , T _C = 150 °C	16	^
Non-repetitive peak surge current per leg		I _{FSM}		100	A
Peak repetitive forward current per leg		I _{FM}	Rated V_R , square wave, 20 kHz, T_C = 150 °C	16	
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$\begin{array}{c} V_{BR,} \\ V_{R} \end{array} I_{R} = 100 \ \mu A \end{array} \qquad \qquad 200$		-	-		
Forward voltage V _F	V	I _F = 8 A	-	-	0.975	V
	I _F = 8 A, T _J = 150 °C	-	-	0.895		
Reverse leakage current I _R	$V_{R} = V_{R}$ rated	-	-	5		
	IR	$T_J = 150 \ ^{\circ}C, \ V_R = V_R \ rated$	-	-	250	μA
Junction capacitance	CT	V _R = 200 V	-	25	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body -		8.0	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply





TO-220AB

Base 2 common Q

cathode

2 O Common 3

cathode

Anode O

O Anode



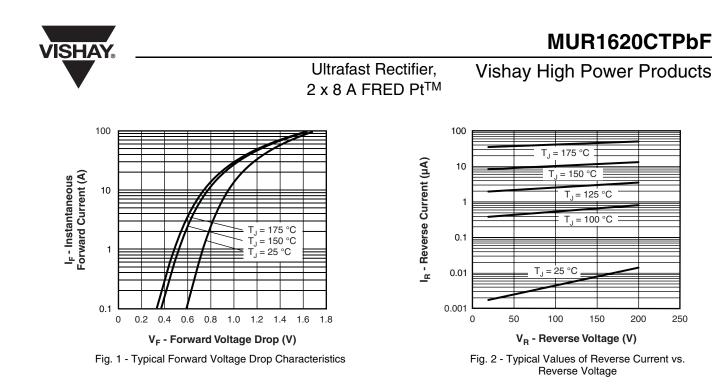
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	-	35		
	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ A}$		-	-	25		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	20	-	ns
		T _J = 125 °C		-	34	-	
Peak recovery current		T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/µs	-	1.7	-	А
Peak recovery current I _{RRM}	T _J = 125 °C	$V_{\rm B} = 160 \text{ V}$	-	4.2	-	A	
Reverse recovery charge	Qrr	T _J = 25 °C		-	23	-	nC
	Qrr	T _J = 125 °C		-	75	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	3.0	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-220AB		MUR1	620CT	-



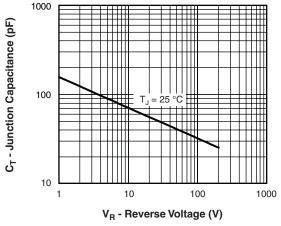


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

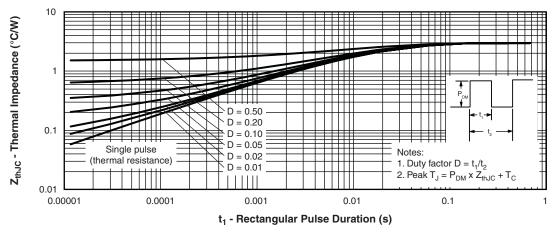


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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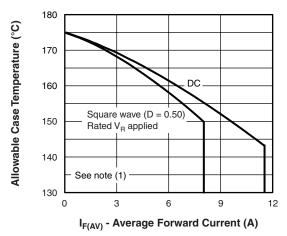
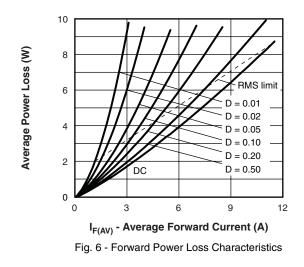


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

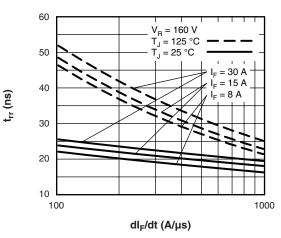
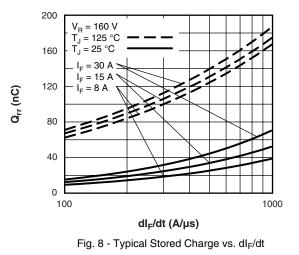


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt



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V_R = 200 V L = 70 μH

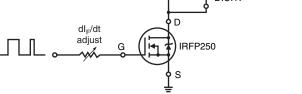


Fig. 9 - Reverse Recovery Parameter Test Circuit

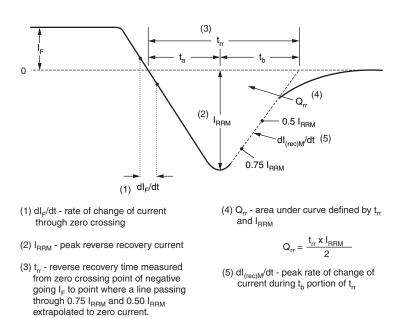


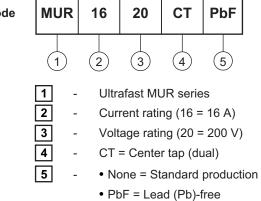
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			



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