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RoHS

HALOGEN FREE

GREEN

(5-2008)

Solid Tantalum Surface-Mount Capacitors TANTAMOUNT™, Low ESR, Hi-Rel COTS, Built-in-Fuse, Leadframeless Molded



PERFORMANCE CHARACTERISTICS

Operating Temperature: -55 °C to +125 °C (above 85 °C, voltage derating is required)

Capacitance: 10 μF to 470 μF

Capacitance Tolerance: \pm 10 %, \pm 20 %

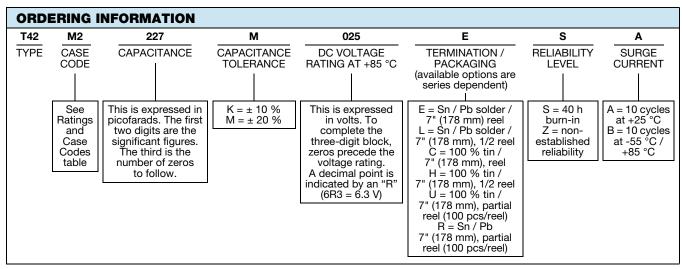
Voltage Rating: 16 V_{DC} to 75 V_{DC}

FEATURES

- Individually fused multiple anode construction ensures fail-safe operation and graceful degradation in the event of failure
- High-reliability design with reliability screening available
- Surge current testing per MIL-PRF-55365 options available
- Ultra-low ESR
- Fuse characteristics: guaranteed fuse protection at 5 A, 10 ms
- Fuse characteristics are optimized to ensure activation to protect against catastrophic failures while avoiding false triggering
- Terminations: wraparound Sn / Pb, standard. 100 % tin available
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details



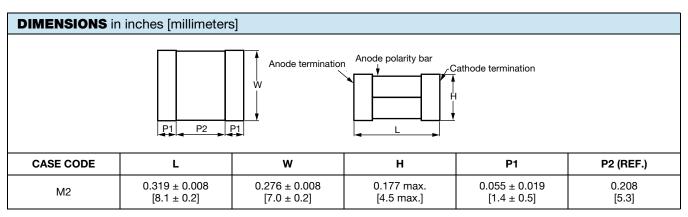
Note

We reserve the right to supply higher voltage ratings and tighter capacitance tolerance capacitors in the same case size.
 Low ESR solid tantalum chip capacitors allow delta ESR of 1.25 times the datasheet limits after mounting.

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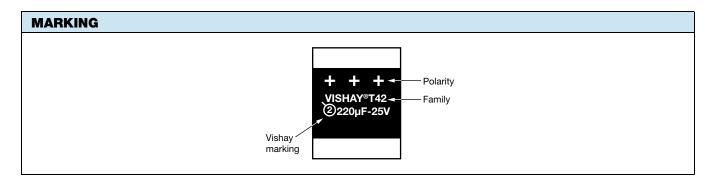
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RATINGS	RATINGS AND CASE CODES						
μF	16 V	20 V	25 V	35 V	50 V	63 V	75 V
10							M2 ⁽¹⁾
15						M2 ⁽¹⁾	
22						M2 ⁽¹⁾	
33					M2 ⁽¹⁾		
47				M2 ⁽¹⁾			
68				M2 ⁽¹⁾			
100				M2 ⁽¹⁾			
150			M2 ⁽¹⁾				
220			M2				
330		M2 ⁽¹⁾					
470	M2 ⁽¹⁾						

Note

(1) Rating in development, contact factory for availability





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STANDARD F	RATINGS					
CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT +25 °C (μA)	MAX. DF AT +25 °C (%)	MAX. ESR AT +25 °C 100 kHz (mΩ)	MAX. RIPPLE 100 kHz I _{RMS} (A)
		16 V _{DC} AT	+85 °C; 10 V _{DC} AT -	⊦125 °C		
470	M2 ⁽¹⁾	T42M2477(1)016(2)(3)(4)	75.2	20	200	0.6
		20 V _{DC} AT	+85 °C; 13 V _{DC} AT -	+125 °C		
330	M2 ⁽¹⁾	T42M2337(1)020(2)(3)(4)	66.0	18	200	0.6
		25 V _{DC} AT	+85 °C; 17 V _{DC} AT -	+125 °C		
150	M2 ⁽¹⁾	T42M2157(1)025(2)(3)(4)	37.5	12	180	0.7
220	M2	T42M2227M025(2)(3)(4)	55.0	15	200	0.6
		35 V _{DC} AT	+85 °C; 23 V _{DC} AT -	⊦125 °C		
47	M2 ⁽¹⁾	T42M2476(1)035(2)(3)(4)	16.5	10	200	0.6
68	$M2^{(1)}$	T42M2686(1)035(2)(3)(4)	23.8	10	200	0.6
100	$M2^{(1)}$	T42M2107(1)035(2)(3)(4)	35.0	12	200	0.6
		50 V _{DC} AT	+85 °C; 33 V _{DC} AT -	⊦125 °C		
33	M2 ⁽¹⁾	T42M2336(1)050(2)(3)(4)	16.5	10	250	0.6
		63 V _{DC} AT	+85 °C; 42 V _{DC} AT -	+125 °C		
15	M2 ⁽¹⁾	T42M2156(1)063(2)(3)(4)	9.5	8	500	0.4
22	$M2^{(1)}$	T42M2226(1)063(2)(3)(4)	13.9	10	350	0.5
		75 V _{DC} AT	+85 °C; 50 V _{DC} AT -	+125 °C		
10	M2 ⁽¹⁾	T42M2106(1)075(2)(3)(4)	7.5	8	600	0.4

Notes

- Part number definitions:
 - (1) Capacitance tolerance: K = 10 %, M = 20 %
 - (2) Termination and packaging: C, E, H, L, U, R
 - (3) Reliability level: S, Z
 - (4) Surge current: A, B
- (1) Rating in development, contact factory for availability

RECOMMENDED VOLTAGE DERATING GUIDELIN	ES (for temperatures below +85 °C)
STANDARD CONDITIONS. FOR EXAMPLE: OUTPUT FILTERS	
Capacitor Voltage Rating	Operating Voltage
16	10
20	12
25	15
35	24
50	28
63	37.8
75	45
SEVERE CONDITIONS. FOR EXAMPLE: INPUT FILTERS	
Capacitor Voltage Rating	Operating Voltage
16	8.0
20	10
25	12
35	15
50	24
63	32
75	37



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CARRIER TAPE DIMENSIONS in inches [millimeters]						
TYPE	CASE CODE	TAPE WIDTH W (mm)	P ₁	K _{0 max.}	B _{1 max} .	
T42	M2	16	0.476 ± 0.004 [12.0 ± 0.1]	0.193 [4.9]	0.193 [8.6]	

STANDARD PACKAGING QUANTITY						
CASE CODE	QUANTITY (PCS/REEL)					
CASE CODE	7" REEL	7" HALF REEL	7" PARTIAL REEL			
M2	320	160	100			

POWER DISSIPATION	
CASE CODE	MAXIMUM PERMISSIBLE POWER DISSIPATION (W) AT +25 °C IN FREE AIR
M2	0.084

ITEM	PERFORMANCE CHAR	ACTERISTICS				
Category temperature range		5 °C with voltage derating)				
Capacitance tolerance	`	via bridge method, at 25 °C	C. 120 Hz			
Dissipation factor			e method, at 25 °C, 120 Hz.			
ESR	<u> </u>	<u> </u>	e method, at 25 °C, 100 kHz			
Leakage current	After application of rated resistor in series with the Standard Ratings table. <i>I</i>	voltage applied to capacito capacitor under test, leaka	ors for 5 min using a steady sage current at 25 °C is not rank varies with temperature a	source of power with 1 $k\Omega$		
Capacitance change by temperature	+10 % max	(at +125 °C) (at +85 °C) (at -55 °C)	+20 % max. +15 % max	e value > 300 μF (at +125 °C) . (at +85 °C) . (at -55 °C)		
Reverse voltage	Capacitors are capable of withstanding peak voltages in the reverse direction equal to: 10 % of the DC rating at +25 °C 5 % of the DC rating at +85 °C Vishay does not recommend intentional or repetitive application of reverse voltage.					
Ripple current and Temperature derating	For maximum permissible ripple current (I _{RMS}) or / and voltage (V _{RMS}) please refer to product datasheet and Guide to Application. If capacitors are to be used at temperatures above +25 °C, the permissible RMS ripple current or voltage shall be calculated using the derating factors: 1.0 at +25 °C 0.9 at +85 °C 0.4 at +125 °C					
Maximum operating voltage		OPERATING T	EMPERATURE			
	+85	5 °C	+12	5 °C		
	RATED VOLTAGE (V)	SURGE VOLTAGE(V)	RATED VOLTAGE (V)	SURGE VOLTAGE(V)		
	16	20	10	12		
	20	26	13	16		
25 32 17 35 46 23						
						50
	50 ⁽¹⁾	60	33	40		
	63	76	42	50		
	75	90	50	60		

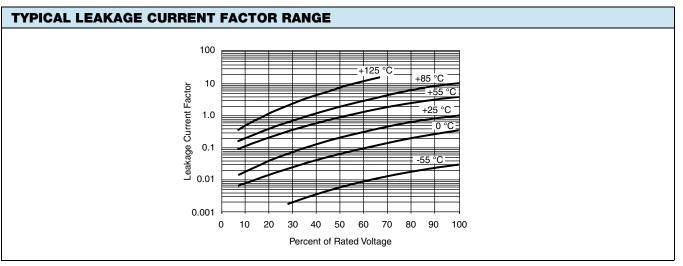
Note

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 $^{^{(1)}\,}$ Capacitance values 15 μF and higher



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Notes

- At +25 °C, the leakage current shall not exceed the value listed in the Standard Ratings table
- At +85 °C, the leakage current shall not exceed 10 times the value listed in the Standard Ratings table
- At +125 °C, the leakage current shall not exceed 12 times the value listed in the Standard Ratings table

CAPACITOR PERFORMANCE CHARACTERISTICS						
ITEM	CONDITION	POST TEST PERFORMANCE				
Surge voltage	85 °C, 1000 successive test cycles at 1.3 of rated	Capacitance change	Within ± 10 % of initial			
	voltage in series with a 1 k Ω resistor at the rate of 30 s ON, 30 s OFF, MIL-PRF-55365	Dissipation factor	Not to exceed initial			
		Leakage current	Not to exceed initial			
Life test at +85 °C			Within ± 10 % of initial			
	MIL-STD-202 method 108	Leakage current	Not to exceed 125 % of initial			
Life test at +125 °C	1000 h application of 2/3 rated voltage at 125 °C,	Capacitance change	Within ± 20 % of initial			
	MIL-STD-202 method 108	Leakage current	Not to exceed 125 % of initial			

ENVIRONMENTAL CHARACTERISTICS					
ITEM	CONDITION	POST TEST PERFORMANCE			
Moisture resistance	MIL-STD-202, method 106, rated voltage.	Capacitance change			
		Cap. ≤ 600 μF	Within ± 10 % of initial		
		Cap. > 600 μF	Within ± 20 % of initial		
		Dissipation factor	Initial specified value or less		
		Leakage current	Initial specified value or less		
Thermal shock	Capacitors are subjected to 5 cycles per MIL-STD-202 method 107 of the following: -55 °C (+ 0 °C, - 5 °C) for 30 min, then +25 °C (+ 10 °C, - 5 °C) for 5 min, then +125 °C (+ 3 °C, - 0 °C) for 30 min, then +25 °C (+ 10 °C, - 5 °C) for 5 min	Capacitance change			
		Cap. ≤ 600 μF	Within ± 10 % of initial		
		Cap. > 600 μF	Within ± 20 % of initial		
		Dissipation factor	Initial specified value or less		
		Leakage current	Initial specified value or less		





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MECHANICAL PERFORMANCE CHARACTERISTICS					
ITEM	CONDITION	POST TEST PERFORMANCE			
Shear test	Apply a pressure load of 5 N for 10 s ± 1 s horizontally to the center of capacitor side body. AEC-Q200-006	There shall be no visual damage when viewed at 20 x magnification and the component shall meet the original electrical requirements.			
Vibration	MIL-STD-202, method 204, condition D, 10 Hz to 2000 Hz, 20 <i>g</i> peak	There shall be no mecha capacitors post-condition	nical or visual damage to ning.		
Shock	MIL-STD-202, method 213, condition I,	Capacitance change	Within ± 10 % of initial		
(specified pulse)	100 <i>g</i> peak	Dissipation factor	Initial specified value or less		
		Leakage current	Initial specified value or less		
		There shall be no mechanical or visual damage to capacitors post-conditioning.			
Resistance to	MIL-STD-202, method 210, condition J,	Capacitance change	Within ± 10 % of initial		
soldering heat	except with only one heat cycle	Dissipation factor	Initial specified value or less		
		Leakage current	Initial specified value or less		
		There shall be no mecha capacitors post-conditio	nical or visual damage to ning.		
Solderability	MIL-STD-202, method 208, ANSI/J-STD-002, test B. Applies only to solder and tin plated terminations. Does not apply to gold terminations.	All terminations shall exhibit a continuous solder coating free from defects for a minimum of 95 % of the critical area of any individual lead.			
Resistance to solvent	MIL-STD-202, method 215	Marking has to remain le encapsulation material.	egible, no degradation of		

Guide for Leadframeless Molded Tantalum Capacitors

INTRODUCTION

Tantalum electrolytic capacitors are the preferred choice in applications where volumetric efficiency, stable electrical parameters, high reliability, and long service life are primary considerations. The stability and resistance to elevated temperatures of the tantalum / tantalum oxide / manganese dioxide system make solid tantalum capacitors an appropriate choice for today's surface mount assembly technology.

Vishay Sprague has been a pioneer and leader in this field, producing a large variety of tantalum capacitor types for consumer, industrial, automotive, military, and aerospace electronic applications.

Tantalum is not found in its pure state. Rather, it is commonly found in a number of oxide minerals, often in combination with Columbium ore. This combination is known as "tantalite" when its contents are more than one-half tantalum. Important sources of tantalite include Australia, Brazil, Canada, China, and several African countries. Synthetic tantalite concentrates produced from tin slags in Thailand, Malaysia, and Brazil are also a significant raw material for tantalum production.

Electronic applications, and particularly capacitors, consume the largest share of world tantalum production. Other important applications for tantalum include cutting tools (tantalum carbide), high temperature super alloys, chemical processing equipment, medical implants, and military ordnance.

Vishay Sprague is a major user of tantalum materials in the form of powder and wire for capacitor elements and rod and sheet for high temperature vacuum processing.

THE BASICS OF TANTALUM CAPACITORS

Most metals form crystalline oxides which are non-protecting, such as rust on iron or black oxide on copper. A few metals form dense, stable, tightly adhering, electrically insulating oxides. These are the so-called "valve" metals and include titanium, zirconium, niobium, tantalum, hafnium, and aluminum. Only a few of these permit the accurate control of oxide thickness by electrochemical means. Of these, the most valuable for the electronics industry are aluminum and tantalum.

Capacitors are basic to all kinds of electrical equipment, from radios and television sets to missile controls and automobile ignitions. Their function is to store an electrical charge for later use.

Capacitors consist of two conducting surfaces, usually metal plates, whose function is to conduct electricity. They are separated by an insulating material or dielectric. The dielectric used in all tantalum electrolytic capacitors is tantalum pentoxide.

Tantalum pentoxide compound possesses high-dielectric strength and a high-dielectric constant. As capacitors are being manufactured, a film of tantalum pentoxide is applied to their electrodes by means of an electrolytic process. The film is applied in various thicknesses and at various voltages and although transparent to begin with, it takes on different colors as light refracts through it. This coloring occurs on the tantalum electrodes of all types of tantalum capacitors.

Rating for rating, tantalum capacitors tend to have as much as three times better capacitance / volume efficiency than aluminum electrolytic capacitors. An approximation of the capacitance / volume efficiency of other types of capacitors may be inferred from the following table, which shows the dielectric constant ranges of the various materials used in each type. Note that tantalum pentoxide has a dielectric constant of 26, some three times greater than that of aluminum oxide. This, in addition to the fact that extremely thin films can be deposited during the electrolytic process mentioned earlier, makes the tantalum capacitor extremely efficient with respect to the number of microfarads available per unit volume. The capacitance of any capacitor is determined by the surface area of the two conducting plates, the distance between the plates, and the dielectric constant of the insulating material between the plates.

COMPARISON OF CAPACITOR DIELECTRIC CONSTANTS					
DIELECTRIC	e DIELECTRIC CONSTANT				
Air or Vacuum	1.0				
Paper	2.0 to 6.0				
Plastic	2.1 to 6.0				
Mineral Oil	2.2 to 2.3				
Silicone Oil	2.7 to 2.8				
Quartz	3.8 to 4.4				
Glass	4.8 to 8.0				
Porcelain	5.1 to 5.9				
Mica	5.4 to 8.7				
Aluminum Oxide	8.4				
Tantalum Pentoxide	26				
Ceramic	12 to 400K				

In the tantalum electrolytic capacitor, the distance between the plates is very small since it is only the thickness of the tantalum pentoxide film. As the dielectric constant of the tantalum pentoxide is high, the capacitance of a tantalum capacitor is high if the area of the plates is large:

$$C = \frac{eA}{t}$$

where

C = capacitance

e = dielectric constant

A = surface area of the dielectric

t = thickness of the dielectric

Tantalum capacitors contain either liquid or solid electrolytes. In solid electrolyte capacitors, a dry material (manganese dioxide) forms the cathode plate. A tantalum lead is embedded in or welded to the pellet, which is in turn connected to a termination or lead wire. The drawings show the construction details of the surface mount types of tantalum capacitors shown in this catalog.

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SOLID ELECTROLYTE TANTALUM CAPACITORS

Solid electrolyte capacitors contain manganese dioxide, which is formed on the tantalum pentoxide dielectric layer by impregnating the pellet with a solution of manganous nitrate. The pellet is then heated in an oven, and the manganous nitrate is converted to manganese dioxide.

The pellet is next coated with graphite, followed by a layer of metallic silver, which provides a conductive surface between the pellet and the leadframe.

Molded chip tantalum capacitor encases the element in plastic resins, such as epoxy materials. The molding compound has been selected to meet the requirements of UL 94 V-0 and outgassing requirements of ASTM E-595. After assembly, the capacitors are tested and inspected to assure long life and reliability. It offers excellent reliability and high stability for consumer and commercial electronics with the added feature of low cost.

Surface mount designs of "Solid Tantalum" capacitors use lead frames or lead frameless designs as shown in the accompanying drawings.

TANTALUM CAPACITORS FOR ALL DESIGN CONSIDERATIONS

Solid electrolyte designs are the least expensive for a given rating and are used in many applications where their very small size for a given unit of capacitance is of importance. They will typically withstand up to about 10 % of the rated DC working voltage in a reverse direction. Also important are their good low temperature performance characteristics and freedom from corrosive electrolytes.

Vishay Sprague patented the original solid electrolyte capacitors and was the first to market them in 1956. Vishay Sprague has the broadest line of tantalum capacitors and has continued its position of leadership in this field. Data sheets covering the various types and styles of Vishay Sprague capacitors for consumer and entertainment electronics, industry, and military applications are available where detailed performance characteristics must be specified.

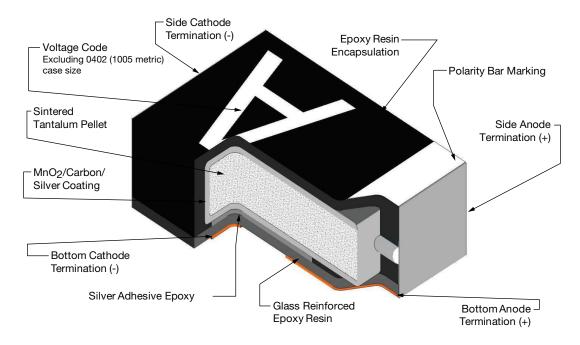


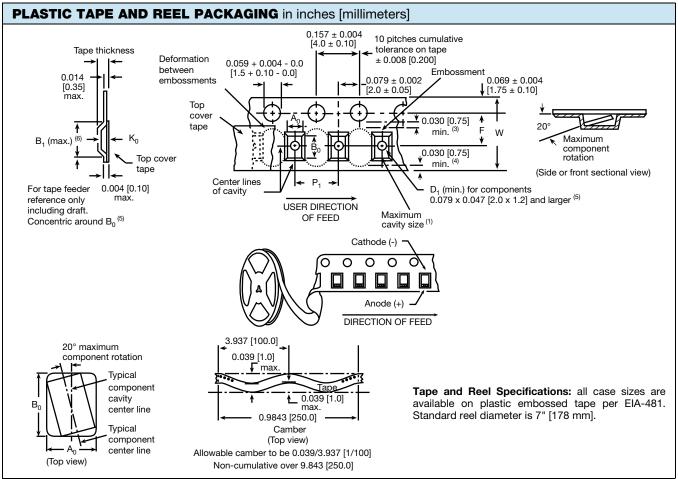
Fig. 1 - Leadframeless Molded Capacitors, All Types



SOLID TANTALUM CA	SOLID TANTALUM CAPACITORS - LEADFRAMELESS MOLDED							
SERIES	TL8	TL8 298D 298W TR8						
PRODUCT IMAGE				96				
TYPE		Solid tantalum leadframele	ess molded chip capacitors	•				
	Small size including 0603 and 0402 foot print							
FEATURES	Ultra low profile	Industrial grade	Industrial grade, extended range	Low ESR				
TEMPERATURE RANGE	Operating Temperature: -55 °C to +125 °C (above 40 °C, voltage derating is required)	Operating Temperature: -55 °C to +125 °C (above 85 °C, voltage derating is required)	Operating Temperature: -55 °C to +125 °C (above 40 °C, voltage derating is required)	Operating Temperature: -55 °C to +125 °C (above 85 °C, voltage derating is required)				
CAPACITANCE RANGE	0.68 μF to 220 μF	0.33 μF to 220 μF	2.2 μF to 220 μF	1 μF to 220 μF				
VOLTAGE RANGE	4 V to 25 V	2.5 V to 50 V	4 V to 16 V	2.5 V to 25 V				
CAPACITANCE TOLERANCE	APACITANCE TOLERANCE ± 20 %, ± 10 %							
DISSIPATION FACTOR	6 % to 80 %	6 % to 80 %	30 % to 80 %	6 % to 80 %				
CASE CODES	W9, A0, B0	K, M, R, P, Q, A, S, B	K, M, Q	M, R, P, Q, A, B				
TERMINATION	100 % tin		100 % tin or gold plated					

SOLID TANTALUM CAPACITORS - LEADFRAMELESS MOLDED						
SERIES	TP8	28 TM8 DLA 110				
PRODUCT IMAGE	66		96			
TYPE	Solid tantalum leadframeless molded chip capacitors					
	Small size including 0603 and 0402 foot print					
FEATURES	High performance, automotive grade High reliability		High reliability, DLA approved			
TEMPERATURE RANGE	Operating Temperature: -55 °C to +125 °C (above 85 °C, voltage derating is required)					
CAPACITANCE RANGE	1 μF to 100 μF	1 μF to 100 μF 0.68 μF to 47 μF 1 μF to				
VOLTAGE RANGE	6.3 V to 40 V	6.3 V to 40 V 2 V to 40 V				
CAPACITANCE TOLERANCE	± 20 %, ± 10 %					
DISSIPATION FACTOR	6 % to 30 %	6 % to 30 % 6 % to 20 % 6 % to				
CASE CODES	M, W, R, P, A, N, T, B	K, M, G, W, R, P, A, N, T	M, W, R, P, A, N, T			
TERMINATION	100 % tin	Tin / lead solder plated, 100 % tin and gold plated	Tin / lead solder plated or gold plated			





Notes

- Metric dimensions will govern. Dimensions in inches are rounded and for reference only
- A₀, B₀, K₀, are determined by the maximum dimensions to the ends of the terminals extending from the component body and / or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A₀, B₀, K₀) must be within 0.002" (0.05 mm) minimum and 0.020" (0.50 mm) maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20°
- Tape with components shall pass around radius "R" without damage. The minimum trailer length may require additional length to provide 'R" minimum for 12 mm embossed tape for reels with hub diameters approaching N minimum
- This dimension is the flat area from the edge of the sprocket hole to either outward deformation of the carrier tape between the embossed cavities or to the edge of the cavity whichever is less

 This dimension is the flat area from the edge of the carrier tape opposite the sprocket holes to either the outward deformation of the carrier
- tape between the embossed cavity or to the edge of the cavity whichever is less
- The embossed hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location shall be applied independent of each other
- B₁ dimension is a reference dimension tape feeder clearance only

CARRIER TAPE DIMENSIONS in inches [millimeters] FOR 298D, 298W, TR8, TP8, TL8							
CASE CODE	TAPE SIZE	B ₁ (MAX.) ⁽¹⁾	D ₁ (MIN.)	F	K ₀ (MAX.)	P ₁	W
M ⁽²⁾	8 mm	0.075 [1.91]	0.02 [0.5]	0.138 [3.5]	0.043 [1.10]	0.157 [4.0]	0.315 [8.0]
W	8 mm	0.112 [2.85]	0.039 [1.0]	0.138 [3.5]	0.053 [1.35]	0.157 [4.0]	0.315 [8.0]
R	8 mm	0.098 [2.46]	0.039 [1.0]	0.138 [3.5]	0.066 [1.71]	0.157 [4.0]	0.315 [8.0]
Р	8 mm	0.108 [2.75]	0.02 [0.5]	0.138 [3.5]	0.054 [1.37]	0.157 [4.0]	0.315 [8.0]
Α	8 mm	0.153 [3.90]	0.039 [1.0]	0.138 [3.5]	0.078 [2.00]	0.157 [4.0]	0.315 [8.0]
A0, Q	8 mm	-	0.02 [0.5]	0.138 [3.5]	0.049 [1.25]	0.157 [4.0]	0.315 [8.0]
В	8 mm	0.157 [4.0]	0.039 [1.0]	0.138 [3.5]	0.087[2.22]	0.157 [4.0]	0.315 [8.0]
W9, S	8 mm	0.126 [3.20]	0.029 [0.75]	0.138 [3.5]	0.045 [1.15]	0.157 [4.0]	0.315 [8.0]
B0	12 mm	0.181 [4.61]	0.059 [1.5]	0.217 [5.5]	0.049 [1.25]	0.157 [4.0]	0.472 [12.0]

Notes

- For reference only
- (2) Packaging of M case in plastic tape is available per request

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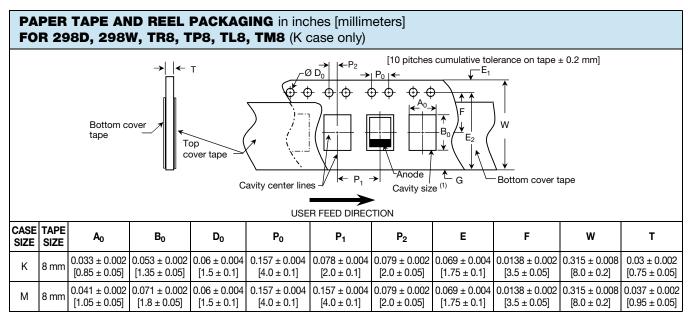


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CARRIER TAPE DIMENSIONS in inches [millimeters] FOR TM8							
CASE CODE	TAPE SIZE	B ₁ (MAX.) ⁽¹⁾	D ₁ (MIN.)	F	K ₀ (MAX.)	P ₁	W
М	8 mm	0.075 [1.91]	0.02 [0.5]	0.138 [3.5]	0.043 [1.10]	0.157 [4.0]	0.315 [8.0]
G	8 mm	0.077 [1.96]	0.02 [0.5]	0.138 [3.5]	0.051 [1.30]	0.157 [4.0]	0.315 [8.0]
W	8 mm	0.112 [2.85]	0.039 [1.0]	0.138 [3.5]	0.053 [1.35]	0.157 [4.0]	0.315 [8.0]
R	8 mm	0.098 [2.46]	0.039 [1.0]	0.138 [3.5]	0.066 [1.71]	0.157 [4.0]	0.315 [8.0]
Р	8 mm	0.108 [2.75]	0.02 [0.5]	0.138 [3.5]	0.054 [1.37]	0.157 [4.0]	0.315 [8.0]
Α	8 mm	0.153 [3.90]	0.039 [1.0]	0.138 [3.5]	0.078 [2.00]	0.157 [4.0]	0.315 [8.0]
N	12 mm	0.154 [3.90]	0.059 [1.5]	0.216 [5.5]	0.051 [1.30]	0.157 [4.0]	0.472 [12.0]
Т	12 mm	0.154 [3.90]	0.059 [1.5]	0.216 [5.5]	0.067 [1.70]	0.157 [4.0]	0.472 [12.0]

Notes

⁽¹⁾ For reference only



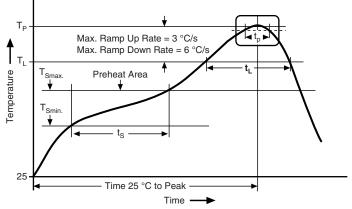
Note

⁽¹⁾ A₀, B₀ are determined by the maximum dimensions to the ends of the terminals extending from the component body and / or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A₀, B₀) must be within 0.002" (0.05 mm) minimum and 0.020" (0.50 mm) maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20°

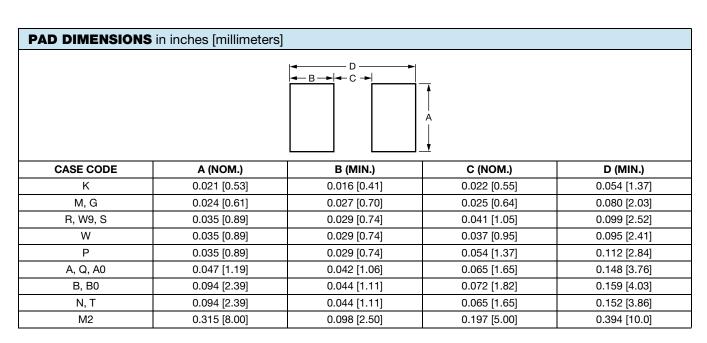
RECOMMENDED REFLOW PROFILES

Vishay Sprague

Capacitors should withstand reflow profile as per J-STD-020 standard, three cycles. Tp Max. Ramp Up Rate = 3 °C/s Max. Ramp Down Rate = 6 °C/s Preheat Area Preheat Area

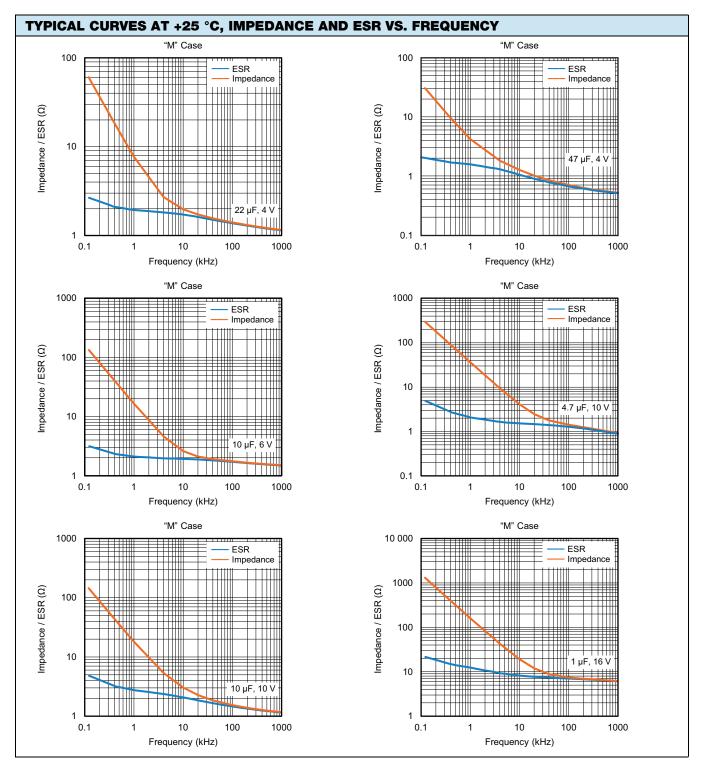


PROFILE FEATURE	SnPb EUTECTIC ASSEMBLY	LEAD (Pb)-FREE ASSEMBLY	
PREHEAT AND SOAK			
Temperature min. (T _{Smin.})	100 °C	150 °C	
Temperature max. (T _{Smax.})	150 °C	200 °C	
Time (t _S) from (T _{Smin.} to T _{Smax.})	60 s to 90 s	60 s to 150 s	
RAMP UP			
Ramp-up rate (T _L to T _p)	3 °C/s maximum		
Liquidus temperature (T _L)	183 °C	217 °C	
Time (t _L) maintained above T _L	60 s to 150 s		
Peak package body temperature (Tp) max.	235 °C	260 °C	
Time (t _p) within 5 °C of the peak max. temperature	20 s	30 s	
RAMP DOWN			
Ramp-down rate (T _p to T _L)	6 °C/s maximum		
Time from 25 °C to peak temperature	m 25 °C to peak temperature 6 min maximum 8 min maxi		

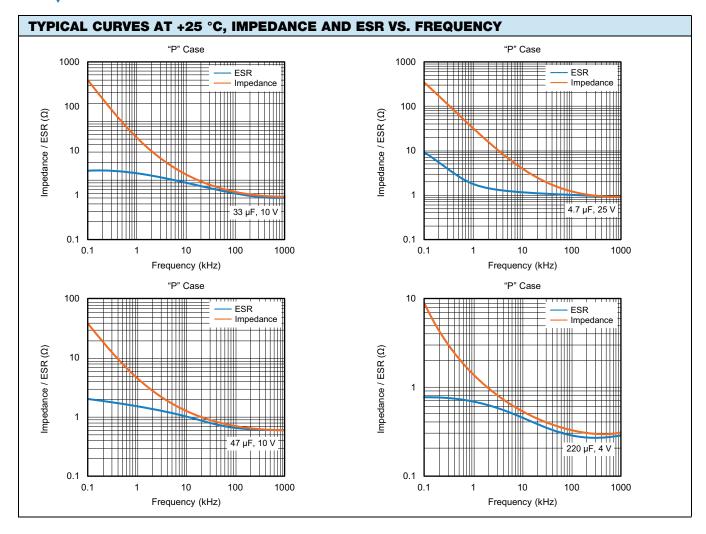


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GUIDE TO APPLICATION

 AC Ripple Current: the maximum allowable ripple current shall be determined from the formula:

$$I_{RMS} = \sqrt{\frac{P}{R_{ESR}}}$$

where,

P = power dissipation in watts at +25 °C (see paragraph number 5 and the table Power Dissipation as given in the tables in the product datasheets)

R_{ESR} = the capacitor equivalent series resistance at the specified frequency

2. **AC Ripple Voltage:** the maximum allowable ripple voltage shall be determined from the formula:

$$V_{RMS} = Z \sqrt{\frac{P}{R_{ESR}}}$$

or, from the formula:

$$V_{RMS} = I_{RMS} \times Z$$

where.

P = power dissipation in watts at +25 °C (see paragraph number 5 and the table Power Dissipation as given in the tables in the product datasheets)

R_{ESR} = the capacitor equivalent series resistance at the specified frequency

Z = the capacitor impedance at the specified frequency

- 2.1 The sum of the peak AC voltage plus the applied DC voltage shall not exceed the DC voltage rating of the capacitor.
- 2.2 The sum of the negative peak AC voltage plus the applied DC voltage shall not allow a voltage reversal exceeding 10 % of the DC working voltage at +25 °C.
- 3. **Reverse Voltage:** these capacitors are capable of withstanding peak voltages in the reverse direction equal to 10 % of the DC rating at +25 °C, 5 % of the DC rating at +25 °C, 5 % of the DC rating at +85 °C, and 1 % of the DC rating at +125 °C.
- 4. **Temperature Derating:** if these capacitors are to be operated at temperatures above +25 °C, the permissible RMS ripple current shall be calculated using the derating factors as shown:

TEMPERATURE	DERATING FACTOR		
+25 °C	1.0		
+85 °C	0.9		
+125 °C	0.4		

5. Power Dissipation: power dissipation will be affected by the heat sinking capability of the mounting surface. Non-sinusoidal ripple current may produce heating effects which differ from those shown. It is important that the equivalent I_{RMS} value be established when calculating permissible operating levels. (Power Dissipation calculated using +25 °C temperature rise.)

- Printed Circuit Board Materials: molded capacitors are compatible with commonly used printed circuit board materials (alumina substrates, FR4, FR5, G10, PTFE-fluorocarbon and porcelanized steel).
- 7. Attachment:
- 7.1 **Solder Paste:** the recommended thickness of the solder paste after application is 0.007" ± 0.001" [0.178 mm ± 0.025 mm]. Care should be exercised in selecting the solder paste. The metal purity should be as high as practical. The flux (in the paste) must be active enough to remove the oxides formed on the metallization prior to the exposure to soldering heat. In practice this can be aided by extending the solder preheat time at temperatures below the liquidous state of the solder.
- 7.2 **Soldering:** capacitors can be attached by conventional soldering techniques; vapor phase, convection reflow, infrared reflow, wave soldering and hot plate methods. The Soldering Profile charts show recommended time / temperature conditions for soldering. Preheating is recommended. The recommended maximum ramp rate is 3 °C per second. Attachment with a soldering iron is not recommended due to the difficulty of controlling temperature and time at temperature. The soldering iron must never come in contact with the capacitor. For details see www.vishay.com/doc?40214.
- 7.2.1 Backward and Forward Compatibility: capacitors with SnPb or 100 % tin termination finishes can be soldered using SnPb or lead (Pb)-free soldering processes.
- 8. Cleaning (Flux Removal) After Soldering: molded capacitors are compatible with all commonly used solvents such as TES, TMS, Prelete, Chlorethane, Terpene and aqueous cleaning media. However, CFC / ODS products are not used in the production of these devices and are not recommended. Solvents containing methylene chloride or other epoxy solvents should be avoided since these will attack the epoxy encapsulation material.
- 8.1 When using ultrasonic cleaning, the board may resonate if the output power is too high. This vibration can cause cracking or a decrease in the adherence of the termination. DO NOT EXCEED 9W/I at 40 kHz for 2 min.
- 9. Recommended Mounting Pad Geometries: proper mounting pad geometries are essential for successful solder connections. These dimensions are highly process sensitive and should be designed to minimize component rework due to unacceptable solder joints. The dimensional configurations shown are the recommended pad geometries for both wave and reflow soldering techniques. These dimensions are intended to be a starting point for circuit board designers and may be fine tuned if necessary based upon the peculiarities of the soldering process and / or circuit board design.

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