

ROHS COMPLIANT

HALOGEN

FREE GREEN

(5-2008)

# Surface Mount Multilayer Ceramic Chip Capacitors Array for Commodity Applications



### FEATURES

- High density mounting due to mounting space saving
- Mounting cost saving
- Increased throughput
- Dry sheet manufacturing technology
- Base Metal Electrode system (BME)
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- For use as a bypass for digital and analog signal line noise
- Computer motherboards and peripherals
- The common electronic circuits

ELECTRICAL SPECIFICATION					
Size	4 x 0603				
Dielectric	C0G (NP0)	X7R	Y5V		
Capacitance <sup>(1)</sup>	10 pF to 470 pF	180 pF to 100 nF	10 nF to 100 nF		
Capacitance Tolerance <sup>(2)</sup>	J (± 5 %), K (± 10 %)	K (± 10 %), M (± 20 %)	M (± 20 %) Z (-20 %/+80 %)		
Rated Voltage (V <sub>DC</sub> )	50 V	16 V, 50 V	50 V		
tan δ/Q <sup>(1)</sup>	Cap. < 30 pF: Q ≥ 400 + 20 C Cap. ≥ 30 pF: Q ≥ 1000	$\begin{array}{l} U_{R} = 50 \; V\!\!: \le 2.5 \; \% \\ U_{R} = 16 \; V\!\!: \le 3.5 \; \% \end{array}$	$\leq 5$ %		
Insulation Resistance at U <sub>R</sub>	$\geq 10 \ \text{G}\Omega \qquad \geq 10 \ \text{G}\Omega \text{ or } \text{R x } \text{C} \geq 500 \ \Omega \text{ x } \text{F, whichever is less}$				
Operating Temperature	-55 °C to +125 °C -25 °C to +85 °C				
Capacitance Change	± 30 ppm	± 15 %	+30 %/-80 %		
Termination	Ni/Sn lead (Pb)-free termination				

#### Notes

 $^{(1)}$  Measured at 30 % ~ 70 % related humidity

NP0: apply 1.0 V<sub>RMS</sub> ± 0.2 V<sub>RMS</sub>, 1.0 MHz ± 10 % at the conditions of 25 °C ambient temperature X7R: apply 1.0 V<sub>RMS</sub> ± 0.2 V<sub>RMS</sub>, 1.0 kHz ± 10 % at the conditions of 25 °C ambient temperature Y5V: apply 1.0 V<sub>RMS</sub> ± 0.2 V<sub>RMS</sub>, 1.0 kHz ± 10 % at the conditions of 20 °C ambient temperature

(2) Preconditioning for X7R, Y5V MLCC: Perform a heat treatment at 150 °C ± 10 °C for 1 h, then leave in ambient condition for 24 h ± 2 h before measurement.



QUICK REFERENCE DATA					
	DIELECTRIC CASE	MAXIMUM VOLTAGE (V)	CAPACITANCE		
DIELECTRIC			MINIMUM	MAXIMUM	
C0G (NP0)	0612	50	10 pF	470 pF	
X7R	0612	50	180 pF	100 nF	
Y5V	0612	50	10 nF	100 nF	

Note

• Detail ratings see "Selection Chart"

ORDERING INFORMATION							
VJ06C4	Α	100	J	Х	Α	С	W1BC
SIZE CODE I 06C4	DIELECTRIC A = COG (NP0) Y = X7R V = Y5V	CAPACITANCE Two significant digits followed by the number of zeros. R is in place of decimal point: 100 = 10 pF 101 = 100 pF	TOLERANCE J = ± 5 % K = ± 10 % M = ± 20 % Z = -20 %/ +80 %	TERMINATION X = Ni Barrier	RATED VOLTAGE J J = 16 V A = 50 V	PACKAGING C = 7" reel/ paper tape	PROCESS CODE FOR BASIC COMMODITY

DIMENSIONS in inches (millimeters)						
SIZE CODE L W T S BW P						
0612 (1632)	0.125 ± 0.006 (3.20 ± 0.15)	0.063 ± 0.006 (1.60 ± 0.15)	0.036 (0.90)	0.012 ± 0.008 (0.30 ± 0.20)	0.016 ± 0.006 (0.40 ± 0.15)	0.031 ± 0.006 (0.80 ± 0.15)



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SELECTION CHART						
DIELECTRIC		COG (NPO) X7R Y5V				
STYLE		VJ06C4			•	
SIZE CODE		0612 (4 x 0603)				
VOLTAGE V <sub>DC</sub>		50 V 16 V 50 V			50 V	
VOLTAGE COD	E	A	J	Α	Α	
CAP. CODE	CAP.					
100	10 pF	В				
150	15 pF	В				
220	22 pF	В				
330	33 pF	В				
470	47 pF	В				
680	68 pF	В				
101	100 pF	В				
151	150 pF	В				
181	180 pF	В		В		
221	220 pF	В		В		
271	270 pF	В		В		
331	330 pF	В		В		
391	390 pF	В				
471	470 pF	В		В		
102	1.0 nF			В		
152	1.5 nF			В		
222	2.2 nF			В		
332	3.3 nF			В		
472	4.7 nF			В		
682	6.8 nF			В		
103	10 nF			В	В	
153	15 nF		В	В	В	
223	22 nF		В	В	В	
333	33 nF		В		В	
473	47 nF		В		В	
683	68 nF		В			
104	100 nF		В		В	

Note

• Letters indicate product thickness, see "Packaging Quantities"

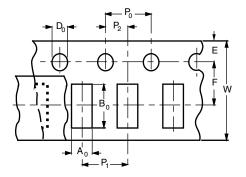
Document Number: 28539



#### PACKAGING QUANTITIES

SIZE CODE	THICKNESS	PAPEF	R TAPE		
	(mm)	7" REEL (C)	13" REEL (P)		
06C4 (4 x 0603)	0.80 ± 0.10	4K	-		

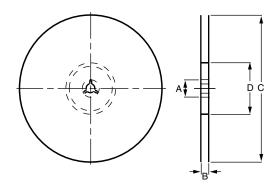
#### PAPER TAPE SPECIFICATIONS



### DIMENSIONS OF PAPER TAPE

in millimeters	
SYMBOL	PRODUCT SIZE CODE
STMBOL	06C4 (4 x 0603)
A <sub>0</sub>	2.00 ± 0.10
B <sub>0</sub>	3.50 ± 0.10
W	8.00 ± 0.10
E	1.75 ± 0.05
F	3.50 ± 0.05
D <sub>0</sub>	1.50 ± 0.05
P <sub>0</sub>	4.00 ± 0.10
P <sub>1</sub>	4.00 ± 0.10
P <sub>2</sub>	2.00 ± 0.05

#### **REEL SPECIFICATION**



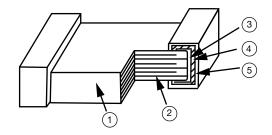
REEL DIMENSIONS AND TAPE WIDTH in millimeters				
SYMBOL	Ø 180 mm; 7"	Ø 330 mm; 13"		
А	$13.0 \pm 0.5$	$13.0 \pm 0.5$		
В	9.0 ± 1.0	9.0 ± 1.0		
С	178.0 ± 1.0	330.0 ± 1.0		
D	60.0 ± 1.0	100.0 ± 1.0		

4 For technical questions, contact: <u>mlcc@vishay.com</u>

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CONSTRUCTION					
NO.	NAME		C0G (NP0), X7R, Y5V		
1	Ceramic	material	BaTiO <sub>3</sub> based		
2	Inner electrode		Ni		
3		Inner layer	Cu		
4	Termination	Middle layer	Ni		
5		Outer layer	Sn (matt)		
	•				



#### **STORAGE AND HANDLING CONDITIONS**

- (1) To store products at 5 °C to 40 °C ambient temperature and 20 % to 70 % relative humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- a. Do not store products in a corrosive environment such as sulfide, chloride gas, or acid. It may cause oxidization of electrode, which easily be resulted in poor soldering.
- b. To store products on the shelf and avoid exposure to moisture.
- c. Do not expose products to excessive shock, vibration, direct sunlight and so on.



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