DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| BV _{DSS} | Rds(ON) Max | ID MAX T _A = +25°C |
|-------------------|---------------------------------------|----------------------------------|
| -20V | $70m\Omega$ @ V _{GS} = -4.5V | -3.8A |
| -200 | $85m\Omega @ V_{GS} = -2.5V$ | -3.3A |

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load switches
- Power management functions
- Portable power adaptors

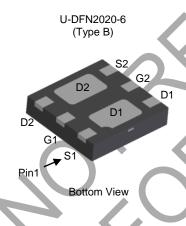
Features

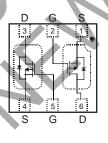
- Low On-Resistance
- Low Gate Threshold Voltage, -0.9V Max
- · Fast Switching Speed
- Low Input/Output Leakage
- Low Profile, 0.5mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMP2160UFDBQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

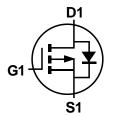
Mechanical Data

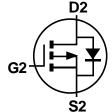
- Package: U-DFN2020-6
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
 - Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (Approximate)











Q1 P-CHANNEL

Q2 P-CHANNEL

Internal Schematic

Ordering Information (Note 4)

| ▼ | | | | | | | | |
|----------------|----------------------|------|-------------|--|--|--|--|--|
| Part Number | Pookage | Pac | Packing | | | | | |
| Fait Number | Package | Qty. | Carrier | | | | | |
| DMP2160UFDBQ-7 | U-DFN2020-6 (Type B) | 3000 | Tape & Reel | | | | | |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

February 2022

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Marking Information



P2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 1 = January) Dot denotes Pin 1

Date Code Key

| Year | 2015 | | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 |
|-------|------|-----|------|------|------|------|------|------|------|------|------|------|
| Code | С | | J | K | L | М | N | 0 | Р | R | S | Т |
| | | | | | | | | | | | | |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Units |
|-------------------------------|------------------|-------|-------|
| Drain-Source Voltage | VDSS | -20 | V |
| Gate-Source Voltage | V _{GSS} | ±12 | V |
| Drain Current (Note 5) | lo | -3.8 | A |
| Pulsed Drain Current (Note 6) | IDM | -13 | A |

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Unit |
|---|--|-------------------|-------------|------|
| Power Dissipation (Note 5) | | P _D | 1.4 | W |
| Thermal Resistance, Junction to Ambient | | R _θ JA | 89 | °C/W |
| Operating and Storage Temperature Range | | TJ, TSTG | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

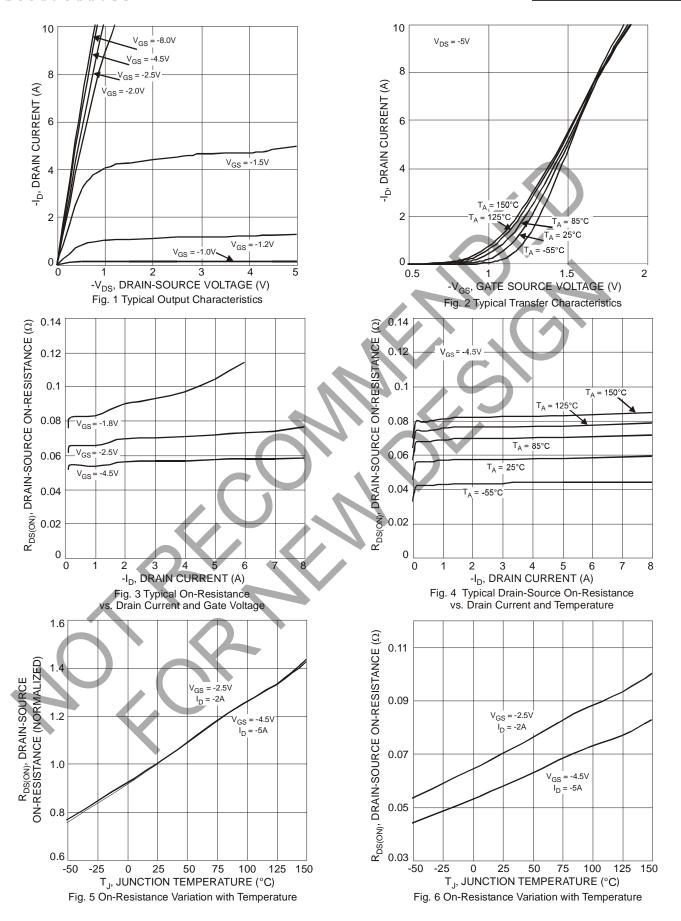
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|-----------------------------------|---------------------|-------|-------|------|-----------|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | |
| Drain-Source Breakdown Voltage | BVDSS | -20 | | — | V | $V_{GS} = 0V, I_{D} = -250\mu A$ |
| Zero Gate Voltage Drain Current | IDSS | _ | _ | -1 | μA | $V_{DS} = -20V, V_{GS} = 0V$ |
| Gate-Source Leakage | lana | _ | _ | ±100 | nA | $V_{GS} = \pm 8V$, $V_{DS} = 0V$ |
| | Igss | _ | | ±800 | ПА | $V_{GS} = \pm 12V, V_{DS} = 0V$ |
| ON CHARACTERISTICS (Note 7) | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | -0.45 | | -0.9 | V | $V_{DS} = V_{GS}$, $I_D = -250\mu A$ |
| | | | 54 | 70 | | $V_{GS} = -4.5V$, $I_D = -2.8A$ |
| Static Drain-Source On-Resistance | RDS(ON) | | 68 | 85 | $m\Omega$ | $V_{GS} = -2.5V$, $I_{D} = -2.0A$ |
| | | _ | 86 | _ | | $V_{GS} = -1.8V, I_D = -1.0A$ |
| Forward Transfer Admittance | Y _{fs} | _ | 8 | _ | S | V _{DS} = -5V, I _D = -2.8A |
| Diode Forward Voltage (Note 7) | V_{SD} | _ | 0.7 | -1.2 | V | $V_{GS} = 0V, I_{S} = -1.6A$ |
| DYNAMIC CHARACTERISTICS | | | | | | |
| Input Capacitance | Ciss | | 536 | _ | pF | 101111 |
| Output Capacitance | Coss | _ | 68 | — | рF | V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz |
| Reverse Transfer Capacitance | Crss | | 59 | _ | pF | 1 – 1.0101112 |
| Gate Resistance | R_g | | 8.72 | _ | Ω | $V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$ |
| Total Gate Charge | Qg | _ | 6.5 | _ | nC | VGS = -4.5V. VDD = -10V. |
| Gate-Source Charge | Qgs | | 8.0 | _ | nC | VGS = -4.5V, VDD = -10V, In = -1.5A |
| Gate-Drain Charge | Q_{gd} | | 1.4 | _ | nC | ID = -1.5A |
| Turn-On Delay Time | t _{D(on)} | _ | 11.51 | _ | ns | |
| Turn-On Rise Time | | _ | 12.09 | _ | ns | $V_{GEN} = -4.5V$, $V_{DD} = -10V$, |
| Turn-Off Delay Time | | | 55.34 | | ns | $R_L = 10\Omega$, $R_G = 6\Omega$ |
| Turn-Off Fall Time | t _f | _ | 27.54 | _ | ns | |

Notes: 5. Device mounted on FR-4 PCB, on minimum recommended 2oz Copper pad layout.

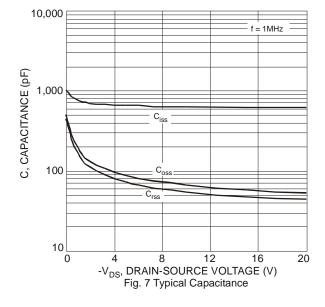
^{6.} Repetitive rating, pulse width limited by junction temperature.

^{7.} Short duration pulse test used to minimize self-heating effect.









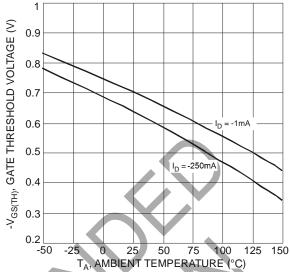
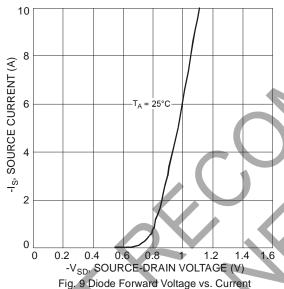


Fig. 8 Gate Threshold Variation vs. Ambient Temperature



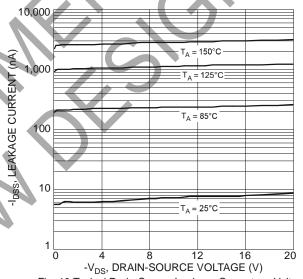


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage

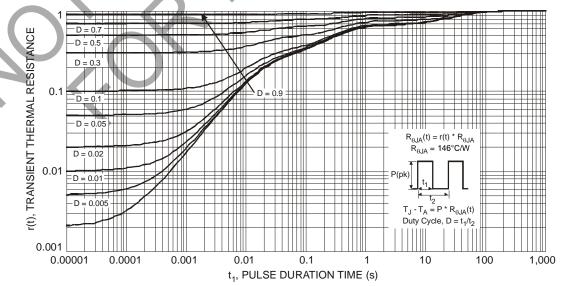


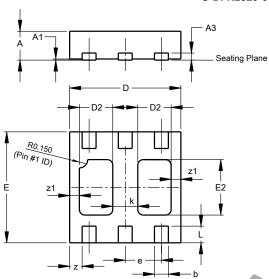
Fig. 11 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)

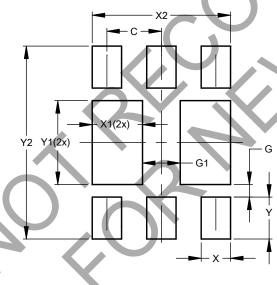


| U-DFN2020-6 | | | | | | | | | |
|----------------------|-------------|-------|-------|--|--|--|--|--|--|
| | Type B | | | | | | | | |
| Dim | Min Max Typ | | | | | | | | |
| Α | 0.545 | 0.605 | 0.575 | | | | | | |
| A1 | 0.00 | 0.05 | 0.02 | | | | | | |
| A3 | - | 1 | 0.13 | | | | | | |
| b | 0.20 | 0.30 | 0.25 | | | | | | |
| D | 1.95 | 2.075 | 2.00 | | | | | | |
| D2 | 0.50 | 0.70 | 0.60 | | | | | | |
| е | | | 0.65 | | | | | | |
| E | 1.95 | 2.075 | 2.00 | | | | | | |
| E2 | 0.90 | 1.10 | 1.00 | | | | | | |
| k | | 1 | 0.45 | | | | | | |
| 4 | 0.25 | 0.35 | 0.30 | | | | | | |
| Z |) | - | 0.225 | | | | | | |
| z1 | - | 1 | 0.175 | | | | | | |
| All Dimensions in mm | | | | | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)



| Dimensions | Value (in mm) |
|------------|------------------|
| С | 0.650 |
| G | 0.150 |
| G1 | 0.450 |
| Χ | 0.350 |
| X1 | 0.600 |
| X2 | 1.650 |
| Υ | 0.500 |
| Y1 | 1.000 |
| Y2 | 2.300 |



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