



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C	
60V	$16m\Omega @ V_{GS} = 10V$	9.2A	
000	$21m\Omega @ V_{GS} = 4.5V$	7.5A	

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load Switch
- Adaptor Switch
- Notebook PC

Features and Benefits

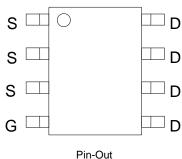
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

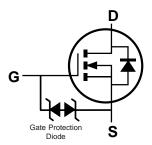
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.076 grams (Approximate)



Top View



Pin-Out Top View



Equivalent Circuit

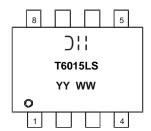
Ordering Information (Note 4)

Part Number	Case	Packaging	
DMT6015LSS-13	SO-8	2,500/Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



⊃¦¦ = Manufacturer's Marking T6015LS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 16 = 2016) WW = Week (01 - 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±16	V
Continuous Durin Compart (Note C) // 40)/	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	9.2 7.4	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	11.9 9.5	А
Continuous Drain Current (Note 6) V 4 FV	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	7.5 6.0	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	9.7 7.7	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	60	Α
Maximum Continuous Body Diode Forward Current (Note 6)			Is	2	Α
Avalanche Current, L = 0.1mH			I _{AS}	15	Α
Avalanche Energy, L = 0.1mH			Eas	11	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P_D	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		85	°C/W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	45	°C/W
Total Power Dissipation (Note 6)		P_D	2.1	W
Thermal Resistance, Junction to Ambient (Note 6) Stead		$R_{\theta JA}$	74	°C/W
			37	°C/W
Thermal Resistance, Junction to Case		$R_{ heta JC}$	13	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	0.5		2.5	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		-	12.4	16	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Dialii-Source Ori-Nesistance	R _{DS(ON)}	_	15.8	21	11122	VGS = 4.5V, ID = 6A	
Diode Forward Voltage	V_{SD}		0.7	1.2	V	$V_{GS} = 0V$, $I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{ISS}	_	1,103			V _{DS} = 30V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	251	_	pF		
Reverse Transfer Capacitance	C _{RSS}	-	20			1 – 1101112	
Gate Resistance	Rg	-	1.5		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_{G}	_	8.9	_			
Total Gate Charge (V _{GS} = 10V)	Q_{G}		18.9		~C	201/ 1 404	
Gate-Source Charge	Q _{GS}	_	3.0	_	nC	$V_{DS} = 30V, I_{D} = 10A$	
Gate-Drain Charge	Q_{GD}		2.8				
Turn-On Delay Time	t _{D(ON)}	_	4.1	_			
Turn-On Rise Time	t _R	_	7.1	_	20	$V_{GS} = 10V, V_{DS} = 30V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	19.5	-	ns	$R_G = 6\Omega$, $I_D = 10A$	
Turn-Off Fall Time	t _F	-	8.6	-			
Reverse Recovery Time	T _{RR}	_	21.2	_	ns	101 11/11 1001/1-	
Reverse Recovery Charge	Q _{RR}	_	13.2	_	nC	$I_F = 10A$, di/dt = 100A/ μ s	

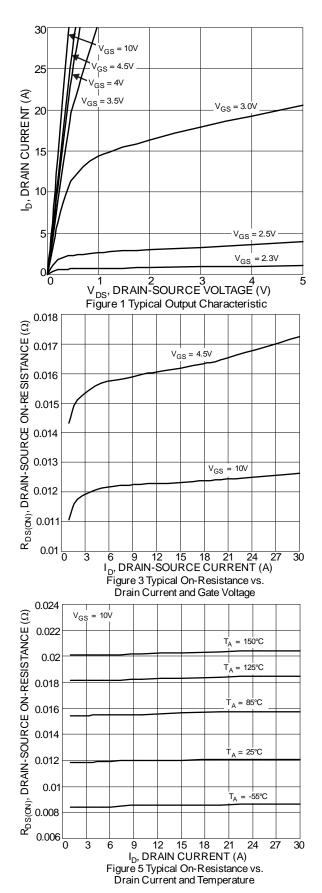
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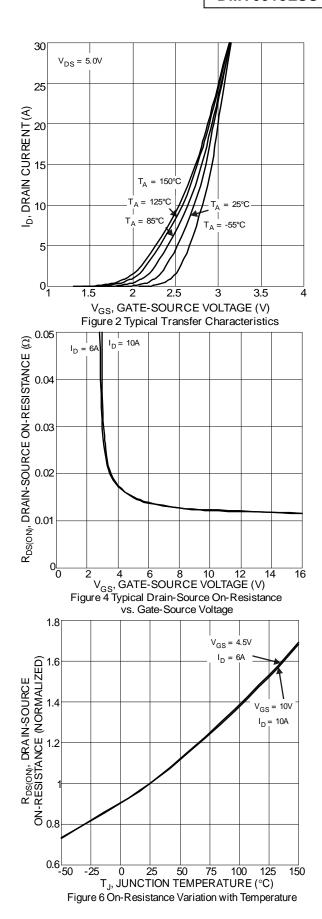
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

7. Short duration pulse test used to minimize self-heating effect.

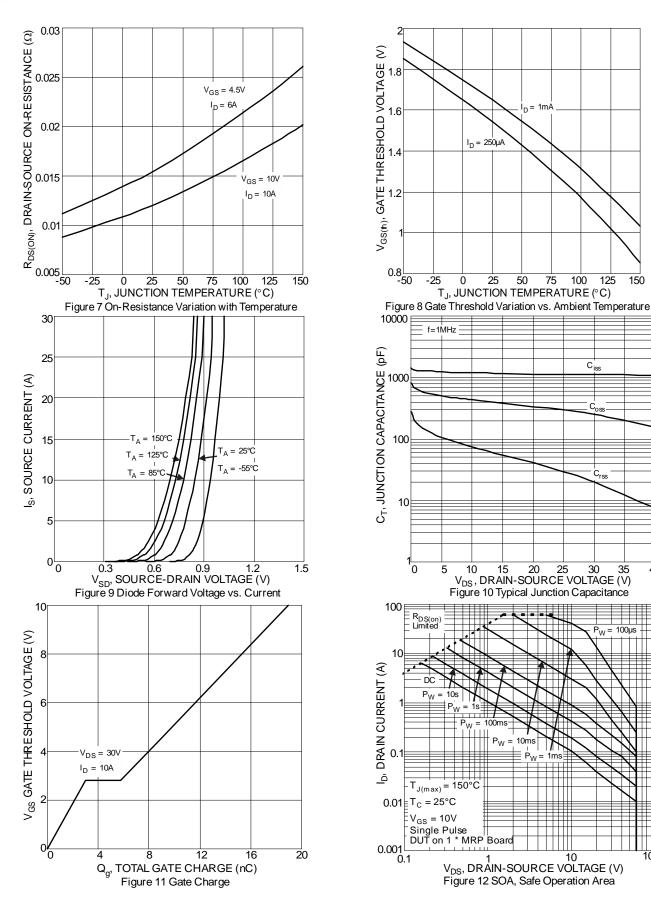
^{8.} Guaranteed by design. Not subject to product testing.







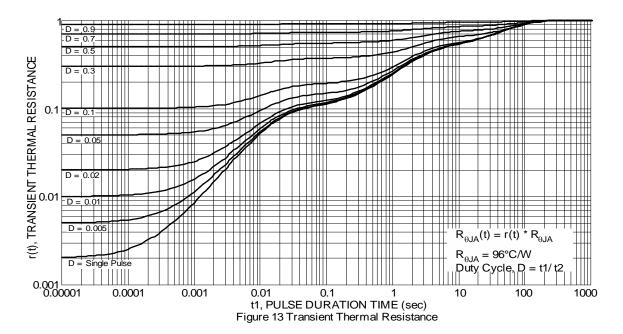




100

40



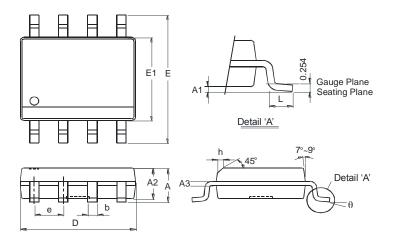




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

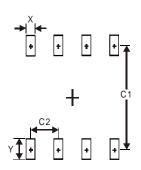
SO-8



SO-8					
Dim	Min	Max			
Α	_	1.75			
A 1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	e 1.27 Typ				
h	_	0.35			
L	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8	

Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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