

35CGQ100 JANS1N7062CCT1 JANTX1N7062CCT1 JANTXV1N7062CCT1

35 Amp. 100V

Ref: MIL-PRF- 19500/762

SCHOTTKY RECTIFIER HIGH EFFICIENCY SERIES

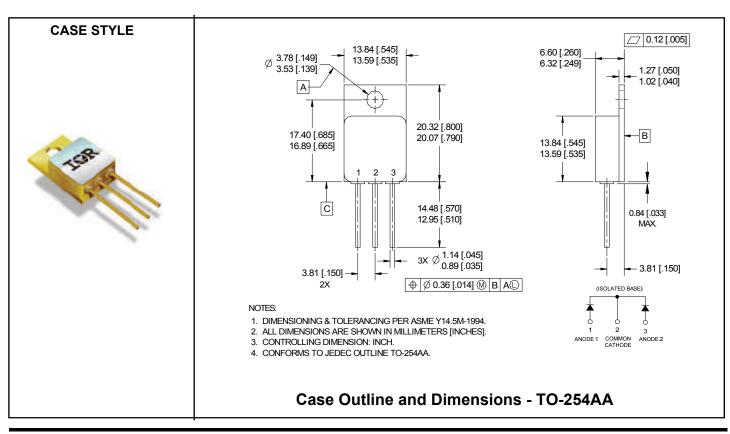
Major Ratings and Characteristics

Characteristics	1N7062CCT1	Units
I _{F(AV)}	35	Α
V _{RRM} (Per Leg)	100	٧
I _{FSM} @ tp = 8.3ms half–sine (Per Leg)	150	Α
V _F @ 35Apk, T _J = 125°C (Per Leg)	0.99	V
T _J , T _{stg} Operating and storage	-65 to 150	°C

Description/Features

The 1N7062CCT1 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of IR HiRel environments. It is packaged in the hemetic isolated TO-254AA package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

- Hermetically Sealed
- Center Tap
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long term Reliability
- Light Weight
- ESD Rating: Class NS per MIL-STD-750,





Voltage Ratings

Part Number	1N7062CCT1
V _R Max. DC Reverse Voltage (V) (Per Leg) V _{RRM} Max. Working Peak Reverse Voltage (V) (Per Leg)	100

Absolute Maximum Ratings

Parameter	Limits	Units	Conditions
I _{F(AV)} Max. Average Forward Current See Fig. 5	35	Α	50% duty cycle @ T _C = 112.5°C, square waveform
I _{FSM} Max. Peak One Cycle Non - Repetitive Surge Current (Per Leg)	150	Α	@ tp = 8.3 ms half-sine

Electrical Specifications

Licci	Electrical Specifications				
	Parameter	Limits	Units	Conditions	
		0.90	V	@ I _F = 15A	
		1.05	V	@ I _F = 30A	T _J = -55°C
		1.08	V	@ I _F = 35A	
	Mary Francisco Valle na Barr	0.82	V	@ I _F = 15A	
	Max. Forward Voltage Drop (Per Leg) See Fig. 1①	1.01	V	@ I _F = 30A	T _J = 25°C
	, ,	1.11	V	@ I _F = 35A	
		0.71	V	@ I _F = 15A	
		0.92	V	@ I _F = 30A	T _J = 125°C
		0.99	V	@ I _F = 35A	
	Max. Reverse Leakage Current (Per Leg) See Fig. 2 ①	0.07	mA	T _J = 25°C	V _R = rated V _R
		40	mA	T _J = 125°C	
Ст	Max. Junction Capacitance (Per Leg)	1000	pF	V _R = 5V _{DC} (1MHz, 25°C)	
Ls	Typical Series Inductance (Per Leg)	7.8	nΗ	Measured from anode lead to cathode lead 6mm (0.25 in.) from package	

Thermal-Mechanical Specifications

Thermal incondition openingations						
	Parameter	Limits	Units	Conditions		
T_J	Max. Junction Temperature Range	-65 to 150	°C			
T _{stg}	Max. Storage Temperature Range	-65 to 150	°C			
R_{thJC}	Max. Thermal Resistance, Junction to Case (Per Leg)	1.25	°C/W	DC operation See Fig. 4		
R_{thJC}	Max. Thermal Resistance, Junction to Case (Per Package)	0.63	°C/W	DC operation		
Wt	Weight (Typical)	9.3	g			
	Die Size (Typical)	158 x 158	mils			
	Case Style	TO-254AA				

 \odot Pulse Width < 300 μ s, Duty Cycle < 2%

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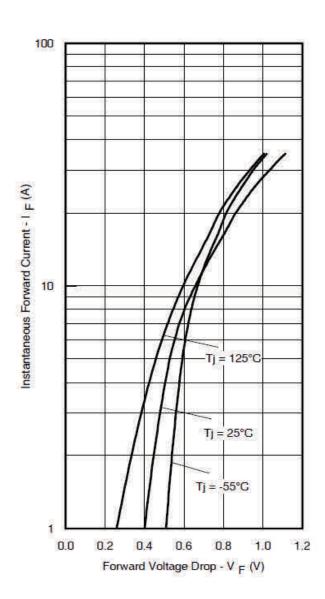


Fig 1. Max. Forward Voltage Drop Characteristics (Per Leg)

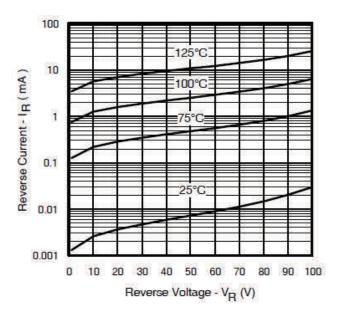


Fig 2. Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

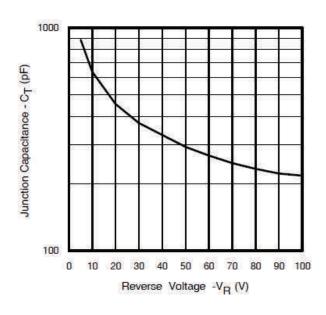


Fig 3. Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)



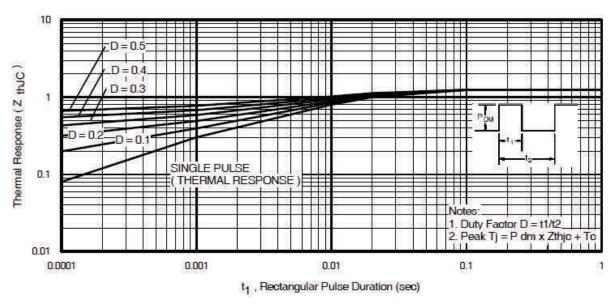


Fig 4. Max. Thermal Impedance ZthJC Characteristics (Per Leg)

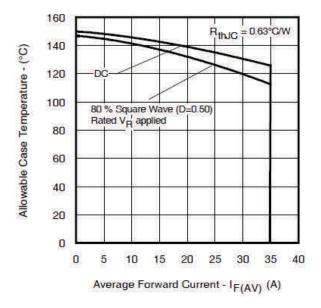


Fig 5. Max. Allowable Case Temperature Vs. Average Forward Current (Per Package)



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Data and specifications subject to change without notice.



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