

SiC

Silicon Carbide Diode

5<sup>th</sup> Generation thinQ!<sup>TM</sup>
650V SiC Schottky Diode
IDH03G65C5

**Final Datasheet** 

Rev. 2.2, 2012-12-10

# Power Management & Multimarket



# 5<sup>th</sup> Generation thinQ!™ SiC Schottky Diode

## 1 Description

ThinQ!<sup>TM</sup> Generation 5 represents Infineon leading edge technology for the SiC Schottky Barrier diodes. The Infineon proprietary diffusion soldering process, already introduced with G3 is now combined with a new, more compact design and thin-wafer technology. The result is a new family of products showing improved efficiency over all load conditions, resulting from both the improved thermal characteristics and a lower figure of merit ( $Qc \times Vf$ ).

The new thinQ!™ Generation 5 has been designed to complement our 650V CoolMOS™ families: this ensures meeting the most stringent application requirements in this voltage range.

#### **Features**

- Revolutionary semiconductor material Silicon Carbide
- Benchmark switching behavior
- No reverse recovery/ No forward recovery
- Temperature independent switching behavior
- · High surge current capability
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC<sup>1)</sup> for target applications
- Breakdown voltage tested at 6.8 mA<sup>2)</sup>
- Optimized for high temperature operation

#### **Benefits**

- System efficiency improvement over Si diodes
- System cost / size savings due to reduced cooling requirements
- Enabling higher frequency / increased power density solutions
- Higher system reliability due to lower operating temperatures
- Reduced EMI

#### **Applications**

- Switch mode power supply
- Power factor correction
- Solar inverter
- Uninterruptible power supply

#### Table 1 Key Performance Parameters

| Parameter                       | Value | Unit |
|---------------------------------|-------|------|
| $V_{DC}$                        | 650   | V    |
| $Q_{C}$ ; $V_{R}$ =400V         | 5     | nC   |
| $E_{\rm C}$ ; $V_{\rm R}$ =400V | 1.1   | μJ   |
| $I_F$ @ $T_C$ < 155°C           | 3     | Α    |

#### Table 2 Pin Definition

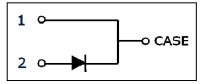
|       |       | •     |
|-------|-------|-------|
| Pin 1 | Pin 2 | Pin 3 |
| С     | Α     | n.a.  |

| Type / ordering Code | Package    | Marking | Related links        |
|----------------------|------------|---------|----------------------|
| IDH03G65C5           | PG-TO220-2 | D0365C5 | www.infineon.com/sic |

- 1) J-STD20 and JESD22
- 2) All devices tested under avalanche conditions for a time periode of 10ms

#### **IDH03G65C5**

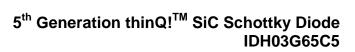














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**Maximum ratings** 

# 2 Maximum ratings

Table 3 Maximum ratings

| Parameter                            | Symbol            | bol Values |      |      | Unit | Note/Test Condition                             |
|--------------------------------------|-------------------|------------|------|------|------|---|
|                                      |                   | Min.       | Тур. | Max. | 1    |   |
| Continuous forward current           | I <sub>F</sub>    | _          | _    | 3    |      | <i>T</i> <sub>C</sub> < 155°C, D=1              |
| Surge non-repetitive forward current | I <sub>F,SM</sub> | _          | _    | 31   | 1,   | $T_C = 25^{\circ}\text{C}, t_p = 10 \text{ ms}$ |
| sine halfwave                        |                   | _          | _    | 29   | A    | $T_{\rm C}$ = 150°C, $t_{\rm p}$ =10 ms         |
| Non-repetitive peak forward current  | $I_{F,max}$       | _          | _    | 178  |      | $T_{\rm C}$ = 25°C, $t_{\rm p}$ =10 µs          |
| i²t value                            | ∫ i²dt            | _          | _    | 4.7  | A²s  | $T_C = 25^{\circ}\text{C}, t_p = 10 \text{ ms}$ |
|                                      |                   | _          | _    | 4.2  |      | $T_{\rm C}$ = 150°C, $t_{\rm p}$ =10 ms         |
| Repetitive peak reverse voltage      | $V_{RRM}$         | _          | _    | 650  | V    | $T_j = 25^{\circ}\text{C}$                      |
| Diode dv/dt ruggedness               | dv/dt             | _          | _    | 100  | V/ns | V <sub>R</sub> =0480 V                          |
| Power dissipation                    | P <sub>tot</sub>  | _          | _    | 42   | W    | $T_C = 25^{\circ}\text{C}$                      |
| Operating and storage temperature    | $T_j;T_{stg}$     | -55        | _    | 175  | °C   |   |
| Mounting torque                      |                   | _          | _    | 70   | Ncm  | M3 screws                                       |

# 3 Thermal characteristics

Table 4 Thermal characteristics TO-220-2

| Parameter  | Symbol     | Values |      |      | Unit | Note/Test Condition                  |
|--|------------|--------|------|------|------|--------------------------------------|
|  |            | Min.   | Тур. | Max. |      |                                      |
| Thermal resistance, junction-case                          | $R_{thJC}$ | -      | 2.2  | 3.6  |      |                                      |
| Thermal resistance, junction-<br>ambient                   | $R_{thJA}$ | _      | _    | 62   | K/W  | leaded                               |
| Soldering temperature, wavesoldering only allowed at leads | $T_{sold}$ | _      | _    | 260  | °C   | 1.6mm (0.063 in.) from case for 10 s |

# 5<sup>th</sup> Generation thinQ!<sup>™</sup> SiC Schottky Diode IDH03G65C5

**Electrical characteristics** 

# 4 Electrical characteristics

Table 5 Static characteristics

| Parameter Sym         | Symbol          |      | Values |      |    | Note/Test Condition                          |
|-----------------------|-----------------|------|--------|------|----|--|
|                       |                 | Min. | Тур.   | Max. |    |  |
| DC blocking voltage   | V <sub>DC</sub> | 650  | _      | _    |    | $I_{R}$ = 0.05 mA, $T_{j}$ =25°C             |
| Diode forward voltage | V <sub>F</sub>  | _    | 1.5    | 1.7  | V  | I <sub>F</sub> = 3 A, T <sub>j</sub> =25°C   |
|                       |                 | _    | 1.8    | 2.1  |    | $I_{\rm F}$ = 3 A, $T_{\rm j}$ =150°C        |
| Reverse current       | I <sub>R</sub>  | _    | 0.20   | 50   |    | V <sub>R</sub> =650 V, T <sub>j</sub> =25°C  |
|                       |                 | _    | 0.04   | 18   | μΑ | V <sub>R</sub> =600 V, T <sub>j</sub> =25°C  |
|                       |                 | _    | 0.6    | 370  |    | V <sub>R</sub> =650 V, T <sub>i</sub> =150°C |

#### Table 6 AC characteristics

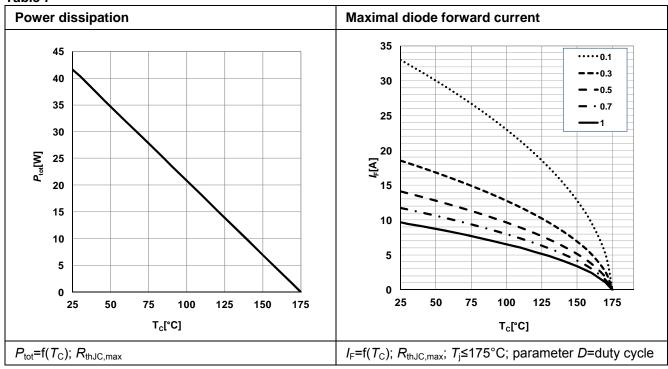
| Parameter               | Symbol Values |      |      | Unit | Note/Test Condition |  |
|-------------------------|---------------|------|------|------|---------------------|--|
|                         |               | Min. | Тур. | Max. |                     |  |
| Total capacitive charge | Qc            | _    | 5    |      | nC                  | $V_R$ =400 V, <i>di/dt</i> =200A/µs,<br>$I_F \le I_{F,MAX}$ , $T_j$ =150°C |
| Total Capacitance       | С             | _    | 100  | _    |                     | V <sub>R</sub> =1 V, <i>f</i> =1 MHz                                       |
|                         |               | _    | 13.0 | _    | pF                  | V <sub>R</sub> =300 V, <i>f</i> =1 MHz                                     |
|                         |               | _    | 12.8 | _    |                     | V <sub>R</sub> =600 V, <i>f</i> =1 MHz                                     |



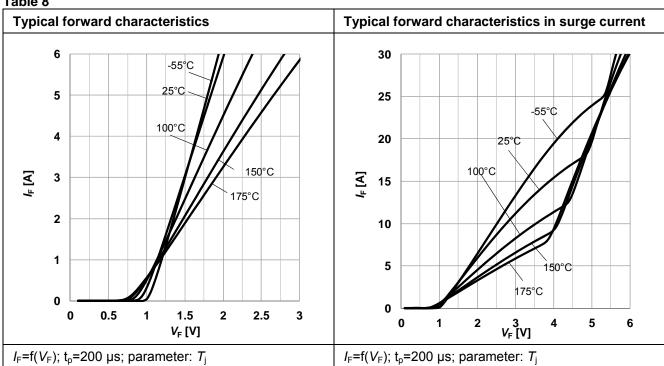
**Electrical characteristics diagrams** 

#### **Electrical characteristics diagrams** 5

#### Table 7



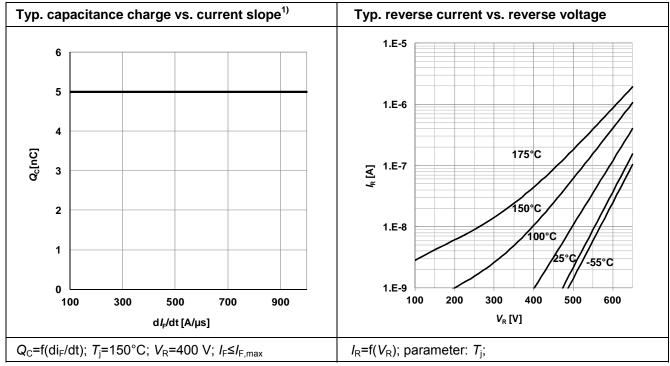
#### Table 8





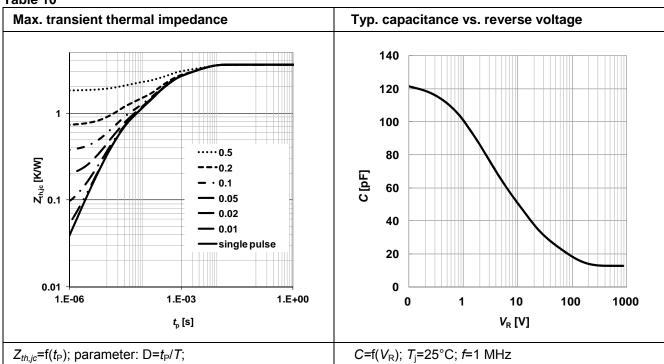
#### **Electrical characteristics diagrams**

Table 9



<sup>1)</sup> Only capacitive charge, guaranteed by design.

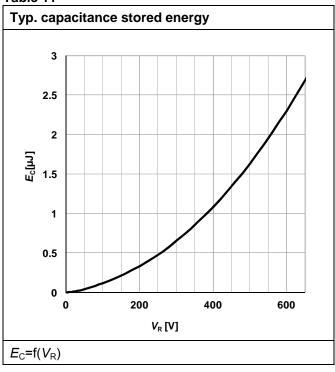
Table 10





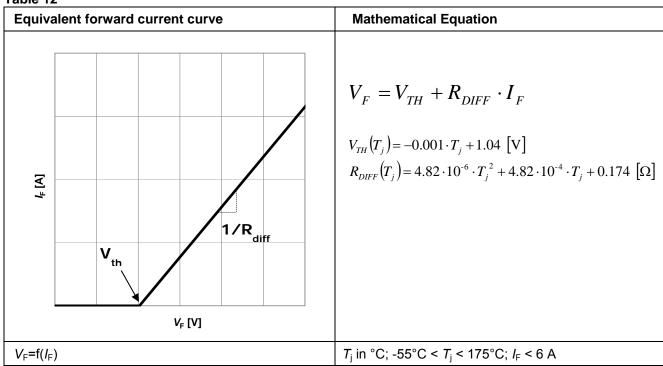
**Electrical characteristics diagrams** 

Table 11



# 6 Simplified Forward Characteristics Model

Table 12





Package outlines

# 7 Package outlines

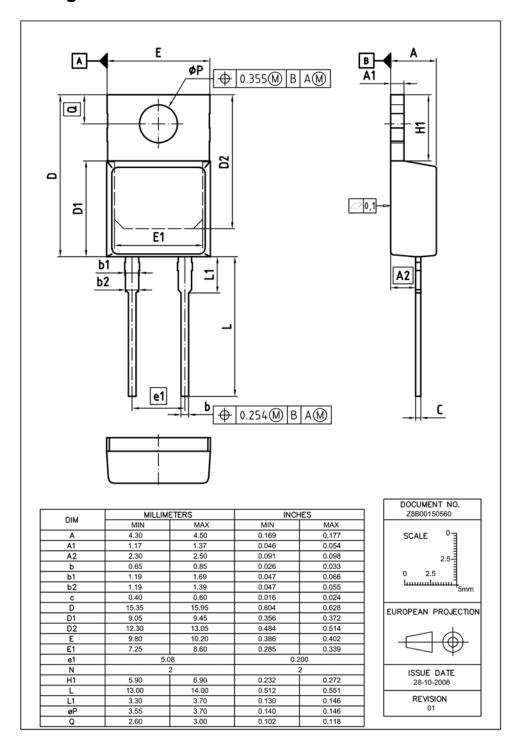


Figure 1 Outlines TO-220, dimensions in mm/inches

## 5<sup>th</sup> Generation thinQ!<sup>TM</sup> SiC Schottky Diode IDH03G65C5

**Revision History** 

#### 8 **Revision History**

5<sup>th</sup> Generation thinQ!<sup>™</sup> SiC Schottky Diode

Revision History: 2012-12-10, Rev. 2.2

Previous Revision:

| Flevious Kev | Flevious Revision.   |  |  |  |  |  |
|--------------|--|--|--|--|--|--|
| Revision     | Subjects (major changes since last version)                                    |  |  |  |  |  |
| 2.0          | Release of the final datasheet.  |  |  |  |  |  |
| 2.1          | Reverse current values, maximum diode forward voltage.                         |  |  |  |  |  |
| 2.2          | Reverse current values, tested avalanche current, simplified calculation model |  |  |  |  |  |

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