



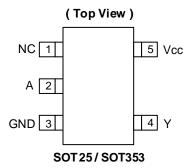
SINGLE SCHMITT-TRIGGER INVERTER

Description

The DIODES™ 74AHC1G14Q is an automotive compliant Schmitt-trigger inverter gate with a standard push-pull output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The gate performs the positive Boolean function:

$$Y = \overline{A}$$

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 2.0V to 5.5V
- ±8mA Output Drive at 4.5V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- Inputs Not Limited by Vcc
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHC1G14Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Applications

- General purpose logics
- Wide array of products, such as:
 - Automotive applications within grade 1 temperature range
 - Industrial computing/controls/automations
 - High reliability networking/communications
 - Industrial/agricultural equipment

Notes:

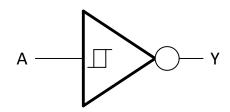
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Name	Description
NC	No Connection
Α	Data Input
GND	Ground
Υ	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Input	Output
Α	Y
Н	L
L	Н

Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to Vcc + 0.5	V
I _{IK}	Input Clamp Current V _I < 0	-20	mA
Іок	Output Clamp Current (Vo < 0 or Vo > Vcc)	±20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	±25	mA
Icc	Continuous Current Through Vcc	75	mA
I _{GND}	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
Tstg	Storage Temperature	-65 to +150	°C
PD	Total Power Dissipation (Note 6)	250	mW

Notes:

- 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
- 5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.
- 6. This will need to be derated at higher operating temperatures to prevent exceeding maximum T_J. Refer to package thermal characteristics section.



Recommended Operating Conditions (Note 7)

Symbol		Parameter	Min	Max	Unit
Vcc	Operating Voltage	_	2	5.5	V
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
		Vcc = 2V	_	-50	μA
I _{OH}	High-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	-4	
		$V_{CC} = 5V \pm 0.5V$	_	-8	mA
		V _{CC} = 2V	_	50	μA
loL	Low-Level Output Current	V _{CC} = 3.3V ± 0.3V	_	4	
		$V_{CC} = 5V \pm 0.5V$	_	8	mA
T _A	Operating Free-Air Temperature	_	-40	+125	°C

Note:

Electrical Characteristics (All typical values are at Vcc = 3.3V, TA = +25°C.)

			.,		+25°C		-40°C to	o +85°C	-40°C to	+125°C	
Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
	D :: 0 : 1 :		3V	_	_	2.2	_	2.2	_	2.2	V
V _{T+}	Positive-Going Input	_	4.5V	_		3.15	_	3.15		3.15	٧
	Threshold Voltage		5.5V	_		3.85	_	3.85		3.85	V
	Namativa Caina Innest		3V	0.9	_	_	0.9	_	0.9	_	V
VT-	Negative-Going Input Threshold Voltage	_	4.5V	1.35	_	_	1.35	_	1.35	_	V
	Threshold voltage		5.5V	1.65	_	_	1.65	_	1.65	_	V
	Lhustarasia		3V	0.3	_	1.2	0.3	1.2	0.25	1.2	V
ΔVτ	Hysteresis (V _{T+} - V _{T-})	_	4.5V	0.4	_	1.4	0.4	1.4	0.35	1.4	V
	(VI+-VI-)		5.5V	0.5	_	1.6	0.5	1.6	0.45	1.6	V
		Mr. Mr.	2V	1.9	2.0	_	1.9	_	1.9	_	
		VI = VIL IOH = -50µA	3V	2.9	3.0	_	2.9	_	2.9	_	
	High Lovel Output		4.5V	4.4	4.5	_	4.4	_	4.4	_	_
Voн	High Level Output Voltage	VI = VIL IOH = -4mA	3V	2.58	_	_	2.48	_	2.40	_	V
		VI = VIL IOH = -8mA	4.5V	3.94	_	_	3.8	_	3.70	_	
		., .,	2V	_	_	0.1	_	0.1	_	0.1	
		VI = VIH	3V			0.1	_	0.1	_	0.1	
	Lavel aval Over	I _{OL} = 50μA	4.5V	_		0.1	—	0.1		0.1	
Vol	Low Level Output Voltage	$V_I = V_{IH}$ $I_{OL} = 4mA$	3V	-	_	0.36	_	0.44	_	0.55	V
		VI = VIH IOL = 8mA	4.5V	-	_	0.36	_	0.44	_	0.55	
II	Input Current	V _I = 5.5V or GND	0 to 5.5V	_	_	±0.1	_	±1	_	±2	μΑ
Icc	Supply Current	V _I = 5.5V or GND I _O = 0	5.5V	_	_	2	_	20	_	40	μА
Cı	Input Capacitance	V _I = V _{CC} or GND	5.5V	_	2.0	10		10	_	10	pF

^{7.} Unused inputs should be held at V_{CC} or Ground.



Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Note 0	1	184	1	0000
θ JA	Junction-to-Ambient	SOT353	Note 8	_	385	_	°C/W
	Thermal Resistance	SOT25	Nata 0	_	62	_	0044
θυс	Junction-to-Case	SOT353	Note 8		164	1	°C/W

Note:

Switching Characteristics

 $V_{CC} = 3.3V \pm 0.3V$ (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C to	o +85°C	-40°C to	+125°C	Unit
	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	
4	۸	V	C _L = 15pF	1.0	4.2	12.8	1.0	15.0	1.0	16.5	ns
tPD	А	Y	C _L = 50pF	1.0	6.0	16.3	1.0	18.5	1.0	20.5	ns

Vcc = 5V ± 0.5V (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C to	-85°C	-40°C to	+125°C	Unit
	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	
4	۸	V	C _L = 15pF	1.0	3.2	8.6	1.0	10.0	1.0	11.0	ns
tpD	А	Y	C _L = 50pF	1.0	4.6	10.6	1.0	12.0	1.0	13.5	ns

Operating Characteristics

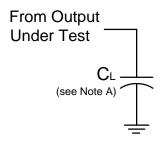
 $T_A = +25$ °C

	Parameter	Test Conditions	Vcc = 5V Typ	Unit
CPD	Power Dissipation Capacitance	f = 1MHz No Load	10	pF

^{8.} Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Measurement Information



Vcc	In	puts	. V _M	CL
100	VI	t _R /t _F	·W	32
3.3V±0.3V	V _{CC}	≤3ns	V _{CC} /2	15pF
5V±0.5V	Vcc	≤3ns	Vcc/2	15pF
3.3V±0.3V	V _{CC}	≤3ns	V _{CC} /2	50pF
5V±0.5V	Vcc	≤3ns	Vcc/2	50pF

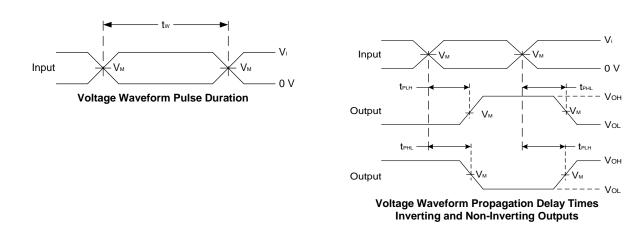


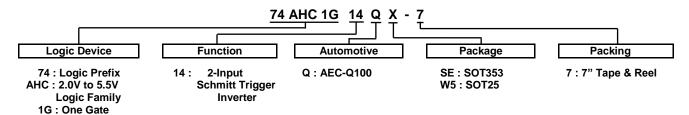
Figure 1. Load Circuit and Voltage Waveforms

Notes:

- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 1MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .



Ordering Information (Note 9)



Part Number	Package	Package	Package Size	Pac	king
Fait Number	Code	(Notes 10 & 11)	Fackage Size	Qty.	Carrier
74AHC1G14QSE-7	SE	SOT353	2.15 mm \times 2.1 mm \times 1.1 mm 0.65 mm lead pitch	3000	7" Tape & Reel
74AHC1G14QW5-7	W5	SOT25	3.0 mm \times 2.8 mm \times 1.2 mm 0.95 mm lead pitch	3000	7" Tape & Reel

Notes: 9. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

10. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.

11. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information

(Top View)

5 4 XXX YWX 1 2 3

XXX : Identification Code
Y : Year 0~9

<u>r . rear 0~9</u> V : Week:A~Z 1~26 week

a~z 27~52 week z represents week 52 and 53

X : A~ Z: Internal Code

SOT 25 / SOT 353

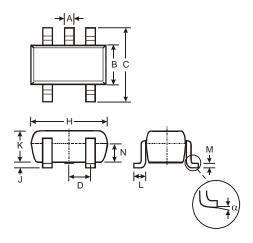
Part Number	Package	Identification Code
74AHC1G14QW5-7	SOT25	YVQ
74AHC1G14QSE-7	SOT353	YVQ



Package Outline Dimensions

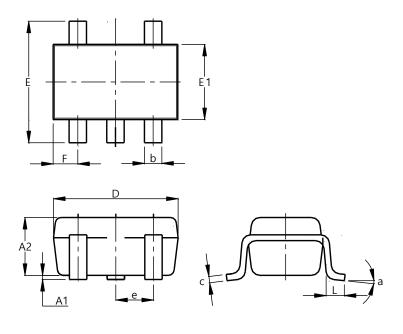
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(1) Package Type: SOT25



SOT25					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
U	2.70	3.00	2.80		
D	-	-	0.95		
Η	2.90	3.10	3.00		
7	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

(2) Package Type: SOT353



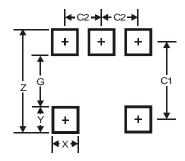
SOT353					
Dim	Min	Max	Тур		
A 1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
E	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



Suggested Pad Layout

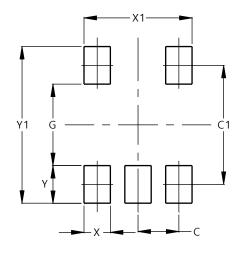
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value	
Z	3.20	
G	1.60	
Х	0.55	
Y	0.80	
C1	2.40	
C2	0.95	

(2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Υ	0.600
Y1	2.500

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0158 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0064 grams (Approximate)

March 2022



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