

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in an MLPAK33 (SOT8002) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- Logic-level compatible
- Trench MOSFET technology
- MLPAK33 package (3.3 x 3.3 mm footprint)

### 3. Applications

- DC-to-DC converters
- Battery management
- Low-side load-switch
- Switching circuits

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	30	V
V <sub>GS</sub>	gate-source voltage			-20	-	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-	11.3	А
Static chara	acteristics						
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 7.5 A; T <sub>j</sub> = 25 °C		-	15	18	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 6.6 A; T <sub>j</sub> = 25 °C		-	18	23	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



# 5. Pinning information

Table 2	Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	S	source		D				
2	S	source						
3	S	source		G_(FTA)				
4	G	gate	— П П	mbb076 S				
5	D	drain						
6	D	drain						
7	D	drain	MLPAK33 (SOT8002-1)					
8	D	drain						

## 6. Ordering information

Type number	Package		
	Name	Description	Version
PXN018-30QL	MLPAK33	plastic thermal enhanced surface mounted package; mini leads; 8 terminals; pitch 0.65 mm; 3.3 x 3.3 x 0.8 mm body	SOT8002-1

## 7. Marking

Table 4. Marking codes	
Type number	Marking code
PXN018-30QL	9AA

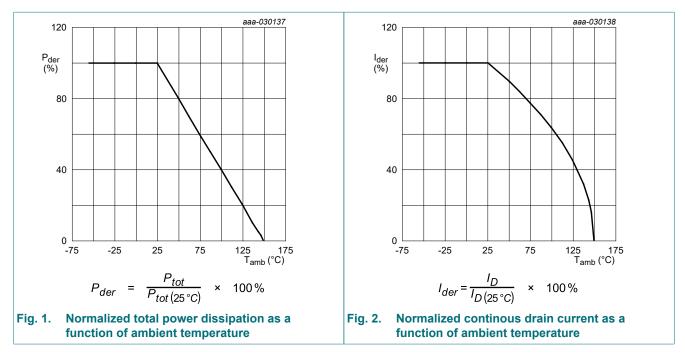
### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

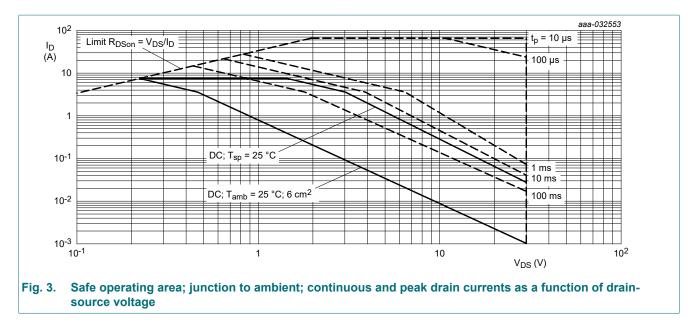
Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	30	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	11.3	A
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	7.5	А
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C	[1]	-	4.8	А
		V <sub>GS</sub> = 10 V; T <sub>sp</sub> = 25 °C		-	19.2	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	66	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	3.8	W
		T <sub>amb</sub> = 25 °C	[1]	-	1.7	W
		T <sub>sp</sub> = 25 °C		-	10.9	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode					
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	1.5	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



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#### 30 V, N-channel Trench MOSFET



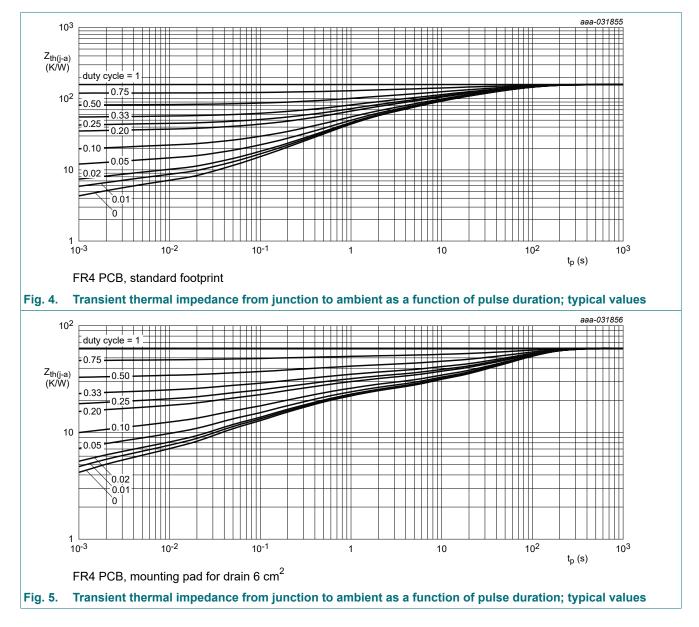
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## 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	160	200	K/W
			[2]	-	60	75	K/W
		in free air; t ≤ 5 s	[2]	-	28	33	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	8.3	11.5	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



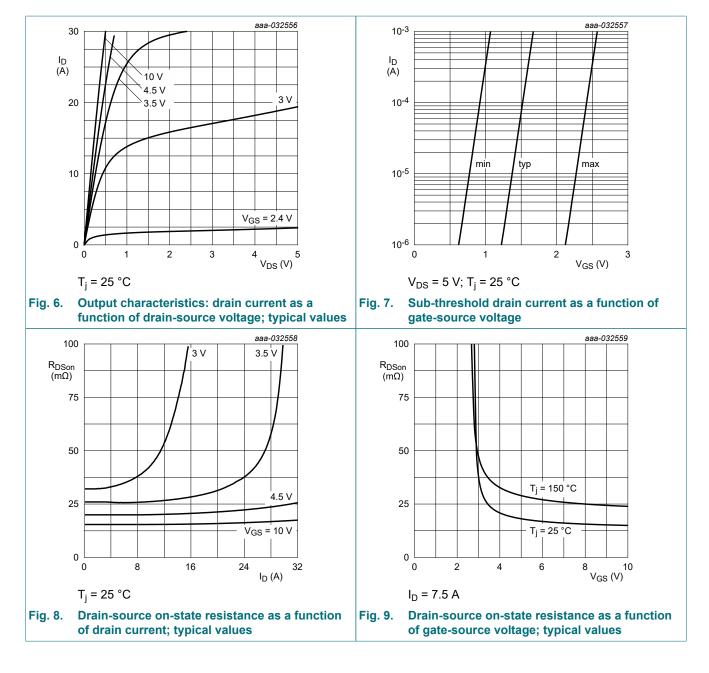
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## **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	30	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	I <sub>D</sub> = 250 μA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C	1	1.6	2.5	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 30 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-0.1	μA
		V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	0.1	μA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 7.5 A; T <sub>j</sub> = 25 °C	-	15	18	mΩ
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 7.5 A; T <sub>j</sub> = 150 °C	-	25	30	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 6.6 A; T <sub>j</sub> = 25 °C	-	18	23	mΩ
9fs	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 7.5 A; T <sub>j</sub> = 25 °C	-	25	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz	-	2	-	Ω
Dynamic ch	aracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 15 V; I <sub>D</sub> = 7.5 A; V <sub>GS</sub> = 10 V; T <sub>j</sub> = 25 °C	-	7.2	10.8	nC
		V <sub>DS</sub> = 15 V; I <sub>D</sub> = 6.6 A; V <sub>GS</sub> = 4.5 V; T <sub>j</sub> = 25 °C	-	3.4	5.1	nC
Q <sub>GS</sub>	gate-source charge		-	1.2	-	nC
Q <sub>GS(th)</sub>	pre-threshold gate- source charge		-	0.7	-	nC
Q <sub>GS(th-pl)</sub>	post-threshold gate- source charge		-	0.5	-	nC
Q <sub>GD</sub>	gate-drain charge		-	1	-	nC
V <sub>GSpl</sub>	gate-source plateau voltage	V <sub>DS</sub> = 15 V; I <sub>D</sub> = 6.6 A; T <sub>j</sub> = 25 °C	-	2.7	-	V
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 15 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	447	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	86	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	30	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 15 V; I <sub>D</sub> = 6.6 A; V <sub>GS</sub> = 4.5 V;	-	4	-	ns
t <sub>r</sub>	rise time	R <sub>G(ext)</sub> = 5 Ω; T <sub>j</sub> = 25 °C	-	7	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	5	-	ns
t <sub>f</sub>	fall time	1	-	2	-	ns
Source-drai	n diode					
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 1.5 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	0.7	1.2	V
t <sub>rr</sub>	reverse recovery time	I <sub>S</sub> = 1.5 A; dI <sub>S</sub> /dt = -100 A/μs;	-	11	-	ns
Q <sub>r</sub>	recovered charge	V <sub>GS</sub> = 4.5 V; V <sub>DS</sub> = 15 V; T <sub>j</sub> = 25 °C	-	3	-	nC
t <sub>a</sub>	reverse recovery rise time		-	7	-	ns
t <sub>b</sub>	reverse recovery fall time		-	4	-	ns
		1	<u> </u>			

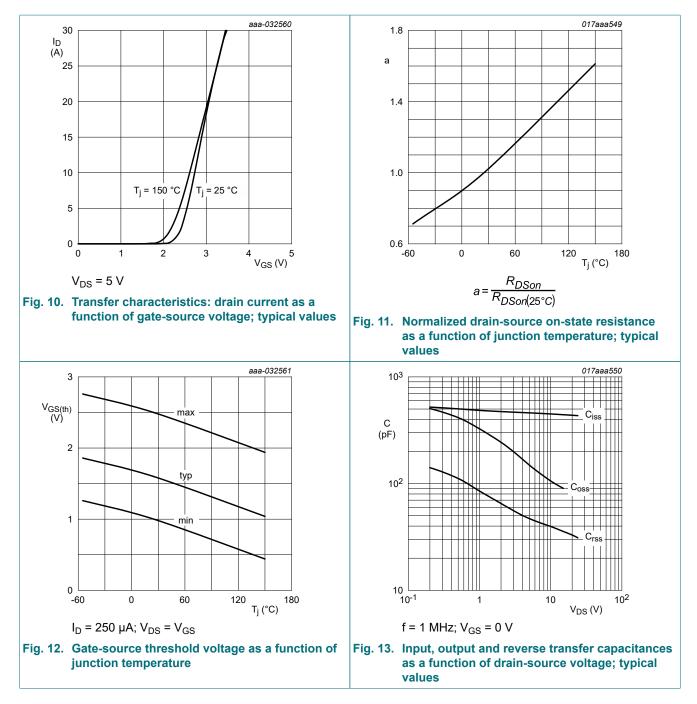
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#### 30 V, N-channel Trench MOSFET

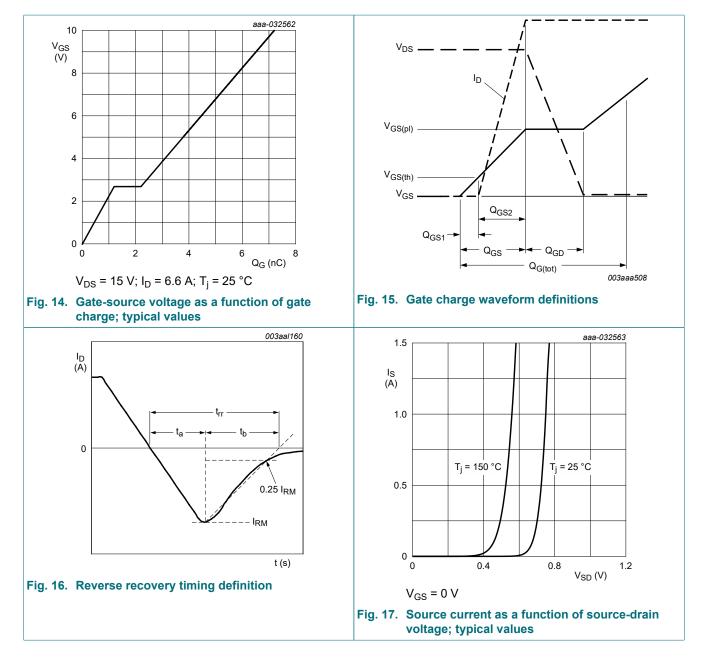


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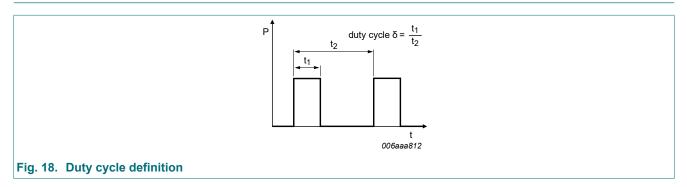
#### 30 V, N-channel Trench MOSFET



#### 30 V, N-channel Trench MOSFET



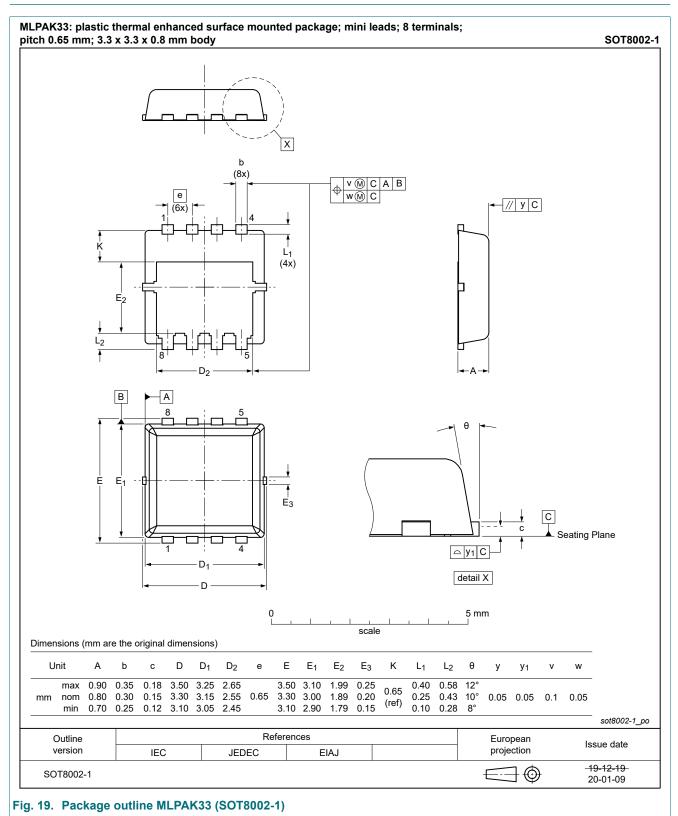
### 11. Test information



PXN018-30QL

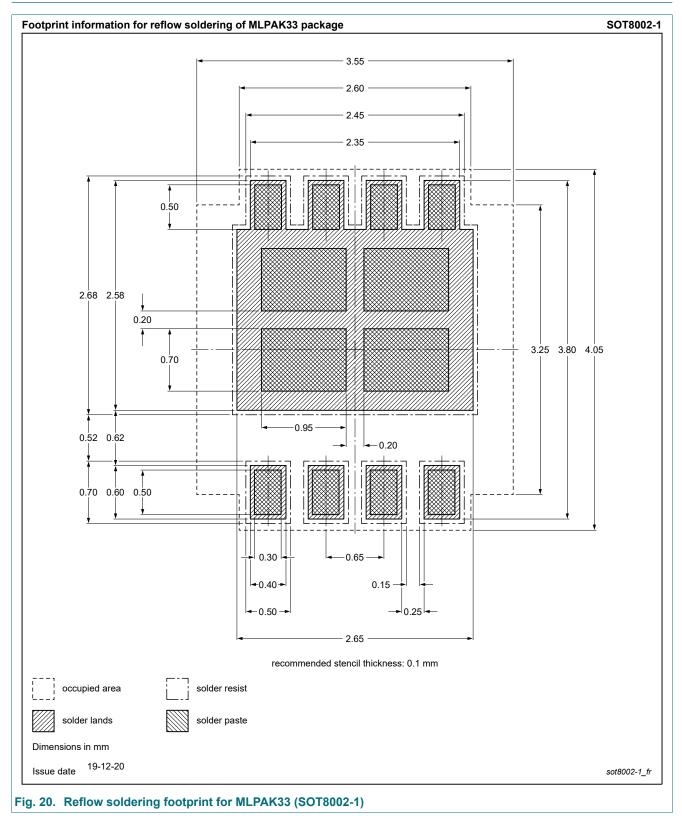
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### 12. Package outline



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## 13. Soldering



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# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PXN018-30QL v.1	20210105	Product data sheet	-	-		

PXN018-30QL

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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PXN018-30QL

## Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Thermal characteristics	5
10.	Characteristics	6
11.	Test information	9
12.	Package outline	10
	Soldering	
14.	. Revision history	12
	Legal information	
	-	

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