

MT29F64G08CBEBBL84A3WC1

[Login or Register to](#)
[Global](#) [Products & Support](#) [NAND Flash](#) [MLC NAND](#) [MT29F64G08CBEBBL84A3WC1](#)
[Add](#) [Email](#)
[See all MLC NAND parts](#) | [See all 64Gb MLC NAND parts](#)

Data Sheets (2)

Data Sheet

[» Download](#)
[Add](#)
[Email](#)

Die Data Sheet

[» Download](#)
[Add](#)
[Email](#)

Orderable Parts (1)

[» Compare all](#)

Orderable Part Information

Status	Production	Alternative Part	N/A
FBGA Code	N/A	SPD Data	N/A
MBQual Data	N/A	Shipping Media	N/A
PLP	No	Start Date	N/A

Specs

Density	64Gb	Status	Production
RoHS	Yes	Width	x8
Voltage	3.3V	Package	Wafer
Pin Count	n/a	MT/s	n/a
I/O	Common	Product Name	

Where to Buy

[View All Distributors](#)

Showing 34 Micron Distributors:

« »

Need Help? Contact a sales rep to request samples, get a quote, and receive a callback.

[» Login](#)
[» Find my Rep](#)

Overview

Documentation & Support

Sim Models & Software

FAQs

Recently Added

Date	What was added
04/2014	Datasheet: NAND 64Gb MLC Async/Sync L84A Die
12/2013	Datasheet: NAND 64Gb/128/256Gb MLC Async/Sync, L84A

FAQs

- » Do you support small block devices?
- » How much ECC do I need to support your devices?
- » I am using the correct amount of error correction code (ECC) for the NAND device, but I'm still seeing bitbyte errors in data I read back from the NAND device.
- » See all FAQs

Sim Models & Software

Title & Description	Secure	ID	Updated
HS spice: NAND 64Gb/128/256/512Gb MLC Async/Sync, L84A:		L84A	09/2012
IBIS: NAND 64Gb/128/256/512Gb MLC Async/Sync, L84A: Rev 2.0		L84A	09/2012

[» See all Sim Models \(2\)](#)

Documentation & Support

Search for documents, data sheets, and other resources.

Search

 [» Search](#)

Filter

<input type="checkbox"/> Customer Service Note	<input type="checkbox"/> Data Sheet	<input type="checkbox"/> Other Documents
<input type="checkbox"/> Part Numbering Guide	<input type="checkbox"/> Presentation	<input type="checkbox"/> Product Flyer
<input type="checkbox"/> RoHS Certification	<input type="checkbox"/> Technical Note	<input type="checkbox"/> Tool
<input type="checkbox"/> Webinar	<input type="checkbox"/> White Paper	

[+ For MT29F64G08CBEBBL84A3WC1 \(4\)](#)
[+ For MLC NAND \(2\)](#)
[+ For NAND Flash \(93\)](#)
[+ For Products and Support \(14\)](#)

Please Note: To view Secure Documents please [click here](#) or click on a secured document to request access.

Search

 [» Search](#)

Filter

<input type="checkbox"/> NAND Flash Software	<input type="checkbox"/> Sim Model
--	------------------------------------

[+ For MT29F64G08CBEBBL84A3WC1 \(2\)](#)
[+ For MLC NAND \(0\)](#)
[+ For NAND Flash \(11\)](#)
[+ For Products and Support \(0\)](#)

Please Note: To view Secure Documents please [click here](#) or click on a secured document to request access.

Disclaimers

- Micron Models: By downloading any Micron model from this site, you must agree to the terms of [this disclaimer](#). If you do not agree to terms, you do not have permission to use the site or download material from it.
- Non-Micron Models: For your convenience, Micron links to third-party simulation models. Note that Micron does not guarantee functionality or accuracy of these models.

[+ Do you support small block devices?](#)
[+ How much ECC do I need to support your devices?](#)
[+ I am using the correct amount of error correction code \(ECC\) for the NAND device, but I'm still seeing bitbyte errors in data I read back from the NAND device.](#)
[+ How do I achieve greater PROGRAM/READ throughput for the NAND device?](#)
[+ How is Nvb specified?](#)
[+ I am seeing a lot of READ DISTURB errors. Can you tell me if there is a problem with your part?](#)
[+ I've heard that NAND has too many errors to boot from. Is this true?](#)
[+ Should I be marking blocks bad due to READ errors?](#)
[+ When I issue a Read ID command \(90h\) to a two-die NAND device, I get a device ID back that states it is a one-die NAND device.](#)
[+ Where can I find additional technical information about Micron NAND devices that is not covered in the device data sheets?](#)
[+ Where can I find simulation models for NAND Flash devices?](#)
[+ Why am I getting a bitbyte error reading back the information I programmed into the NAND device?](#)
[+ Why doesn't the NAND Flash device respond correctly to commands issued to it?](#)
[+ What is a "bank"?](#)
[+ What is the impedance tolerance of the driver in match-impedance mode relative to the expected value base on the perfect reference resistor connected to ZQ pin?](#)
[+ Does thermal information change for IT parts?](#)
[+ My design was based on a specification stating the JTAG was relative to VDD \(1.8V\), but now we've discovered that JTAG is actually relative to VDDQ \(1.5V\). It's a fairly significant board spin to change this; what do I risk by leaving the design as-is? I assume that the specification is still for VDDQ + 0.3V = 1.8V, but with CMOS parts there's no way I can guarantee that it won't swing past that on transitions.](#)
[+ Should the ECC memory chip share chip select and CKE signals with the other two main memory chips in our point-to-point application?](#)
[+ Who do I contact if I have questions about my buymicron.com order?](#)