Product data sheet

1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 plastic package designed to protect one transmission or data line from the damage caused by ESD and other transients.

2. Features and benefits

- · Unidirectional ESD protection of one line
- Low clamping voltage: V_{CL} = 40 V at I_{PPM} = 5 A
- ESD protection > 30 kV
- IEC 61000-4-5 (surge); I_{PPM} = 5 A at t_p = 8/20 μs

3. Application information

- Computers and peripherals
- · Communication systems
- · Audio and video equipment
- Data lines
- · CAN bus protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C	-	-	15	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	-	32	70	pF



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		1 + 2
2	A	anode	1 2	sym035
			SOD523	

^[1] The marking bar indicates pin 1.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD15VS1UB	SOD523	plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body	SOD523

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD15VS1UB	N4

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1]	-	160	W
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	5	Α
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	m ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[2]	-	30	kV
	voltage	HBM MIL-STD883		-	10	kV

- [1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.
- [2] Device stressed with ten non-repetitive ESD pulses.

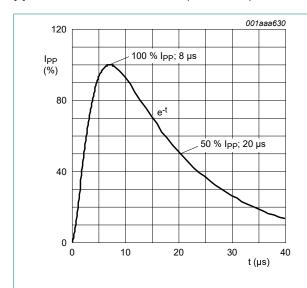


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

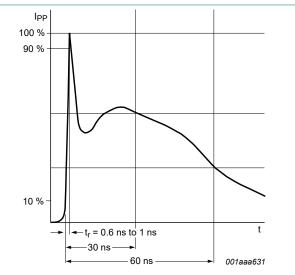


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	15	V
V_{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C		17.6	18	18.4	V
I _{RM}	reverse leakage current	V _{RWM} = 15 V; T _{amb} = 25 °C		-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	32	70	pF
V _{CL}	clamping voltage	I _{PPM} = 1 A; T _{amb} = 25 °C	[1]	-	-	23	V
		I _{PPM} = 5 A; T _{amb} = 25 °C	[1]	-	-	40	V
r _{dif}	differential resistance	I _R = 1 mA; T _{amb} = 25 °C		-	-	225	Ω

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.

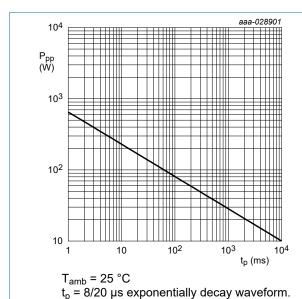


Fig. 3. Peak pulse power dissipation as a function of pulse time; typical values

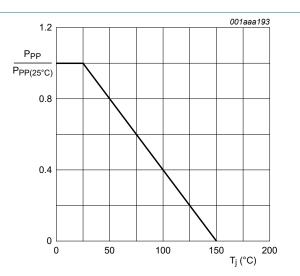


Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

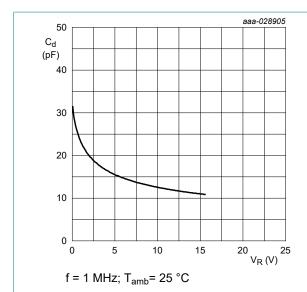


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

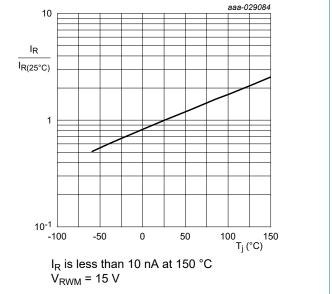
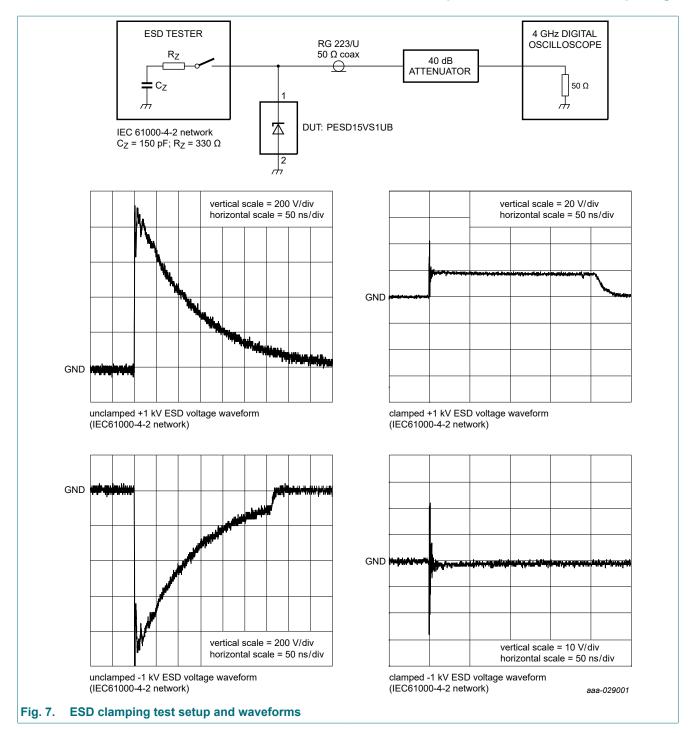
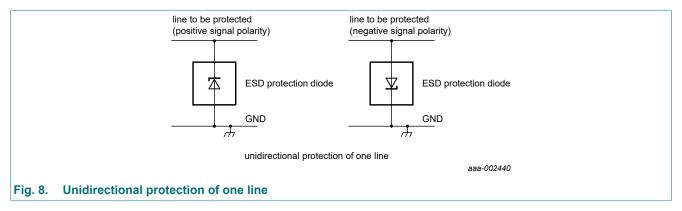


Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values



10. Application information

The device is designed for unidirectional protection of one single data line from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarity is above or below ground. It provides a surge capability of up to 160 W per line for a 8/20 µs waveform.

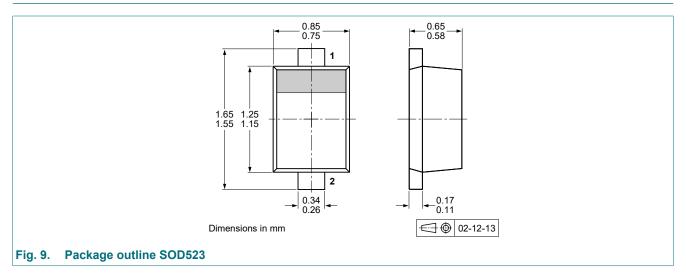


Circuit board layout and protection device placement

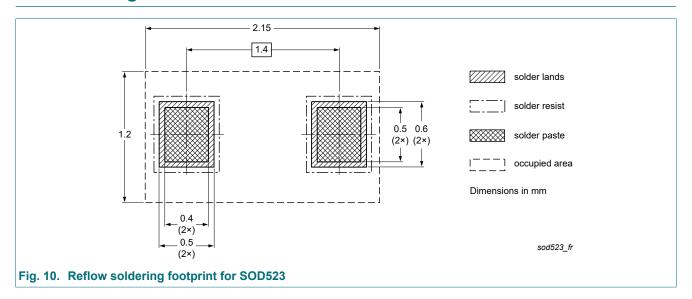
Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- $\textbf{8.} \ \ \text{Use ground planes whenever possible. For multilayer PCBs, use ground vias.}$

11. Package outline



12. Soldering



13. Revision history

Table 7. Revision history

Table 1. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD15VS1UB v.1	20181129	Product data sheet	-	PESDXS1UB_SERIES_2
Modifications:	Nexperia. Legal texts have Soldering section Application inforn Figure 9: updated	nation: updated.		, ,
PESDXS1UB_SERIES_2	20090824	Product data sheet	-	PESDXS1UB_SERIES_1
PESDXS1UB_SERIES_1	20040614	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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PESD15VS1UB

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