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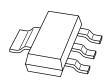
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Kind regards,

Team Nexperia



 500 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

 Rev. 01 — 11 December 2009
 Product data

Product data sheet

1. Product profile

1.1 General description

NPN high-voltage low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBHV9540Z.

1.2 Features

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- AEC-Q101 qualified
- Medium power SMD plastic package

1.3 Applications

- LED driver for LED chain module
- LCD backlighting
- Automotive motor management
- Switch Mode Power Supply (SMPS)

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CESM}	collector-emitter peak voltage	$V_{BE} = 0 V$	-	-	500	V
V _{CEO}	collector-emitter voltage	open base	-	-	400	V
I _C	collector current		-	-	1	А
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 50 mA	[<u>1]</u> 100	155	-	

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.



500 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	base		
2	collector		2, 4
3	emitter		1
4	collector		۲ ۱
			sym016

3. Ordering information

Table 3. Orde	ering informat	ion	
Type number	Package		
	Name	Description	Version
PBHV8140Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223

4. Marking

Table 4.	Marking codes	
Type num	ber	Marking code
PBHV8140)Z	V8140Z

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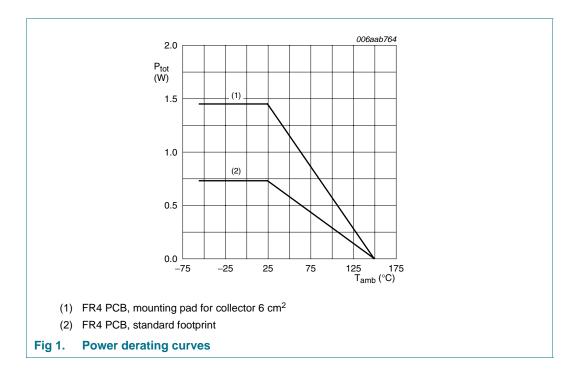
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5. Limiting values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{СВО}	collector-base voltage	open emitter	-	500	V
V _{CEO}	collector-emitter voltage	open base	-	400	V
V _{CESM}	collector-emitter peak voltage	$V_{BE} = 0 V$	-	500	V
V _{EBO}	emitter-base voltage	open collector	-	6	V
I _C	collector current		-	1	А
I _{CM}	peak collector current	single pulse; $t_p \leq 1 ms$	-	2	А
I _{BM}	peak base current	single pulse; $t_p \leq 1 \text{ ms}$	-	400	mA
P _{tot}	total power dissipation	$T_{amb} \leq 25 \ ^{\circ}C$	<u>[1]</u> -	0.73	W
			[2] _	1.45	W
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



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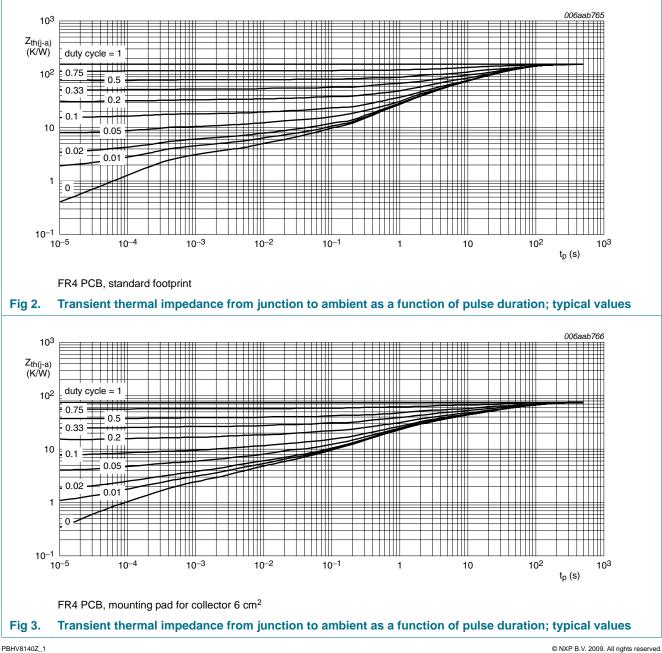
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Thermal characteristics 6.

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	<u>[1]</u> -	-	170	K/W
junction to ambient	junction to ambient		[2] _	-	85	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		-	-	15	K/W

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. [1]

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



Product data sheet

500 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
I _{CBO}	collector-base cut-off	$V_{CB} = 320 \text{ V}; I_E = 0 \text{ A}$		-	-	100	nA
	current	$V_{CB} = 320 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 \text{ °C}$		-	-	10	μA
I _{CES}	collector-emitter cut-off current	$V_{CE} = 320 \text{ V}; \text{ V}_{BE} = 0 \text{ V}$		-	-	100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 4 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V					
		I _C = 50 mA		100	155	-	
		I _C = 100 mA	[1]	80	150	-	
		I _C = 500 mA	[1]	35	65	-	
		I _C = 1 A	[1]	10	20	-	
V _{CEsat}	collector-emitter	$I_{C} = 100 \text{ mA}; I_{B} = 10 \text{ mA}$	[1]	-	45	80	mV
	saturation voltage	I_{C} = 100 mA; I_{B} = 20 mA	[1]	-	30	50	mV
		I _C = 500 mA; I _B = 100 mA	[1]	-	85	140	mV
		I _C = 1 A; I _B = 200 mA	[1]	-	150	250	mV
R _{CEsat}	collector-emitter saturation resistance	$I_{C} = 1 \text{ A}; I_{B} = 200 \text{ mA}$	[1]	-	150	250	mΩ
V _{BEsat}	base-emitter saturation voltage	$I_{\rm C}$ = 1 A; $I_{\rm B}$ = 200 mA	<u>[1]</u>	-	0.95	1.1	V
t _d	delay time	$V_{CC} = 6 \text{ V}; \text{ I}_{C} = 0.5 \text{ A};$		-	25	-	ns
t _r	rise time	$I_{Bon} = 0.1 \text{ A}; I_{Boff} = -0.1 \text{ A}$		-	2820	-	ns
t _{on}	turn-on time			-	2845	-	ns
t _s	storage time			-	2585	-	ns
t _f	fall time			-	1215	-	ns
t _{off}	turn-off time			-	3800	-	ns
f _T	transition frequency	$V_{CE} = 10 \text{ V}; I_{C} = 10 \text{ mA};$ f = 100 MHz		-	25	-	MHz
C _c	collector capacitance	$V_{CB} = 20 \text{ V}; \text{ I}_{E} = \text{i}_{e} = 0 \text{ A};$ f = 1 MHz		-	12	-	pF
C _e	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_C = i_c = 0 \text{ A};$ f = 1 MHz		-	600	-	pF

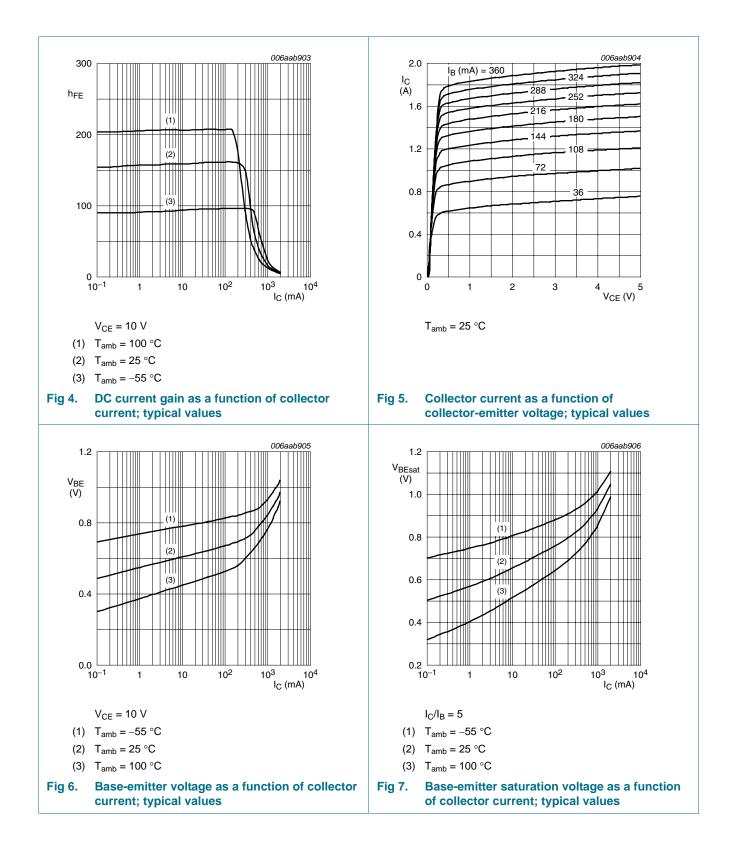
[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$

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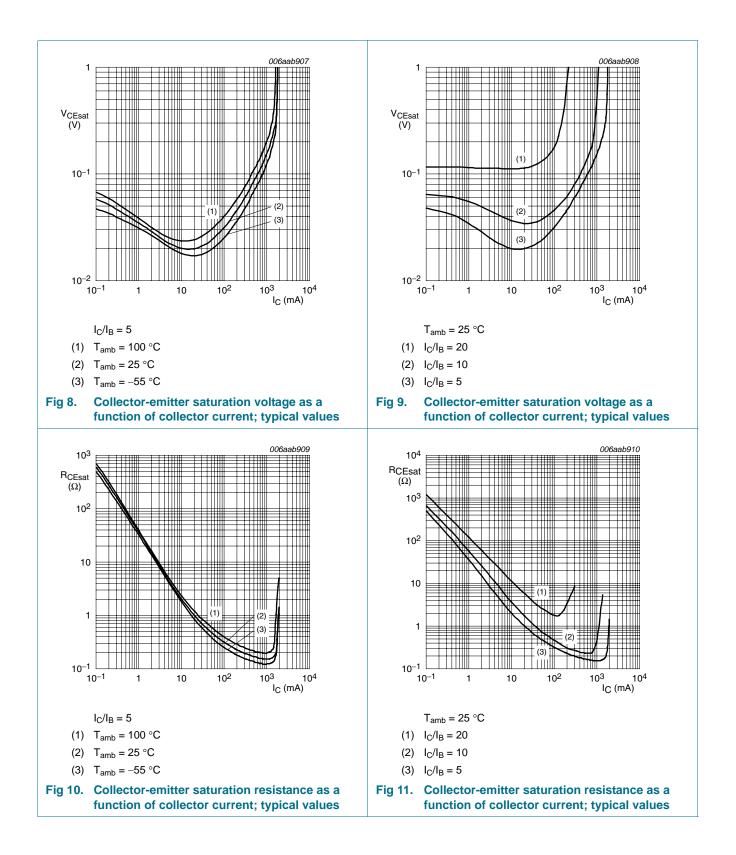


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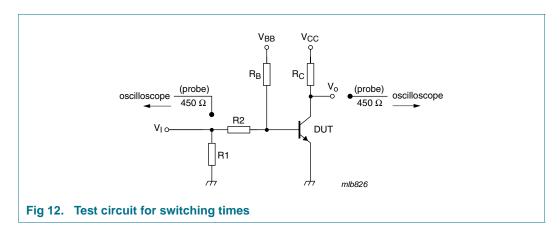
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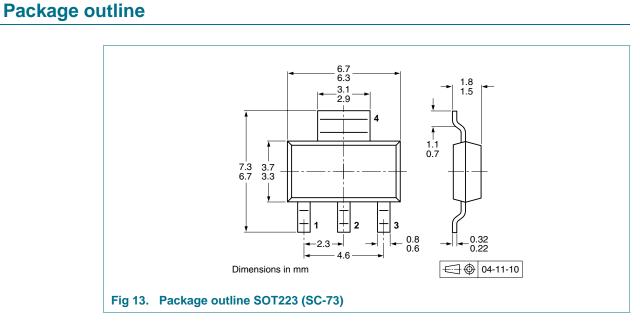
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8. Test information



8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.



10. Packing information

Table 8.Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	e number Package Description		Packing quantity	
			1000	4000
PBHV8140Z	SOT223	8 mm pitch, 12 mm tape and reel	-115	-135

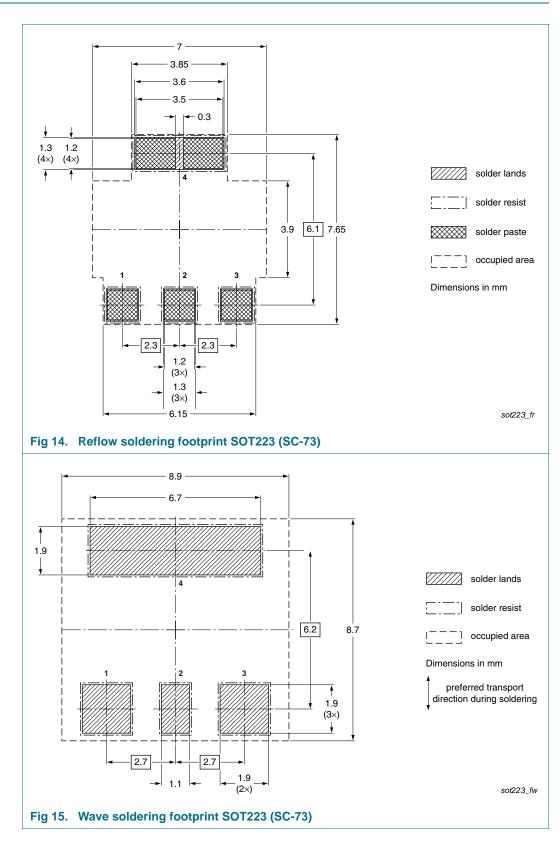
[1] For further information and the availability of packing methods, see <u>Section 14</u>.

PBHV8140Z_1

9.

500 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

11. Soldering



PBHV8140Z_1

Product data sheet

500 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

12. Revision history

Table 9. Revisio	on history				
Document ID	Release date	Data sheet status	Change notice	Supersedes	
PBHV8140Z_1	20091211	Product data sheet	-	-	

500 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

13. Legal information

Data sheet status 13.1

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

The term 'short data sheet' is explained in section "Definitions". [2]

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://w

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500 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

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