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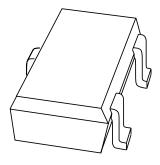
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



BF824WPNP medium frequency transistor

Product data sheet Supersedes data of 1997 Jul 07 1999 Apr 15



PNP medium frequency transistor

BF824W

FEATURES

- Low current (max. 25 mA)
- Low voltage (max. 30 V).

APPLICATIONS

• RF stages in FM front-ends in common base configuration.

DESCRIPTION

PNP medium frequency transistor in a SOT323 plastic package.

MARKING

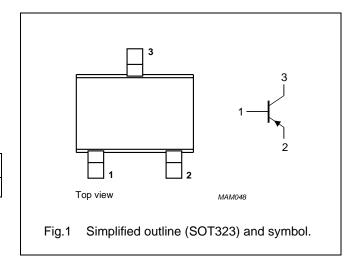
TYPE NUMBER	MARKING CODE(1)		
BF824W	F8*		

Note

* = - : Made in Hong Kong.
 * = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-30	V
V_{CEO}	collector-emitter voltage	open base	_	-30	V
V_{EBO}	emitter-base voltage	open collector	_	-4	V
I _C	collector current (DC)		_	-25	mA
I _{CM}	peak collector current		_	-25	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

PNP medium frequency transistor

BF824W

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	625	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	$I_E = 0; V_{CB} = -30 \text{ V}$	_	-50	nA
		$I_E = 0$; $V_{CB} = -30 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	-	-10	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -4 V$	-	-100	nA
h _{FE}	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -10 \text{ V}$	25	_	
		$I_C = -4 \text{ mA}; V_{CE} = -10 \text{ V}$	25	_	
V_{BE}	base-emitter voltage	$I_C = -4 \text{ mA}; V_{CE} = -10 \text{ V}$	-	-900	mV
C_{rb}	feedback capacitance	$I_C = 0$; $V_{CE} = -10 \text{ V}$; $f = 1 \text{ MHz}$	-	0.3	pF
f _T	transition frequency	$V_{CE} = -10 \text{ V}; f = 100 \text{ MHz}; \text{ note 1}$			
		$I_C = -1 \text{ mA}$	250	_	MHz
		$I_C = -4 \text{ mA}$	400	_	MHz
		$I_C = -8 \text{ mA}$	390	_	MHz

Note

1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

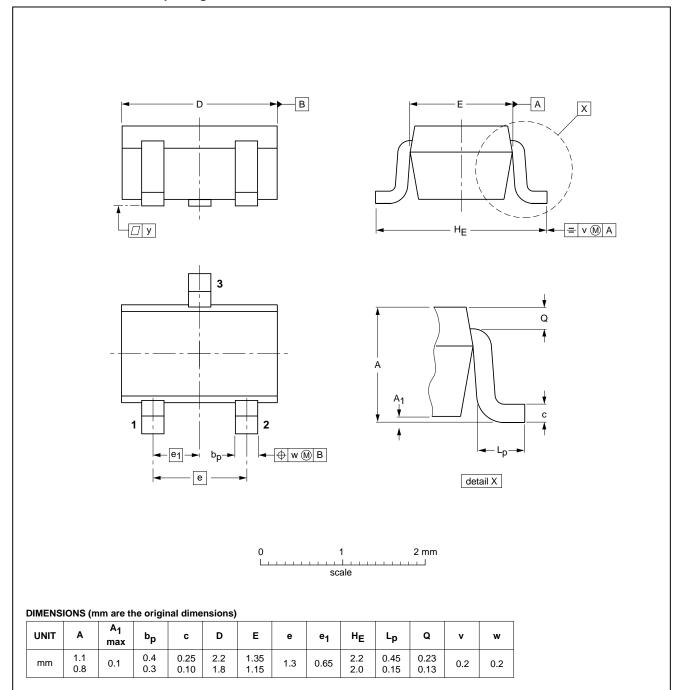
PNP medium frequency transistor

BF824W

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT323



OUTLINE		REFERENCES EUROPEAN		ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT323			SC-70	$ \ \ \bigoplus \big($	97-02-28

PNP medium frequency transistor

BF824W

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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NXP Semiconductors

Customer notification

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Contact information

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