DISCRETE SEMICONDUCTORS

DATA SHEET

PMBTH81 PNP 1 GHz switching transistor

Product specification
File under Discrete Semiconductors, SC14

September 1995







PNP 1 GHz switching transistor

PMBTH81

FEATURES

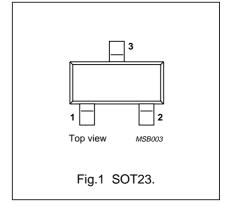
- Low cost
- High transition frequency.

DESCRIPTION

The PMBTH81 is a general purpose silicon pnp transistor, encapsulated in a SOT23 plastic envelope. Its complement is the PMBTH10.

PINNING

PIN	PIN DESCRIPTION			
Code: V31				
1 base				
2	emitter			
3	collector			



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	20	V
P _{tot}	total power dissipation	T _s = 45 °C (note 1)	_	400	mW
C _{ce}	collector-emitter capacitance	V _{CB} = 10 V; I _B = 0; f = 1 MHz	_	0.65	pF
C _{cb}	collector-base capacitance	V _{CB} = 10 V; I _E = 0; f = 1 MHz	_	0.85	pF
f _T	transition frequency	V _{CE} = 10 V; I _C = 5 mA; f = 100 MHz; T _{amb} = 25 °C	600	_	MHz

Note

1. T_s is the temperature at the soldering point of the collector tab.

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LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	20	V
V _{EBO}	emitter-base voltage	open collector	_	3	V
I _C	collector current		_	40	mA
P _{tot}	total power dissipation	T _s = 45 °C (note 1)	_	400	mW
T _{stg}	storage temperature		-65	150	°C
Tj	junction temperature		_	150	°C

THERMAL RESISTANCE

SYMBOL	PARAMETER	THERMAL RESISTANCE
R _{th j-s}	from junction to soldering point (note 1)	260 K/W

Note

1. T_s is the temperature at the soldering point of the collector tab.

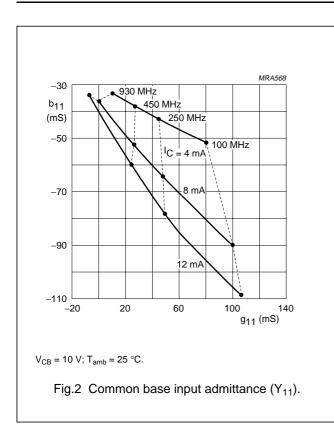
CHARACTERISTICS

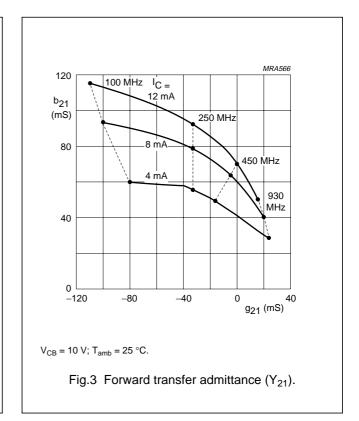
T_j = 25 °C

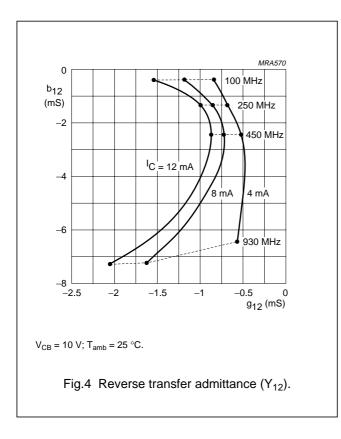
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{(BR)CBO}	collector-base breakdown voltage	open emitter; $I_C = 10 \mu A$; $I_E = 0$	20	_	V
V _{(BR)CEO}	collector-emitter breakdown voltage	open base; $I_C = 1 \text{ mA}$; $I_B = 0$	20	_	V
V _{(BR)EBO}	emitter-base breakdown voltage	open collector; $I_E = 10 \mu A$; $I_C = 0$	3	_	V
V _{CE sat}	collector-emitter saturation voltage	$I_C = 5 \text{ mA}; I_B = 0.5 \text{ mA}$	_	0.5	V
V _{BE on}	base-emitter ON voltage	$V_{CE} = 10 \text{ V}; I_{C} = 5 \text{ mA}$	_	0.9	V
I _{CBO}	collector-base cut-off current	V _{CB} = 10 V; I _E = 0	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 2 V; I _C = 0	_	100	nA
h _{FE}	DC current gain	$V_{CE} = 10 \text{ V}; I_{C} = 5 \text{ mA}$	60	-	
C _{ce}	collector-emitter capacitance	V _{CB} = 10 V; I _B = 0; f = 1 MHz	-	0.65	pF
C _{cb}	collector-base capacitance	V _{CB} = 10 V; I _E = 0; f = 1 MHz	-	0.85	pF
f _T	transition frequency	V _{CE} = 10 V; I _C = 5 mA; f = 100 MHz; T _{amb} = 25 °C	600	_	MHz

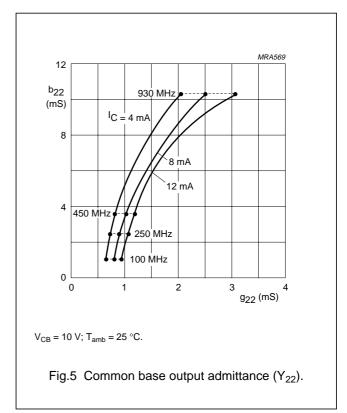
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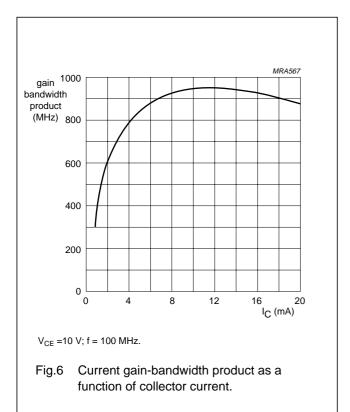






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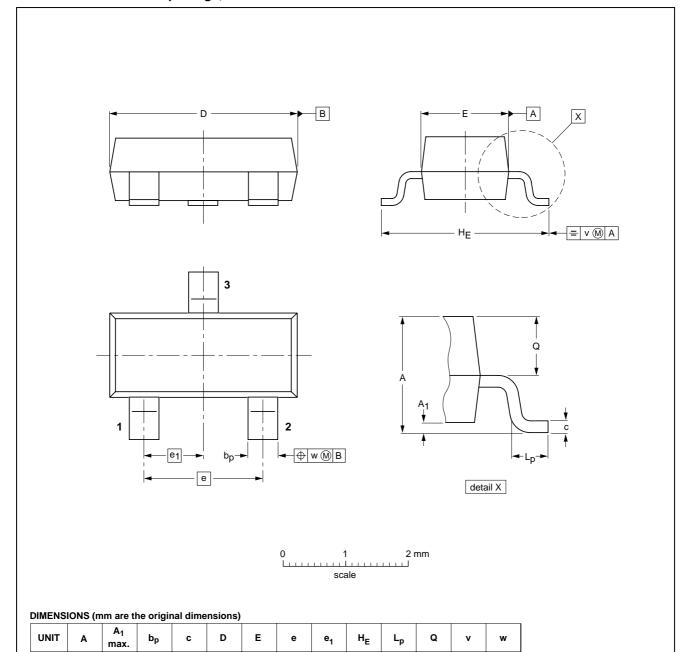
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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE		REFER	RENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT23					97-02-28

0.95

1.9

0.45

0.15

2.5

0.55

0.2

0.1

mm

0.48

0.1

0.15

3.0

1.4 1.2

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification

Application information

Where application information is given, it is advisory and does not form part of the specification.

is not implied. Exposure to limiting values for extended periods may affect device reliability.

LIFE SUPPORT APPLICATIONS

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