



# N-Channel $Q_g$ , Fast Switching MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
30	0.0095 at V <sub>GS</sub> = 10 V	12.5			
30	0.0135 at V <sub>GS</sub> = 4.5 V	10.5			

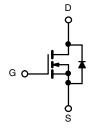
## **FEATURES**

- Halogen-free According to IEC 61249-2-21
- Extremely Low Q<sub>gd</sub> for Switching Losses
  TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>q</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

- High-Side DC/DC Conversion
  - Notebook
  - Server



N-Channel MOSFET

	SO-8		
S 1 S 2 S 3 G 4		8 7 6 5	D D D
'	Top View	•	

Ordering Information: Si4390DY-T1-E3 (Lead (Pb)-free)

Si4390DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Drain Current (T <sub>.I</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	12.5	8.5	A	
Continuous Diain Current (1, = 150 C)	T <sub>A</sub> = 70 °C		10	6.8		
Pulsed Drain Current		I <sub>DM</sub>	20		^	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.7	1.3		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	3.0	1.4	W	
Maximum Fower Dissipation	T <sub>A</sub> = 70 °C		1.9	0.9		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	В	32	42	
Maximum Junction-to-Ambient	Steady State	$R_{thJA}$	68	90	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	15	20	

#### Notes:

a. Surface mounted on 1" x 1" FR4 board.

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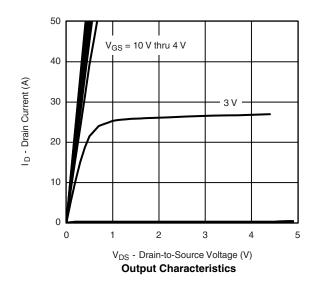
<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25	Symbol	Test Conditions Mir		Тур.	Max.	Unit
Static				.,,,,		<b>-</b>
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8		2.8	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zone Onto Walle on Durin Original		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			5	μΑ
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α
	Ь	$V_{GS} = 10 \text{ V}, I_D = 12.5 \text{ A}$	: 12.5 A		0.0095	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 10.5 \text{ A}$		0.0105	0.0135	Ω
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 12.5 A		38		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 2.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.7	1.1	V
Dynamic <sup>b</sup>						
Total Gate Charge	Qg			10	15	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 12.5 \text{ A}$		3.5		nC
Gate-Drain Charge	$Q_{gd}$			2.1		
Gate Resistance	$R_g$		0.2	0.8	1.4	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			16	30	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		6	12	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 1 A, $V_{GEN}$ = 10 V, $R_g$ = 6 $\Omega$		43	70	ns
Fall Time	t <sub>f</sub>			14	25	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 2.7 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}$		35	60	

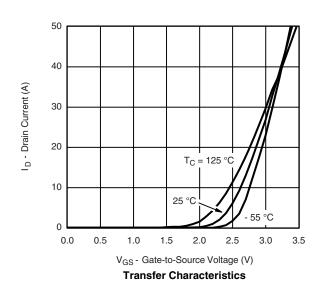
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



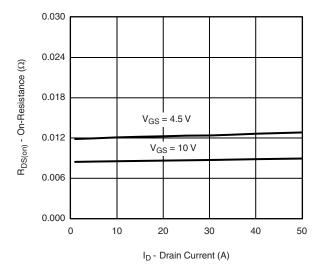


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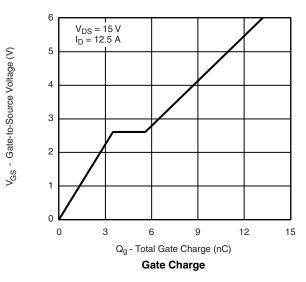


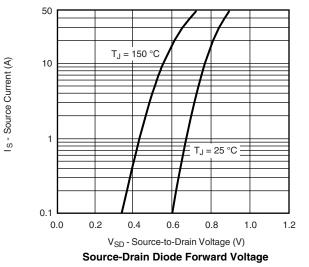


## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Drain Current

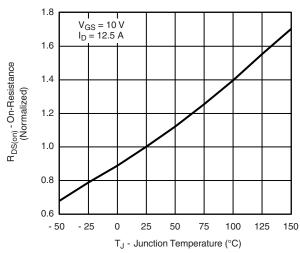




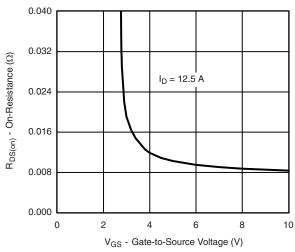
1800  $C_{\text{iss}}$ 1500 C - Capacitance (pF) 1200 900 Coss 600 300  $C_{rss}$ 0 6 12 18 24 30 0

V<sub>DS</sub> - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature

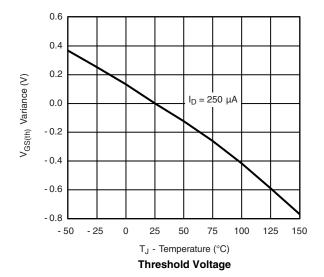


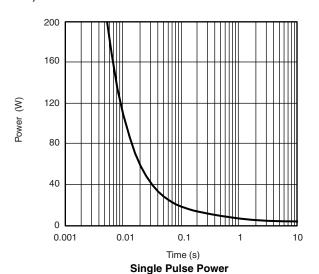
On-Resistance vs. Gate-to-Source Voltage

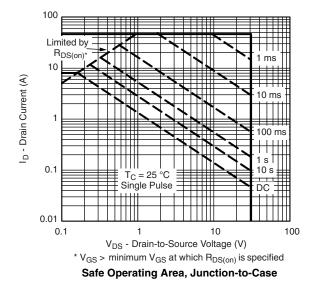
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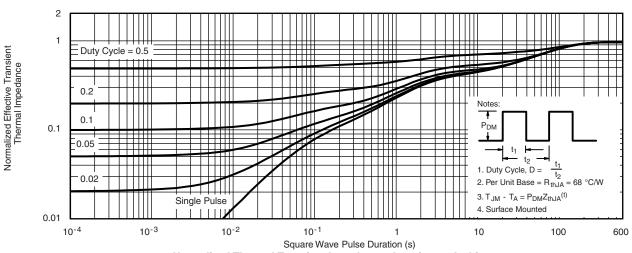
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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



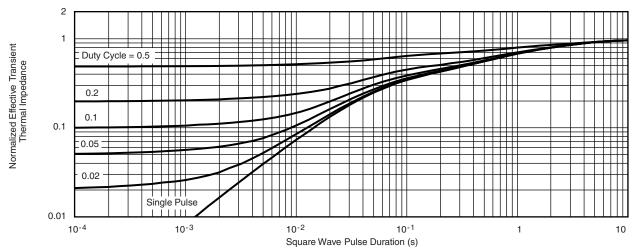








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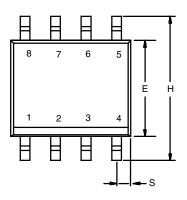


Normalized Thermal Transient Impedance, Junction-to-Foot

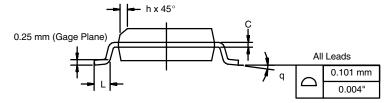
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**SOIC (NARROW): 8-LEAD** JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

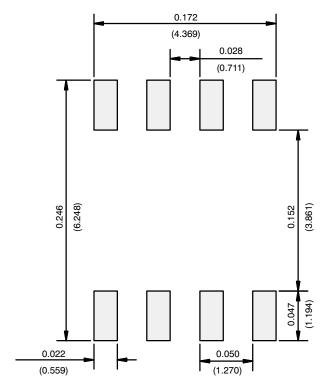
DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06

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### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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