

P-Channel 2.5-V (G-S) MOSFET

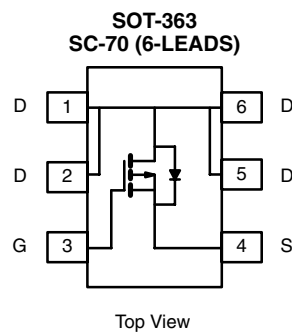
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
- 25	0.180 at $V_{GS} = - 4.5$ V	± 1.5
	0.200 at $V_{GS} = - 3.6$ V	± 1.4
	0.265 at $V_{GS} = - 2.5$ V	± 1.2

FEATURES

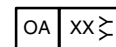
- TrenchFET[®] Power MOSFET



RoHS*
COMPLIANT



Marking Code



Lot Traceability
and Date Code

Part # Code

Ordering Information: Si1403DL-T1
Si1403DL-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted				
Parameter	Symbol	5 Sec	Steady State	Unit
Drain-Source Voltage	V_{DS}	- 20		V
Gate-Source Voltage	V_{GS}	± 12		
Continuous Drain Current ($T_J = 150$ °C)	I_D	$T_A = 25$ °C	± 1.5	± 1.4
		$T_A = 85$ °C	± 1.2	± 1.0
Pulsed Drain Current	I_{DM}	± 5		A
Continuous Diode Current (Diode Conduction) ^a	I_S	- 0.8	- 0.8	
Maximum Power Dissipation ^a	P_D	$T_A = 25$ °C	0.625	0.568
		$T_A = 85$ °C	0.400	0.295
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 5$ sec	R_{thJA}	165	200	°C/W
	Steady State		180	220	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	105	130	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

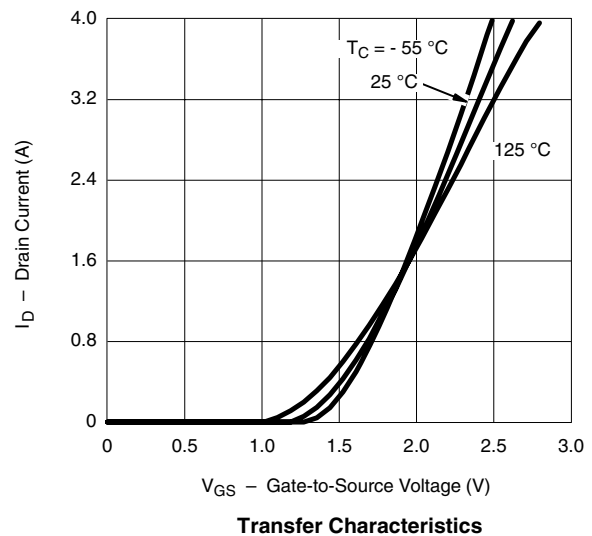
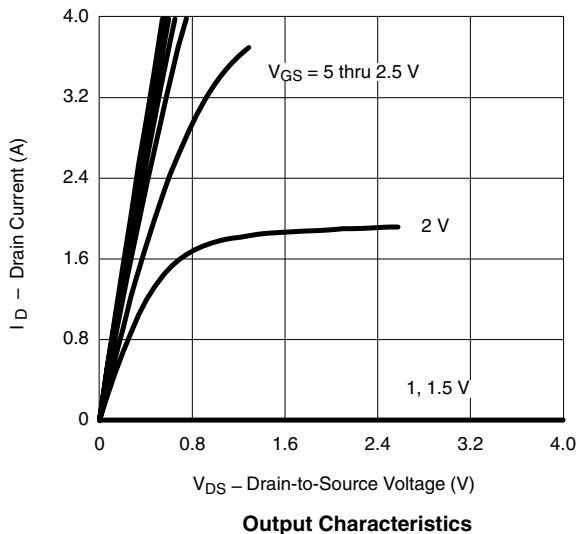
SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.6		-1.5	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}, T_J = 85\text{ }^\circ\text{C}$			-5	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -4.5\text{ V}$	-2			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -1.5\text{ A}$		0.145	0.180	Ω
		$V_{GS} = -3.6\text{ V}, I_D = -1.4\text{ A}$		0.165	0.200	
		$V_{GS} = -2.5\text{ V}, I_D = -0.8\text{ A}$		0.220	0.265	
Forward Transconductance ^a	g_{fs}	$V_{GS} = -10\text{ V}, I_D = -1.5\text{ A}$		3.8		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -0.8, V_{GS} = 0\text{ V}$		-0.78	-1.1	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -10\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -1.5\text{ A}$		3.7	4.5	nC
Gate-Source Charge	Q_{gs}		0.9			
Gate-Drain Charge	Q_{gd}		0.9			
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_L = 10\text{ }\Omega$ $I_D \cong -1\text{ A}, V_{GEN} = -4.5\text{ V}, R_G = 6\text{ }\Omega$		8	12	ns
Rise Time	t_r		25	40		
Turn-Off Delay Time	$t_{d(off)}$		21	32		
Fall Time	t_f		20	30		
Source-Drain Reverse Recovery Time	t_{rr}		$I_F = -0.8\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	20	40	

Notes:

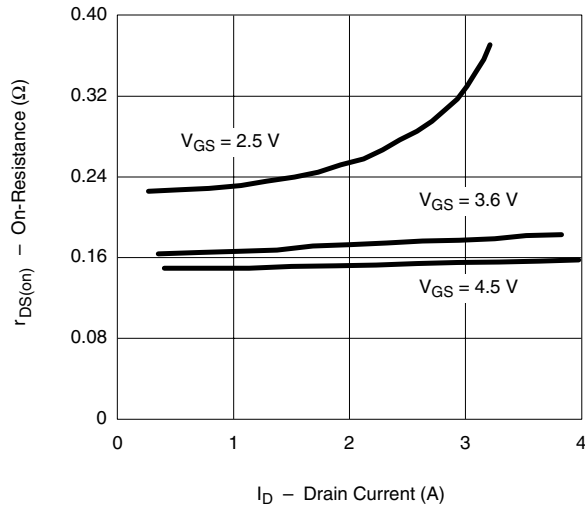
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

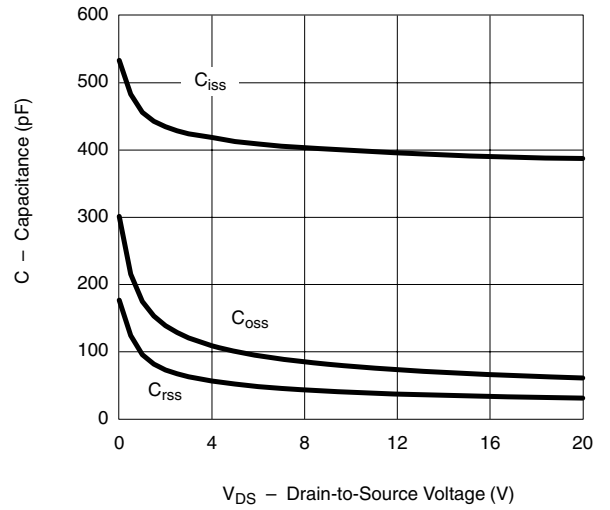
TYPICAL CHARACTERISTICS $T_A = 25\text{ }^\circ\text{C}$, unless noted



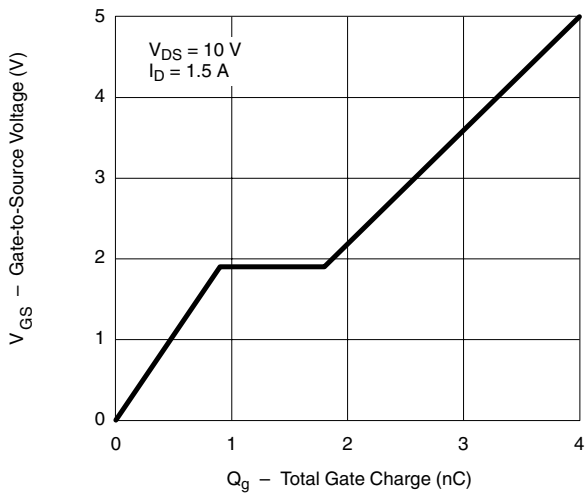
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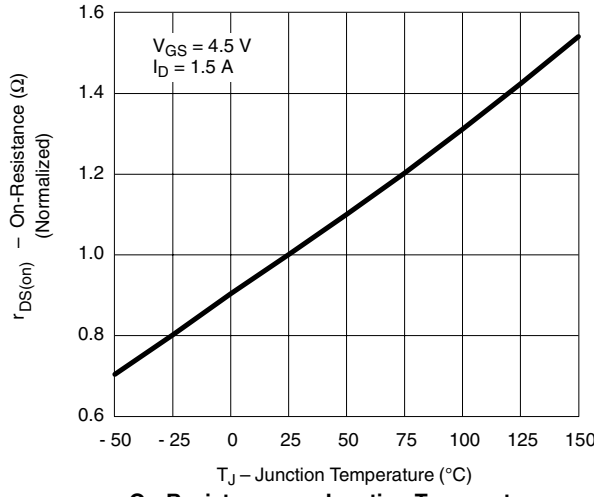
On-Resistance vs. Drain Current



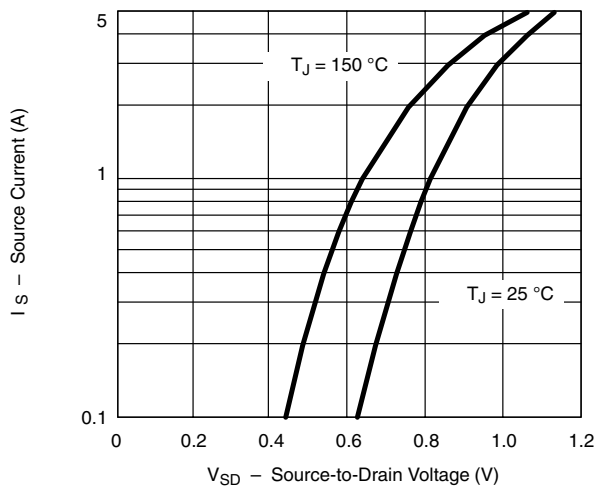
Capacitance



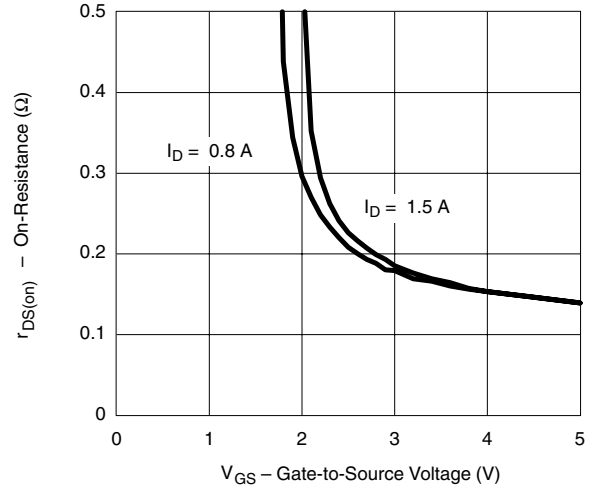
Gate Charge



On-Resistance vs. Junction Temperature



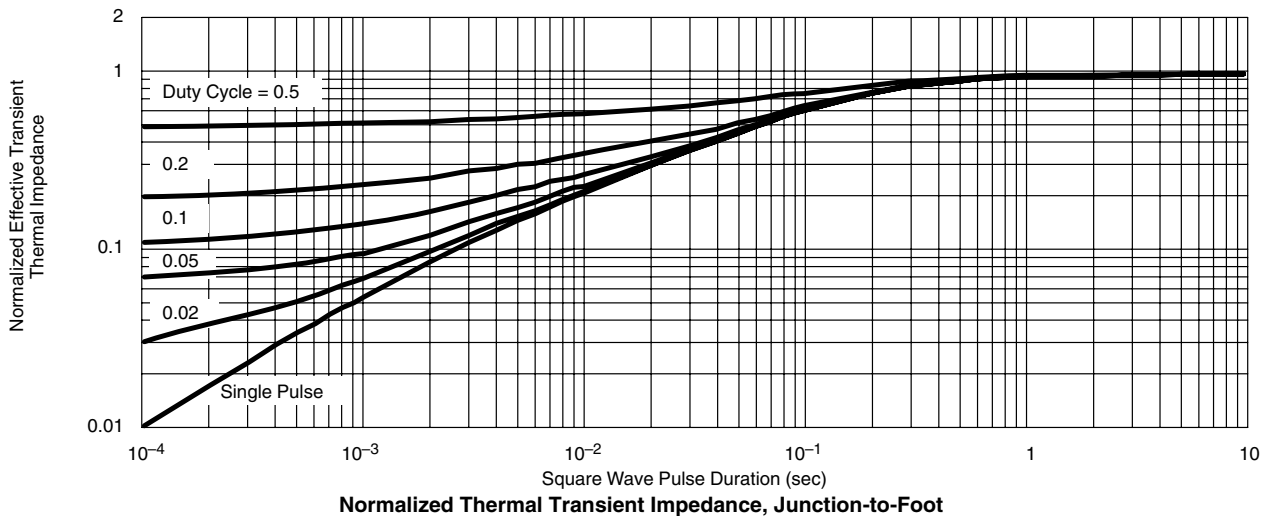
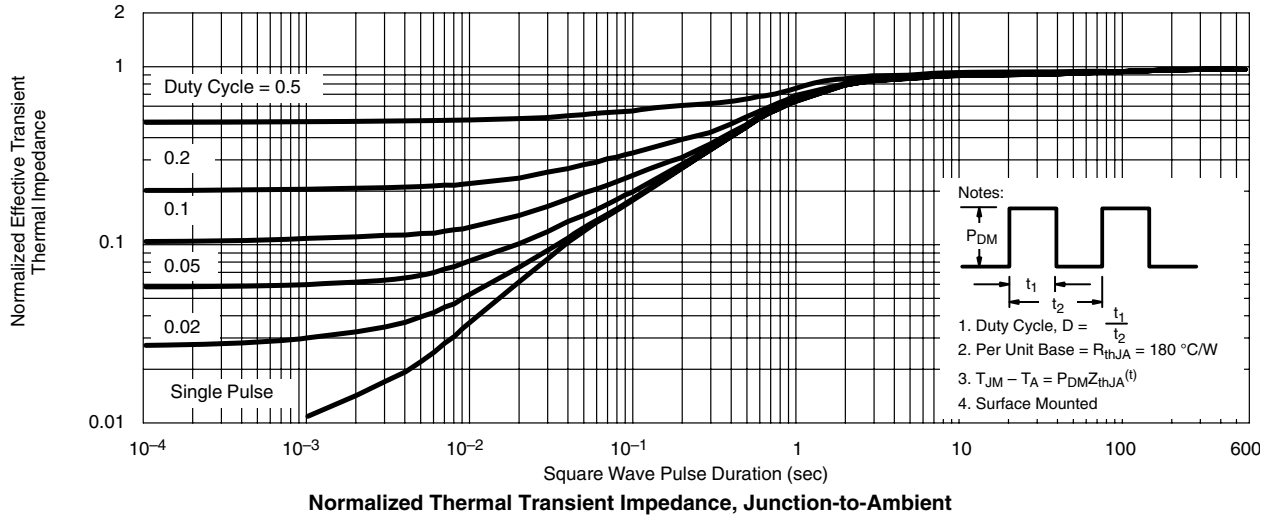
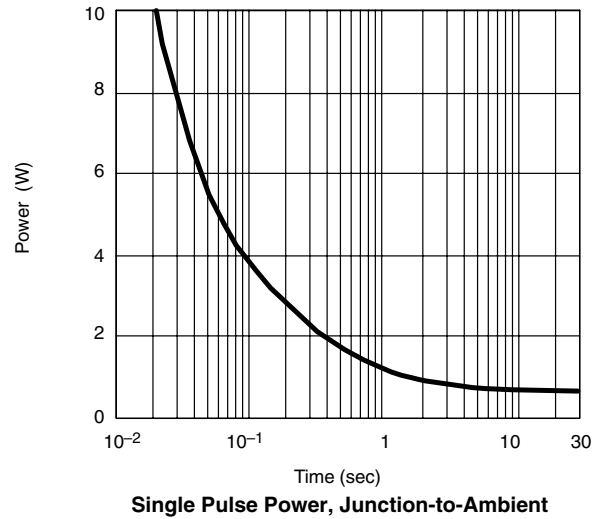
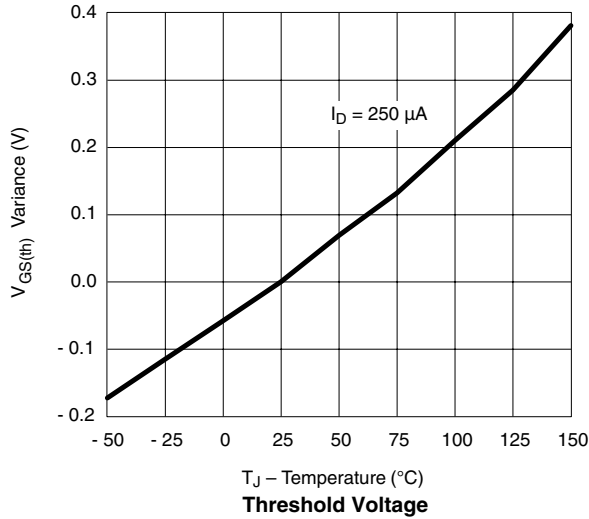
Capacitance



On-Resistance vs. Gate-to-Source Voltage



TYPICAL CHARACTERISTICS $T_A = 25\text{ }^\circ\text{C}$, unless noted



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