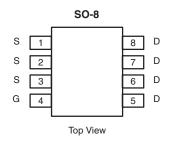




## N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$ $I_{D}(A)^{a}$		Q <sub>g</sub> (Typ.)		
30	0.0032 at V <sub>GS</sub> = 10 V	36	25.5 nC		
	0.0042 at V <sub>GS</sub> = 4.5 V	29	25.5 110		



Ordering Information: Si4324DY-T1-E3 (Lead (Pb)-free) Si4324DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

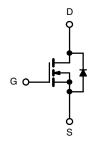
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested

# ROHS COMPLIANT HALOGEN FREE Available

#### **APPLICATIONS**

- Synchronous Buck-Low Side
  - Notebook
  - Server
  - Workstation
- Synchronous Rectifier-POL



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25  ^{\circ}C$ , Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	v	
	T <sub>C</sub> = 25 °C		36		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	29		
Continuous Diain Current (1) = 150 C)	T <sub>A</sub> = 25 °C	1 'b	24 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		19 <sup>b, c</sup>	Α	
Pulsed Drain Current		I <sub>DM</sub>	70		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I.	7.0		
Continuous Source-Diam Diode Current	T <sub>A</sub> = 25 °C	ls –	3.0 <sup>b, c</sup>		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	40		
Avalanche Energy		E <sub>AS</sub>	80	mJ	
	T <sub>C</sub> = 25 °C		7.8		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	5.0	w	
	T <sub>A</sub> = 25 °C	] 'b [	3.5 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C		2.2 <sup>b, c</sup>		
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	29	35	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	13	16	] 0/**	

#### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 80 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, I}_{D} = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		34		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I <sub>D</sub> = 250 μA		- 6.4			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1.4		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μА	
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		0.0025	0.0032	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$		0.0034	0.0042		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$		80		S	
Dynamic <sup>b</sup>							
Input Capacitance	c <sub>iss</sub>			3510		pF	
Output Capacitance	c <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		795			
Reverse Transfer Capacitance	c <sub>rss</sub>			265			
Total Cata Charge	Q <sub>g</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		55.5	85	nC	
Total Gate Charge				25.5	40		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		11.6			
Gate-Drain Charge	$Q_{gd}$			6.6			
Gate Resistance	$R_g$	f = 1 MHz	0.6	1.25	1.9	Ω	
Turn-on Delay Time	t <sub>d(on)</sub>			30	45	ne	
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, R_1 = 1.5 \Omega$		185	280		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_q = 1 \Omega$		30	45		
Fall Time	t <sub>f</sub>	B - AGEN - Ag		13	20		
Turn-on Delay Time	t <sub>d(on)</sub>			17	26	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, R_{L} = 1.5 \Omega$		90	140		
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{DD} = 13 \text{ V}, R_L = 1.3 \Omega$ $I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_q = 1 \Omega$		37	56		
Fall Time	t <sub>f</sub>	D = 1-1-5 - GEN - 10 - 15 - 15		10	16		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			7	А	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				70		
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 3 A		0.72	1.1	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			40	60	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 13 A, dl/dt = 100 A/μs, T <sub>.I</sub> = 25 °C		40	60	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	i <sub>F</sub> = 10 Λ, αί/αι = 100 Α/μ5, 1 <sub>J</sub> = 25 °C		21		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			19			

#### Notes:

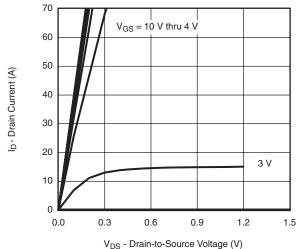
- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

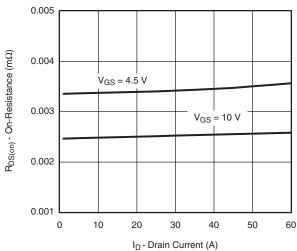




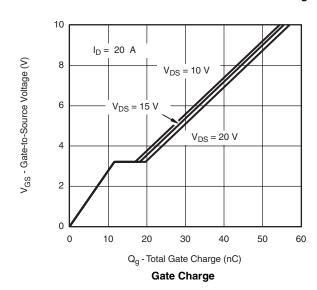
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

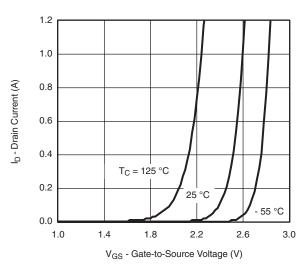


Output Characteristics

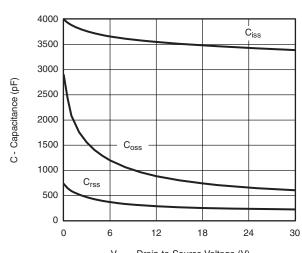


On-Resistance vs. Drain Current and Gate Voltage

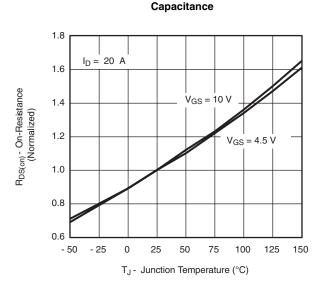




Transfer Characteristics



 $V_{\mbox{\footnotesize DS}}$  - Drain-to-Source Voltage (V)

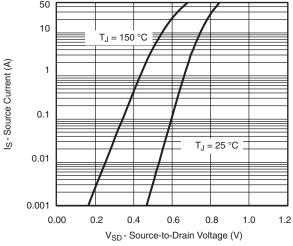


On-Resistance vs. Junction Temperature

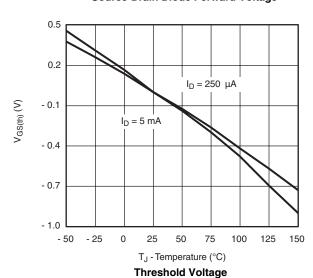
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

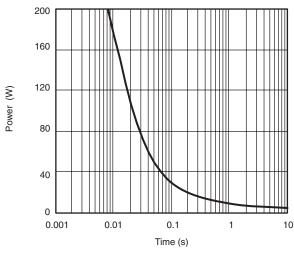


#### Source-Drain Diode Forward Voltage

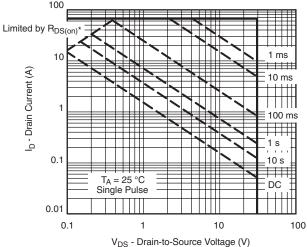


 $C_{OOO} = 0.015$   $C_{OOO} = 0.0012$   $C_{OOO} = 0.000$   $C_{OOO} =$ 

On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

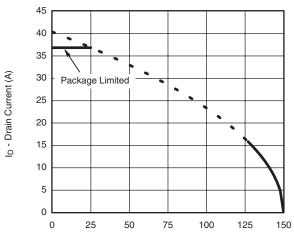


\* V<sub>DS</sub> - Drain-to-Source voltage (V) \* V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

Safe Operating Area, Junction-to-Ambient

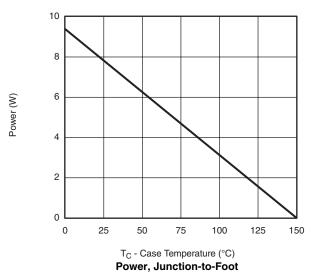


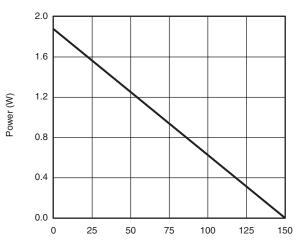
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



#### $T_{\mbox{\scriptsize C}}$ - Case Temperature (°C)

#### Current Derating\*





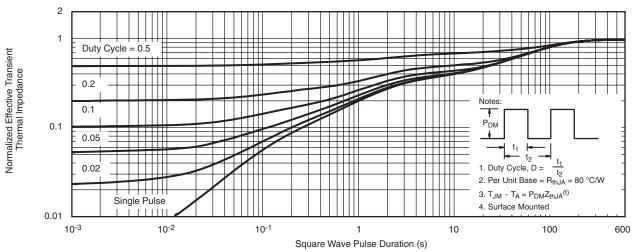
T<sub>A</sub> - Ambient Temperature (°C) **Power, Junction-to-Ambient** 

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

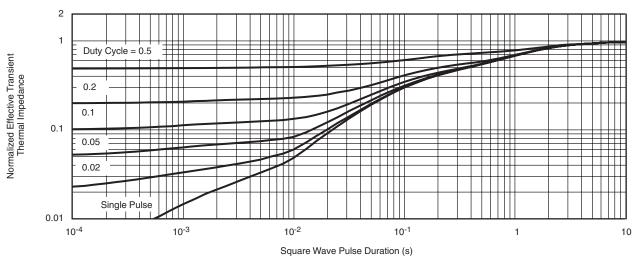
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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