



# P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
- 30	0.012 at V <sub>GS</sub> = - 10 V	- 8.8		
	0.019 at V <sub>GS</sub> = - 4.5 V	- 7.0		

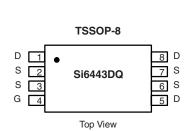
#### **FEATURES**

- · Halogen-free
- TrenchFET® Power MOSFET

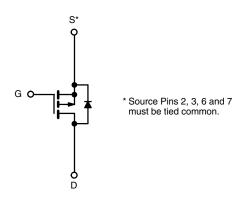


#### **APPLICATIONS**

- Battery Switch
- · Load Switch



Ordering Information: Si6443DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T	$_{A}$ = 25 $^{\circ}$ C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current /T 150 °C\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 8.8	- 7.3	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 7.2	- 5.9	•
Pulsed Drain Current (10 μs Pulse Width)		I <sub>DM</sub>	- 30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.35	- 0.95	
Mariana Barra Birahada	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.50	1.05	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.0	0.67	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manifesture Installed to Analysis and	t ≤ 10 s	- R <sub>thJA</sub>	60	83	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		100	120	
Maximum Junction-to-Foot	Steady State	$R_{thJF}$	35	45	

Notes: a. Surface Mounted on 1" x 1" FR4 board.

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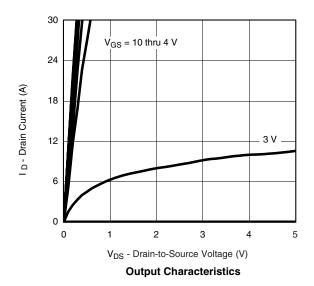
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtaga Dvain Curvent	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V		- 1			
Zero Gate Voltage Drain Current		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> - 5 V, V <sub>GS</sub> = - 10 V	- 20			Α	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 8.8 A		0.0095	0.012	Ω	
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 7.2 A		0.0145	0.019	22	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 8.8 A		30		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.5 A, V <sub>GS</sub> = 0 V		- 0.71	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			38	60		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -8.8 \text{ A}$		9.3		nC	
Gate-Drain Charge	$Q_{gd}$			17.7			
Turn-On Delay Time	t <sub>d(on)</sub>			25	40		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_{L}$ = 15 $\Omega$		21	35		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -1 A$ , $V_{GEN} = -10 V$ , $R_G = 6 \Omega$		115	180	ns	
Fall Time	t <sub>f</sub>			68	110		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.5 A, di/dt = 100 A/μs		65	100		

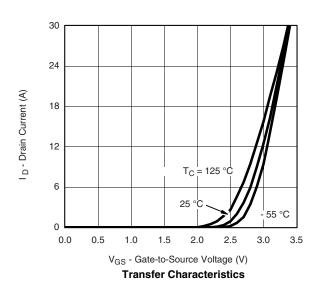
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

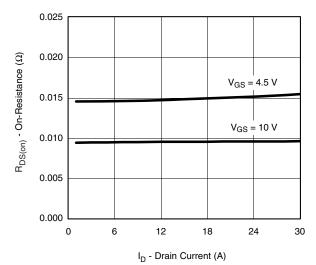




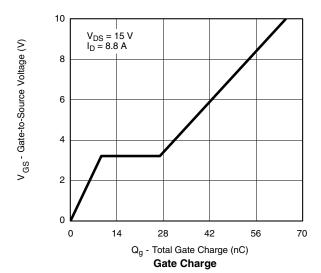


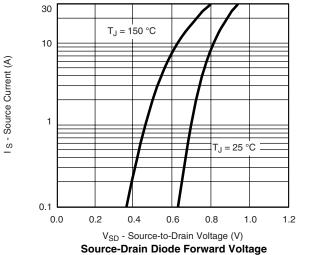


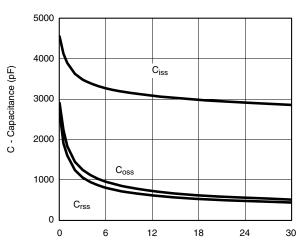
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



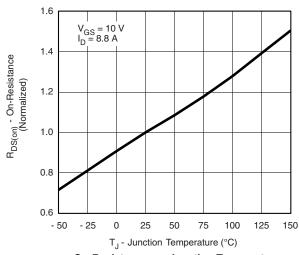
#### On-Resistance vs. Drain Current



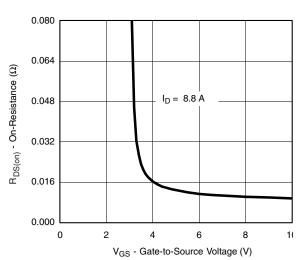




V<sub>DS</sub> - Drain-to-Source Voltage (V) **Capacitance** 



On-Resistance vs. Junction Temperature

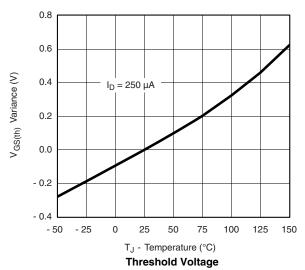


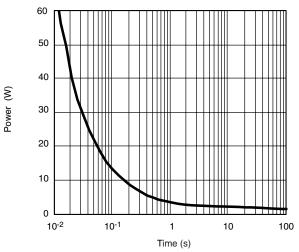
On-Resistance vs. Gate-to-Source Voltage

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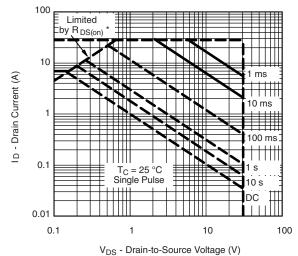
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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



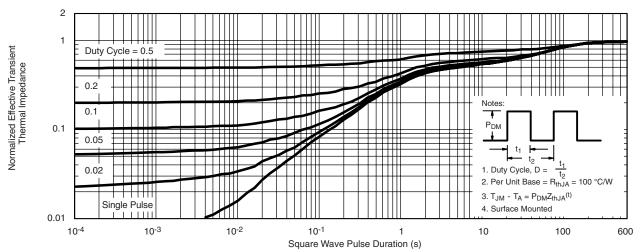


Single Pulse Power, Junction-to-Ambient



\* V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

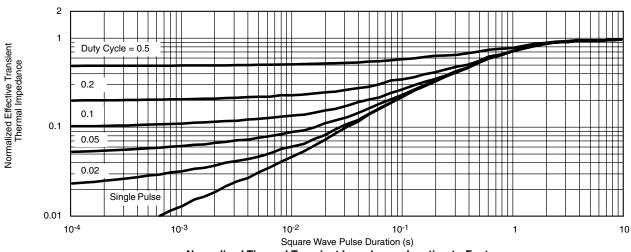
#### Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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